XD6506 Series

ETR03108-001b

Ultra-Low Quiescent Current Voltage Regulator (with Stand-by Function)

■GENERAL DESCRIPTION

The XD6506 series are positive voltage LDO regulators manufactured using CMOS processes. The series achieves Ultra low supply current, 0.8μA (TYP.) and consists of a reference voltage source, an error amplifier, a current fold-back circuit, and a phase compensation circuit plus a driver transistor.

The series is also compatible with low ESR ceramic capacitors, which give added output stability.

The current limiter's fold-back circuit also operates as a short protect for the output current limiter and the output pin. Furthermore, the CE function allows the output of the regulator to be turned off, resulting in greatly reduced power consumption.

■APPLICATIONS

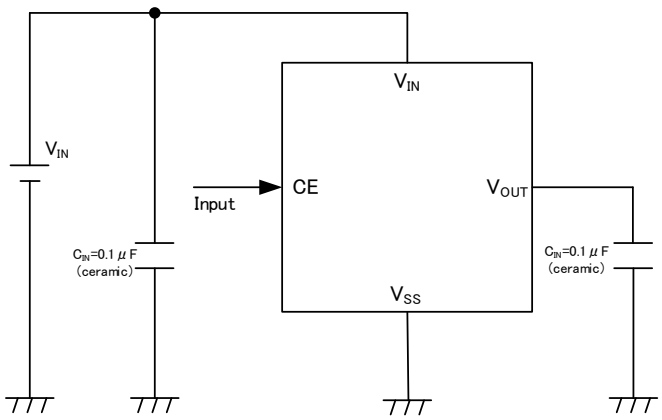
- Car navigation systems
- Car audios
- Automotive ECU
- Other automotive equipment

■FEATURES

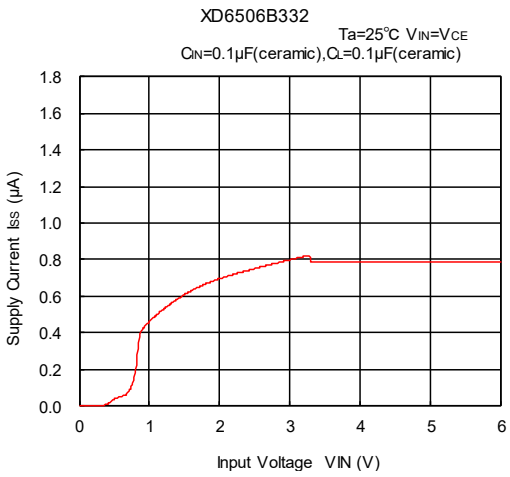
Maximum Output Current	:	150mA
Low Power Consumption	:	0.8μA
Stand-by Current	:	Less than 0.1μA
Dropout Voltage	:	360mV@I_{OUT}=100mA (V_{OUT}=3.3V)
Operating Input Voltage	:	1.5V ~ 6.0V
Output Voltage Range	:	1.2V, 1.5V, 1.8V, 2.5V, 2.8V, 3.0V, 3.3V, 5.0V (*1)
Output Accuracy	:	±2.0%(1.5V＜V_{OUT}≦5.0V) ±30mV(1.2≦V_{OUT}≦1.5V)
Protection function	:	Current limit
Low ESR Capacitor Compatible	:	Ceramic Capacitor Compatible
Operating Temperature Range	:	-40℃ ~ 105℃
Packages	:	SOT-25
Environmentally Friendly	:	EU RoHS Compliant, Pb Free

(*1) Output voltages can be set internally from 1.2V to 5.0V (Step 0.1V).
For other voltages, please contact your local Torex sales office or distribution.

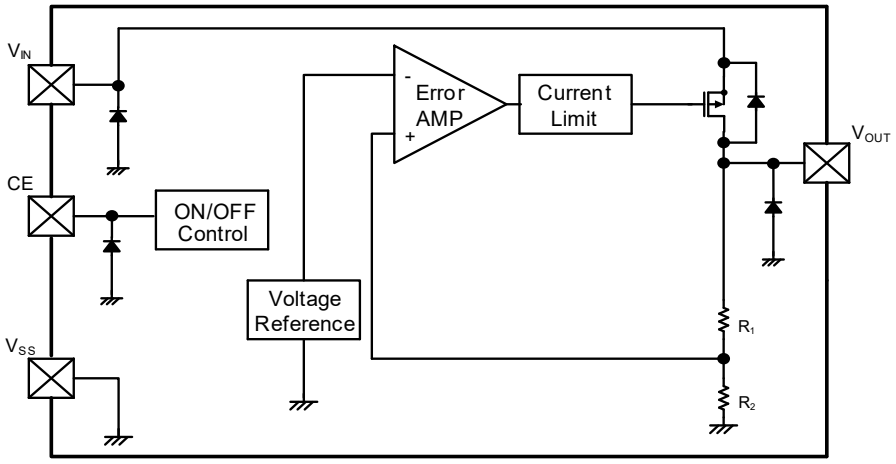
■ TYPICAL APPLICATION CIRCUIT



■TYPICAL PERFORMANCE CHARACTERISTICS



■ BLOCK DIAGRAMS



* Diodes shown in the above circuit are ESD protection diodes and parasitic diodes

■PRODUCT CLASSIFICATION

●Ordering Information

XD6506 ①②③④⑤⑥-⑦(*1)

DESIGNATOR	ITEM	SYMBOL	DESCRIPTION
①	TYPE	B	Refer to Selection Guide
②③	Output Voltage	12, 15, 18, 25, 28, 30, 33, 50(*2)	e.g. 3.3V ⇒ 33, 5.0V ⇒ 50
④	Output Voltage Accuracy	2	±2% (V_{OUT}≧1.5V) ±30mV (V_{OUT}＜1.5V)
⑤⑥-⑦(*1)	Packages (Order Unit)	MR-Q	SOT-25 (3,000pcs/Reel)

(*1) The "-Q" suffix denotes "AEC-Q100" and "Halogen and Antimony free" as well as being fully EU RoHS compliant.

(*2) Output voltages can be set internally from 1.2V to 5.0V (Step 0.1V). For other voltages, please contact your local Torex sales office or distribution.

●Selection Guide

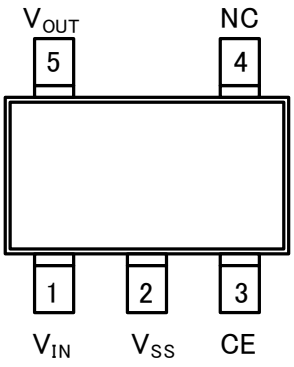
TYPE	CE function
B	Yes

■STANDARD VOLTAGE

●Examples for standard voltage

V_{OUT} (V)	PACKAGES
	SOT-25
1.2	XD6506B122MR-Q
1.5	XD6506B152MR-Q
1.8	XD6506B182MR-Q
2.5	XD6506B252MR-Q
2.8	XD6506B282MR-Q
3.0	XD6506B302MR-Q
3.3	XD6506B332MR-Q
5.0	XD6506B502MR-Q

■PIN CONFIGURATION



SOT-25
(TOP VIEW)

■PIN ASSIGNMENT

PIN NUMBER SOT-25	PIN NAME	FUNCTION
1	V_{IN}	Power Supply Input
2	V_{SS}	Ground
3	CE	ON/OFF Control
4	NC	No Connection
5	V_{OUT}	Output

■FUNCTION

PIN NAME	DESIGNATOR	IC OPERATION
CE	L	Stand-by
	H	Active
	OPEN	Undefined state *

* Please do not leave the CE pin open.

■ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNITS
V_{IN} Pin Voltage		V_{IN}	-0.3 ~ 7.0	V
Output Voltage		V_{OUT}	-0.3 ~ V_{IN} + 0.3 or 7.0 (*1)	V
CE Input Voltage		V_{CE}	-0.3 ~ 7.0	V
Power Dissipation (Ta=25℃)	SOT-25	Pd	250 (IC only)	mW
			600 (40mm x 40mm Standard board)(*2)	
			760 (JESD51-7 board) (*2)	
Operating Ambient Temperature		Topr	-40 ~ 105	℃
Storage Temperature		Tstg	-55 ~ 125	℃

All voltages are described based on the V_{SS}.

(*1) The maximum rating corresponds to the lowest value between V_{IN}+0.3V or 7.0V.

(*2) The power dissipation figure shown is PCB mounted and is for reference only.
Please refer to PACKAGING INFORMATION for the mounting condition.

■ELECTRICAL CHARACTERISTICS

Ta=25 ℃

PARAMETER	SYMBOL	CONDITIONS		MIN.	TYP.	MAX.	UNITS	CIRCUIT
Output Voltage	$V_{OUT(E)}$(*2)	I_{OUT}=1mA	-40℃≦Ta≦105℃(*4)	E-0			V	①
Maximum Output Current	I_{OUTMAX}	$V_{OUT(T)}$=1.2V~2.4V V_{IN}=V_{CE}=$V_{OUT(T)}$+2.0V		150	-	-	mA	①
		$V_{OUT(T)}$≧2.5V V_{IN}=V_{CE}=$V_{OUT(T)}$+1.0V						
Load Regulation	$\triangle V_{OUT}$	$V_{OUT(T)}$= 1.2V~1.3V 1mA≦I_{OUT}≦80mA		-	15	70	mV	①
		$V_{OUT(T)}$≧1.4V 1mA≦I_{OUT}≦100mA						
Dropout Voltage	V_{dif}(*3)	$V_{OUT(T)}$=1.2V~1.3V V_{CE}=V_{IN} , I_{OUT} =80mA		E-1			mV	①
		$V_{OUT(T)}$≧1.4V V_{CE}=V_{IN} , I_{OUT} =100mA						
Supply Current	I_{SS}	$V_{OUT(T)}$≦3.9V		-	0.8	1.5	μA	②
			-40℃≦Ta≦105℃(*4)	-	-	1.8		
		$V_{OUT(T)}$≧4.0V		-	1	2.1		
			-40℃≦Ta≦105℃(*4)	-	-	2.5		
Stand-by Current	I_{STB}	V_{IN}=6.0V , V_{CE}=V_{SS}		-	0.01	0.1	μA	②
			-40℃≦Ta≦105℃(*4)	-	-	0.6		
Line Regulation	ΔV_{OUT}/ (ΔV_{IN}・V_{OUT})	$V_{OUT(T)}$=1.2V , V_{CE}=V_{IN} $V_{OUT(T)}$+0.5V≦V_{IN}≦6.0V I_{OUT}=1mA		-	0.05	0.15	%/V	①
		$V_{OUT(T)}$≧1.3V , V_{CE}=V_{IN} $V_{OUT(T)}$+0.5V≦V_{IN}≦6.0V I_{OUT}=30mA		-	0.05	0.15	%/V	①
Input Voltage	V_{IN}		-40℃≦Ta≦105℃(*4)	1.5	-	6.0	V	−
Current Limit	I_{LIM}	$V_{OUT(T)}$=1.2V~2.4V V_{OUT}=$V_{OUT(E)}$×0.95 V_{IN}=V_{CE}= $V_{OUT(T)}$+2.0V		150	260	-	mA	①
		$V_{OUT(T)}$≧2.5V V_{OUT}=$V_{OUT(E)}$×0.95 V_{IN}=V_{CE}= $V_{OUT(T)}$+1.0V		150	260	-		
Short Current	I_{SHORT}	V_{OUT}= V_{ss}		-	30	-	mA	①
CE "H" Level Voltage	V_{CEH}	V_{IN}=$V_{OUT(T)}$+1.0V	-40℃≦Ta≦105℃(*4)	1.0	-	6.0	V	③
CE "L" Level Voltage	V_{CEL}	V_{IN}=$V_{OUT(T)}$+1.0V	-40℃≦Ta≦105℃(*4)	-	-	0.3	V	③
CE "H" Level Current	I_{CEH}	V_{IN}=V_{CE}=6.0V	-40℃≦Ta≦105℃(*4)	-0.1	-	0.1	μA	②
CE "L" Level Current	I_{CEL}	V_{IN}=6.0V V_{CE}= V_{SS}	-40℃≦Ta≦105℃(*4)	-0.1	-	0.1	μA	②

Unless otherwise stated, V_{IN}=V_{CE}=V_{OUT}+1.0V,

NOTE:

(*1) $V_{OUT(T)}$: Fixed output voltage

(*2) $V_{OUT(E)}$ = Effective output voltage

(i.e. the output voltage when "$V_{OUT(T)}$ + 1.0V" is provided at the V_{IN} pin while maintaining a certain I_{OUT} value).

(*3) V_{dif}={V_{IN1}−V_{OUT1}}

V_{OUT1} is the voltage equal to 98% output voltage when an amply stabilized $V_{OUT(T)}$ +1.0V are supplied to the V_{IN} pin.

V_{IN1} is the input voltage when V_{OUT1} appears at the V_{OUT} pin while input voltage is gradually decreased.

(*4)The ambient temperature range (-40℃≦Ta≦105℃) is a design Value.

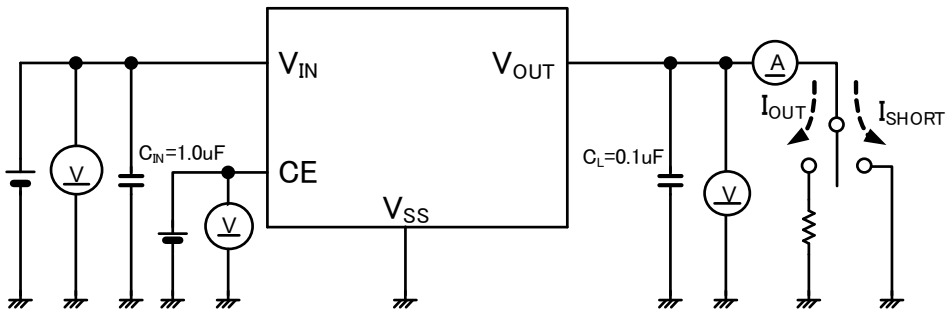
■VOLTAGE CHART

●Voltage Chart 1

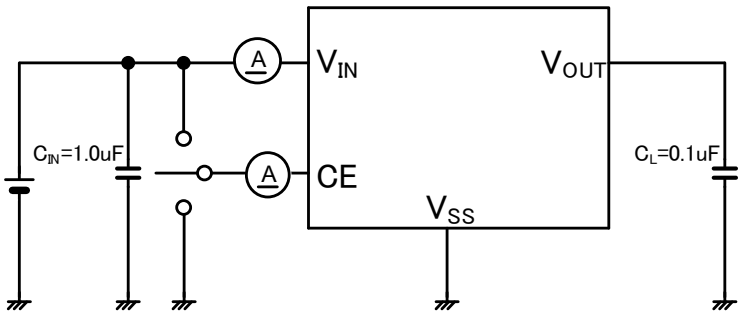
NOMINAL OUTPUT VOLTAGE(V)	E-0				E-1	
	Output Voltage				Dropout Voltage	
	Ta=25℃		-40℃≦Ta≦105℃		Ta=25℃	
$V_{OUT(T)}$	$V_{OUT(E)}$ (V)		$V_{OUT(E)}$ (V)		Vdif (mV)	
	MIN.	MAX.	MIN.	MAX.	TYP.	MAX.
1.2	1.170	1.230	1.125	1.275	910	1130
1.3	1.270	1.330	1.225	1.375		
1.4	1.370	1.430	1.325	1.475		
1.5	1.470	1.530	1.425	1.575	800	1010
1.6	1.568	1.632	1.520	1.680		
1.7	1.666	1.734	1.615	1.785		
1.8	1.764	1.836	1.710	1.890	710	910
1.9	1.862	1.938	1.805	1.995		
2.0	1.960	2.040	1.900	2.100		
2.1	2.058	2.142	1.995	2.205		
2.2	2.156	2.244	2.090	2.310		
2.3	2.254	2.346	2.185	2.415		
2.4	2.352	2.448	2.280	2.520		
2.5	2.450	2.550	2.375	2.625	510	660
2.6	2.548	2.652	2.470	2.730		
2.7	2.646	2.754	2.565	2.835		
2.8	2.744	2.856	2.660	2.940		
2.9	2.842	2.958	2.755	3.045		
3.0	2.940	3.060	2.850	3.150	360	480
3.1	3.038	3.162	2.945	3.255		
3.2	3.136	3.264	3.040	3.360		
3.3	3.234	3.366	3.135	3.465		
3.4	3.332	3.468	3.230	3.570		
3.5	3.430	3.570	3.325	3.675		
3.6	3.528	3.672	3.420	3.780		
3.7	3.626	3.774	3.515	3.885		
3.8	3.724	3.876	3.610	3.990		
3.9	3.822	3.978	3.705	4.095		
4.0	3.920	4.080	3.800	4.200		
4.1	4.018	4.182	3.895	4.305		
4.2	4.116	4.284	3.990	4.410		
4.3	4.214	4.386	4.085	4.515		
4.4	4.312	4.488	4.180	4.620		
4.5	4.410	4.590	4.275	4.725		
4.6	4.508	4.692	4.370	4.830		
4.7	4.606	4.794	4.465	4.935		
4.8	4.704	4.896	4.560	5.040		
4.9	4.802	4.998	4.655	5.145		
5.0	4.900	5.100	4.750	5.250	260	350

■TEST CIRCUITS

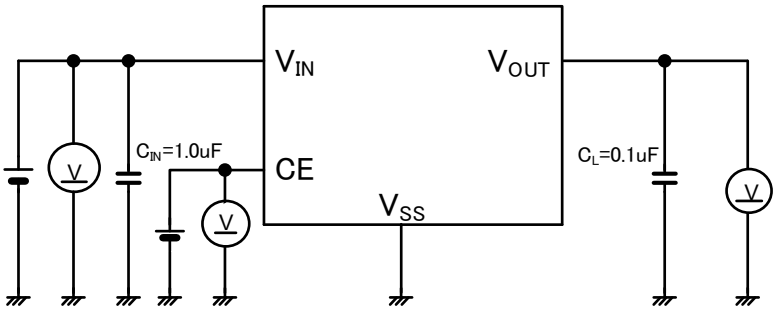
●CIRCUIT①



●CIRCUIT②

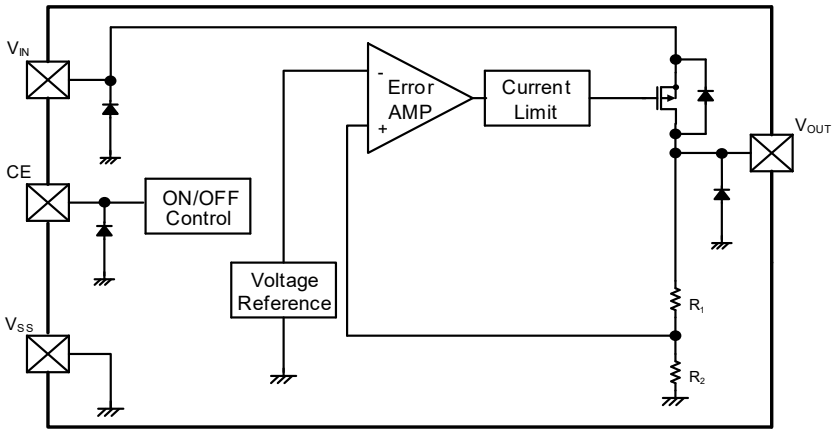


●CIRCUIT③



■ OPERATIONAL EXPLANATION

The voltage divided by resistors R_1 & R_2 is compared with the internal voltage reference by the error amplifier. The P-channel MOSFET, which is connected to the V_{OUT} pin, is then driven by the subsequent output signal. The output voltage at the V_{OUT} pin is controlled and stabilized by a system of negative feedback. The current limit operates in relation to the level of output current. Further, the IC's internal circuitry can be operated or shutdown via the CE pin's signal.



* Diodes shown in the above circuit are ESD protection diodes and parasitic diodes

＜Low ESR Capacitors＞

The XD6506 series needs an output capacitor (C_L) for phase compensation. In order to ensure the stable phase compensation, please place an output capacitor (C_L) of 0.1μF or bigger at the V_{OUT} pin and V_{SS} pin as close as possible. For a stable power input, please connect an input capacitor (C_{IN}) of 0.1μF between the input pin (V_{IN}) and the ground pin (V_{SS}). Since Input capacitor (C_{IN}), the output capacitor (C_L) are bias dependence of the capacitor the influence of the missing capacity due to temperature characteristics, also there is a risk that cannot be stable phase compensation. Please pay attention to the selection of the capacitor to be used.

＜Current Limit＞

The XD6506 series includes a current fold-back circuit as a current limit protection. When the load current reaches the current limit level, the current fold-back circuit operates and output voltage drops. The output voltage drops further and output current decreases. When the output pin is shorted, a current of about 30mA flows.

＜CE Pin＞

The IC's internal circuitry can be active or stand-by via the signal from the CE pin. In stand-by mode, output at the V_{OUT} pin will be pulled down to the V_{SS} level via R_1 & R_2. We suggest that you use this IC with either a V_{IN} voltage or a V_{SS} voltage input at the CE pin. If this IC is used with the correct specifications for the CE pin, the operational logic is fixed and the IC will operate normally. However, supply current may increase as a result of through current in the IC's internal circuitry.

■ NOTES ON USE

1. For temporary, transitional voltage drop or voltage rising phenomenon, the IC is liable to malfunction should the ratings be exceeded.

2. Where wiring impedance is high, operations may become unstable due to noise and/or phase lag depending on output current. Please strengthen V_{IN} and V_{SS} wiring in particular. The input capacitor (C_{IN}) and the output capacitor (C_L) should be placed to the IC as close as possible with a shorter wiring.

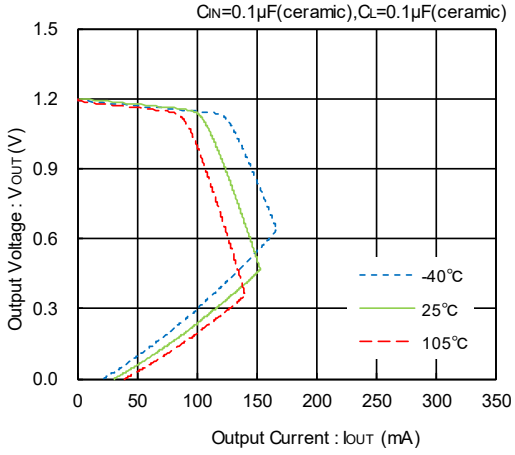
3. In order to stabilize the VIN's voltage level, we recommend that an input capacitor (C_{IN}) of about 0.1 to 1.0μF be connected between the V_{IN} pin and the V_{SS} pin. Moreover, during transient response, so as to prevent an undershoot or overshoot, we recommend that the output capacitor (C_L) of about 0.1 to1.0μF be connected between the V_{OUT} pin and the V_{SS} pin.

4. Torex places an importance on improving our products and its reliability. However, by any possibility, we would request user fail-safe design and post-aging treatment on system or equipment.

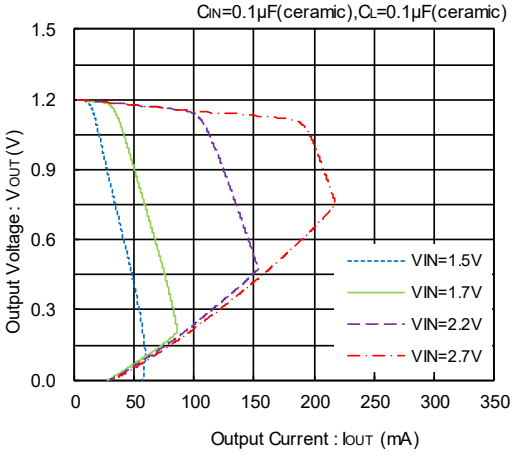
■TYPICAL PERFORMANCE CHARACTERISTICS

(1) Output Voltage vs. Output Current

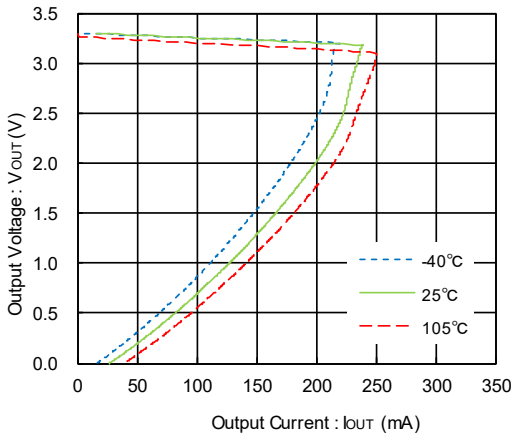
XD6506x122
$V_{IN}=V_{CE}=2.2V$
$C_{IN}=0.1\mu F$(ceramic),$C_L=0.1\mu F$(ceramic)



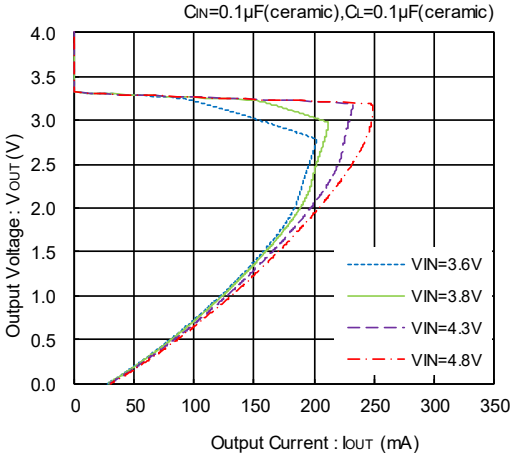
XD6506x122
Ta=25℃
$C_{IN}=0.1\mu F$(ceramic),$C_L=0.1\mu F$(ceramic)



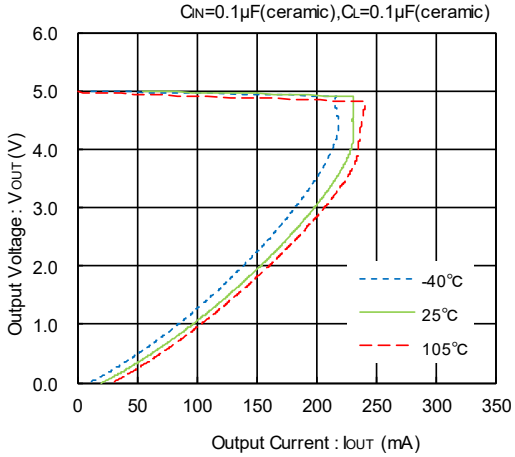
XD6506x332
$V_{IN}=V_{CE}=4.3V$
$C_{IN}=0.1\mu F$(ceramic),$C_L=0.1\mu F$(ceramic)



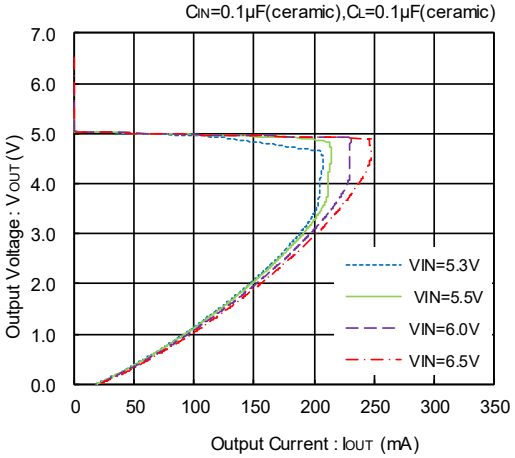
XD6506x332
Ta=25℃
$C_{IN}=0.1\mu F$(ceramic),$C_L=0.1\mu F$(ceramic)



XD6506x502
$V_{IN}=V_{CE}=6.0V$
$C_{IN}=0.1\mu F$(ceramic),$C_L=0.1\mu F$(ceramic)



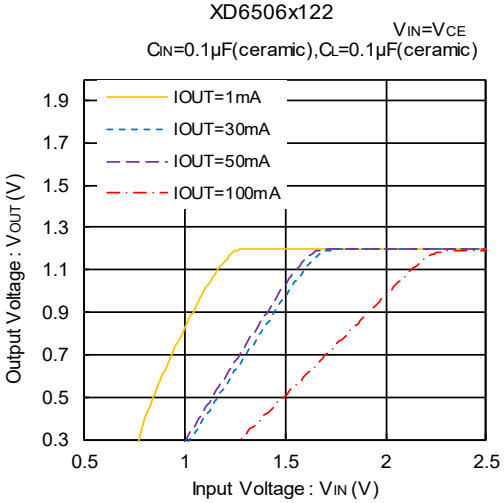
XD6506x502
Ta=25℃
$C_{IN}=0.1\mu F$(ceramic),$C_L=0.1\mu F$(ceramic)



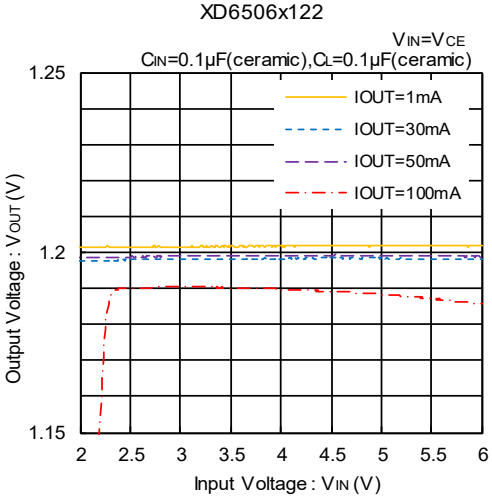
■ TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

(2) Output Voltage vs. Input Voltage

XD6506x122



XD6506x122



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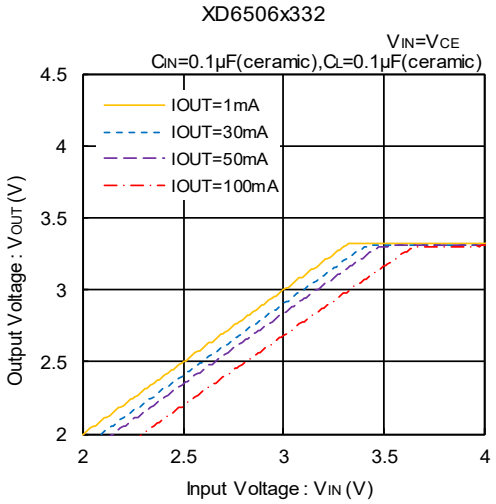
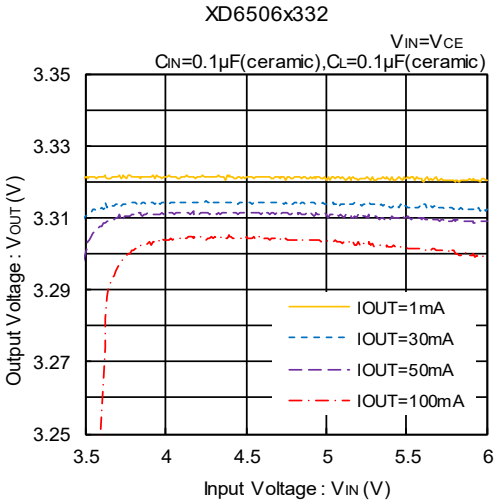
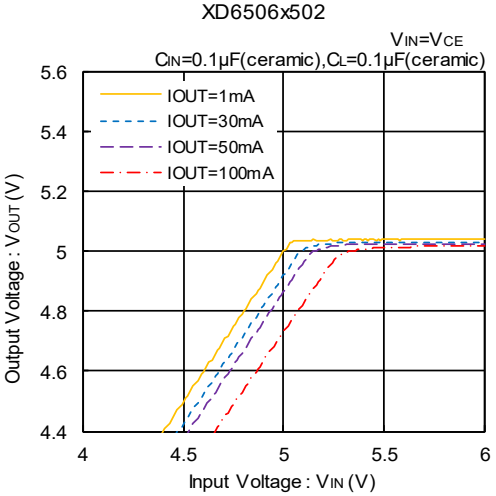


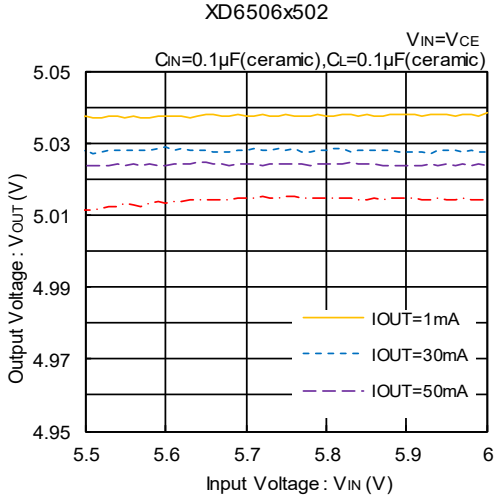
XD6506x332



XD6506x502

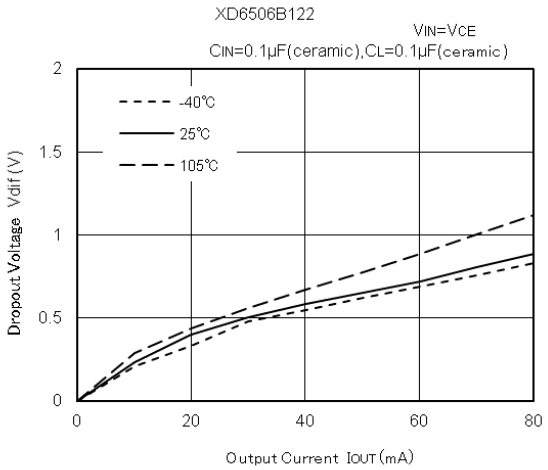


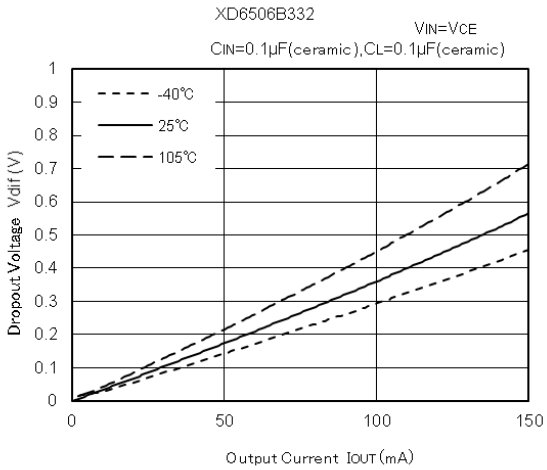
XD6506x502

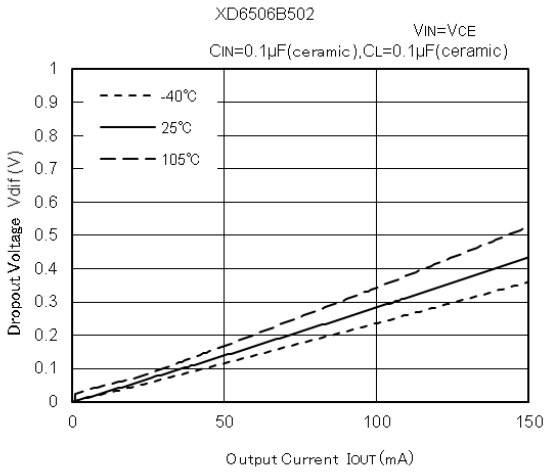


■ TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

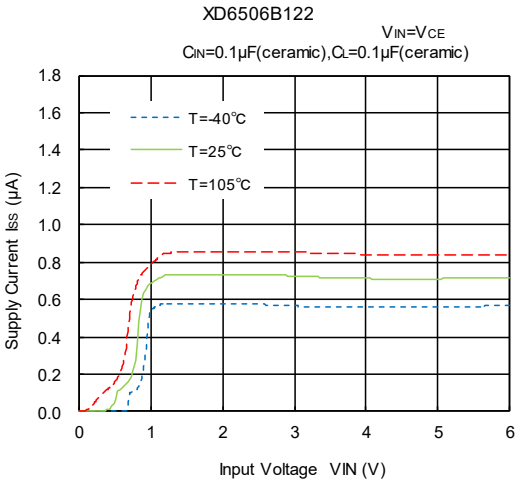
(3) Dropout Voltage vs. Output Current

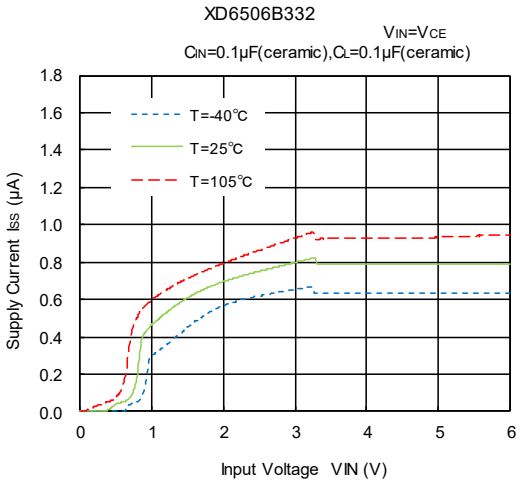






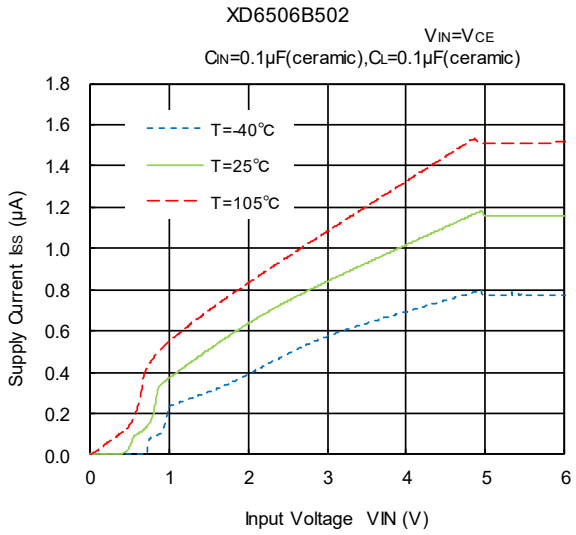
(4) Supply Current vs. Input Voltage



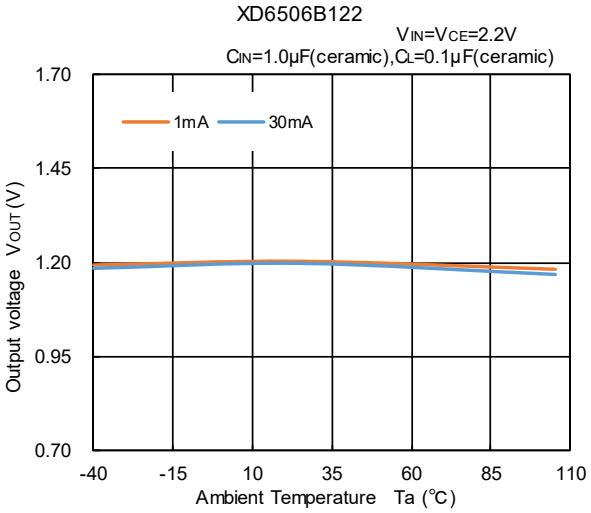


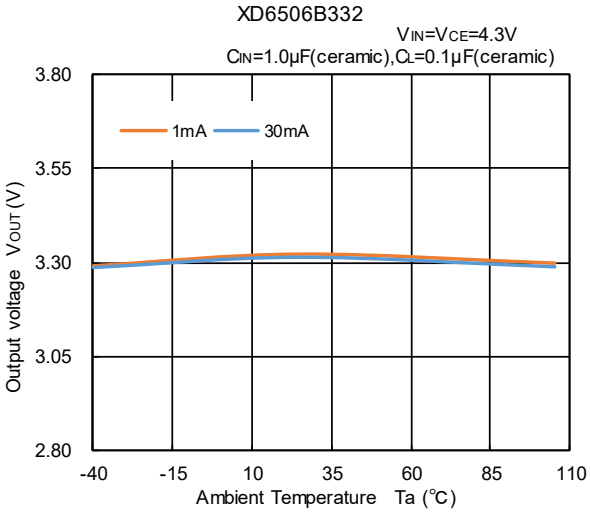
■ TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

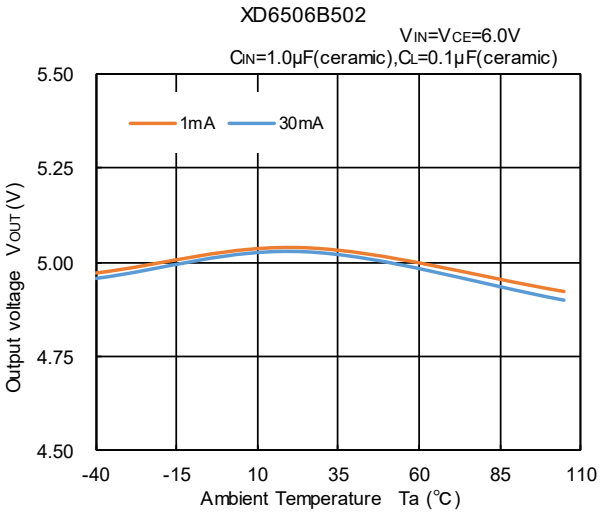
(4) Supply Current vs. Input Voltage (Continued)



(5) Output Voltage vs. Ambient Temperature

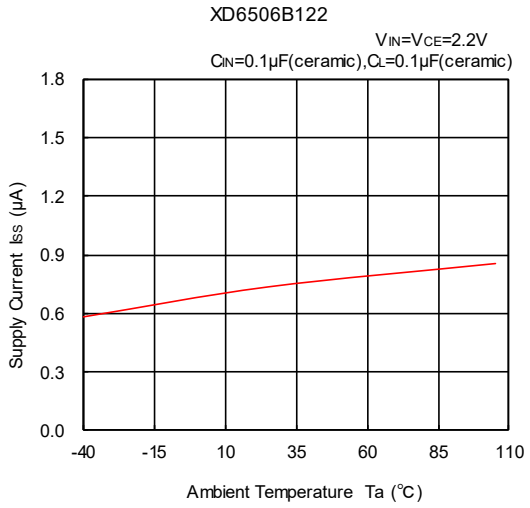


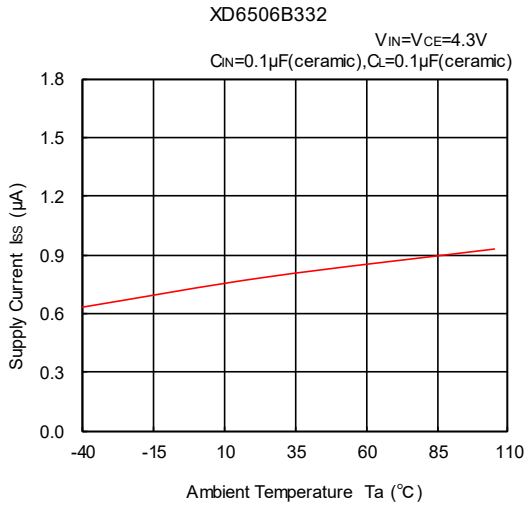


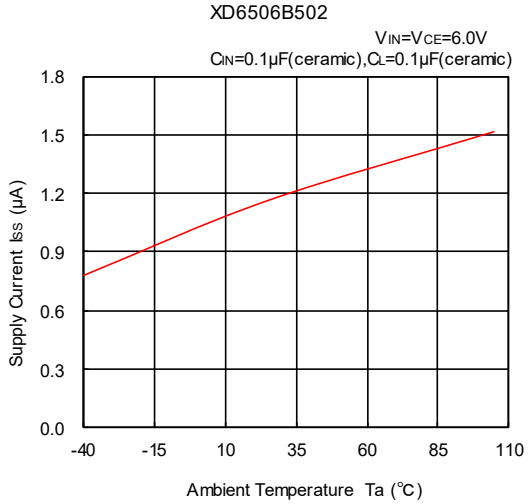


■ TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

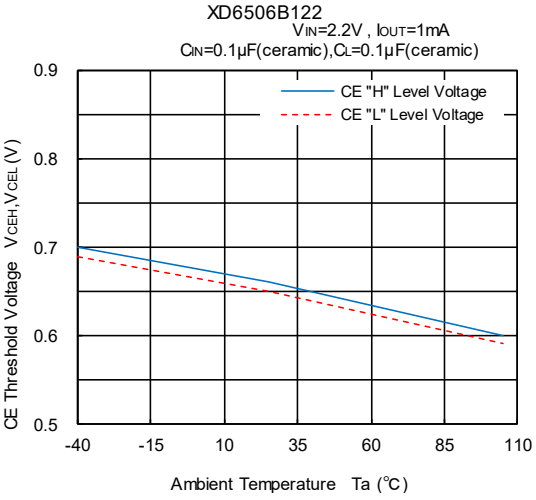
(6) Supply Current vs. Ambient Temperature

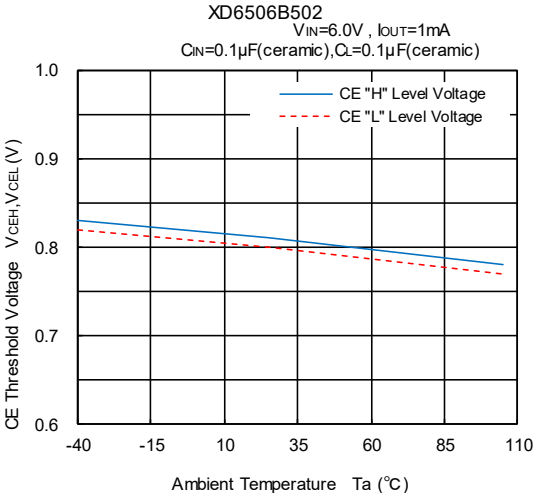






(7) CE Threshold Voltage vs. Ambient Temperature

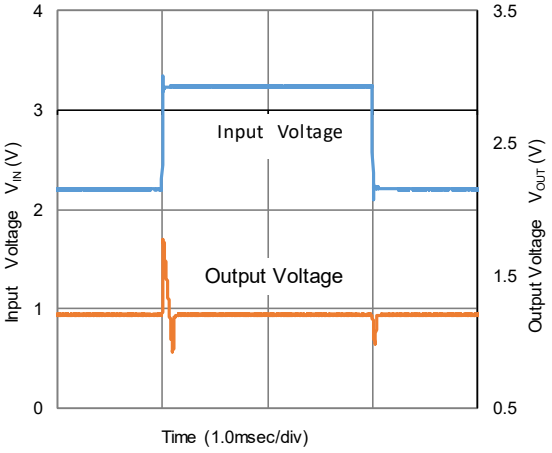




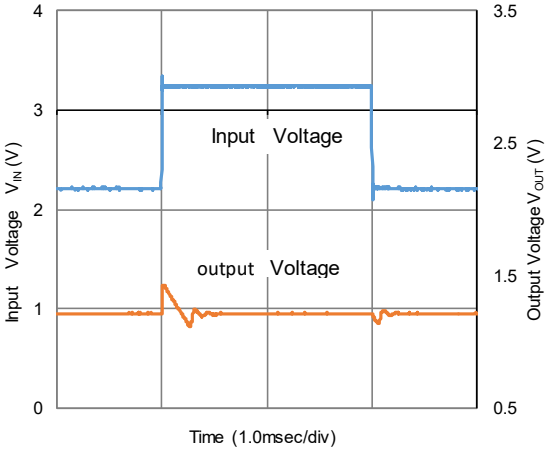
■ TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

(8) Input Transient Response

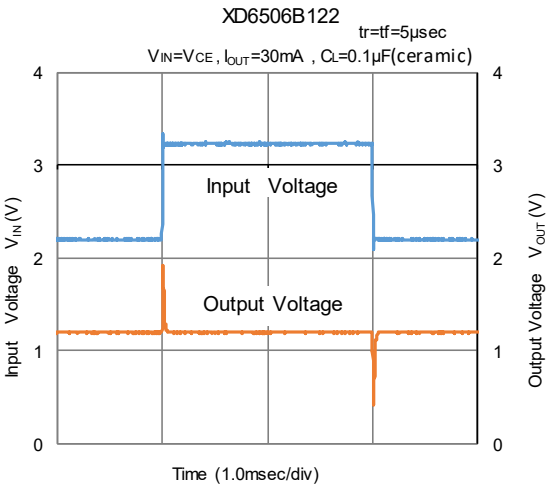
XD6506B122 tr=tf=5µsec
$V_{IN}=V_{CE}$, I_{OUT}=1mA , C_L=0.1µF(ceramic)



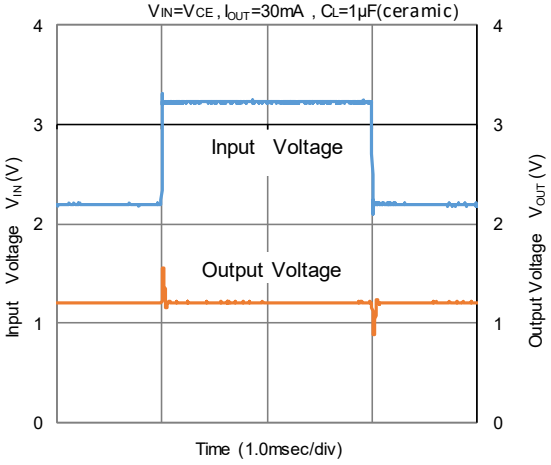
XD6506B122 tr=tf=5µsec
$V_{IN}=V_{CE}$, I_{OUT}=1mA , C_L=1µF(ceramic)



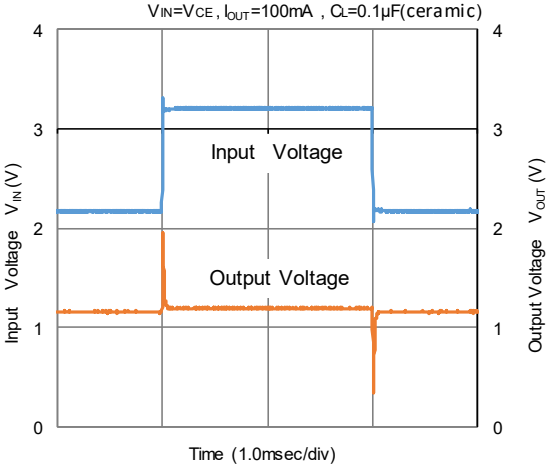
XD6506B122 tr=tf=5µsec
$V_{IN}=V_{CE}$, I_{OUT}=30mA , C_L=0.1µF(ceramic)



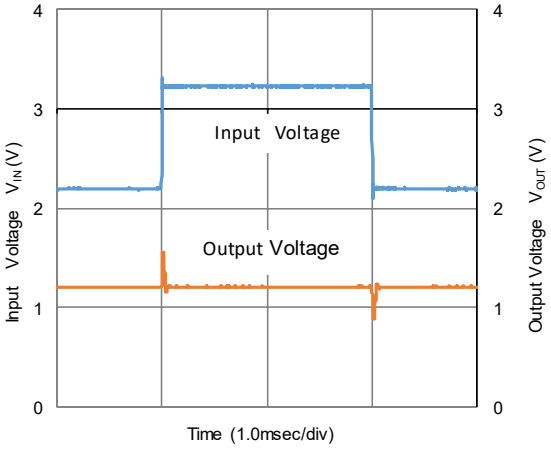
XD6506B122 tr=tf=5µsec
$V_{IN}=V_{CE}$, I_{OUT}=30mA , C_L=1µF(ceramic)



XD6506B122 tr=tf=5µsec
$V_{IN}=V_{CE}$, I_{OUT}=100mA , C_L=0.1µF(ceramic)

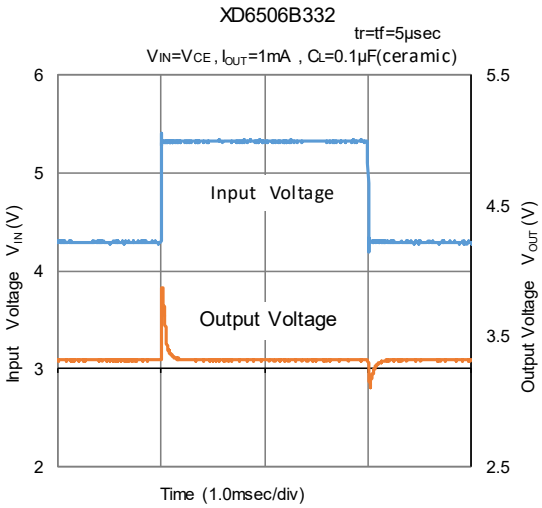


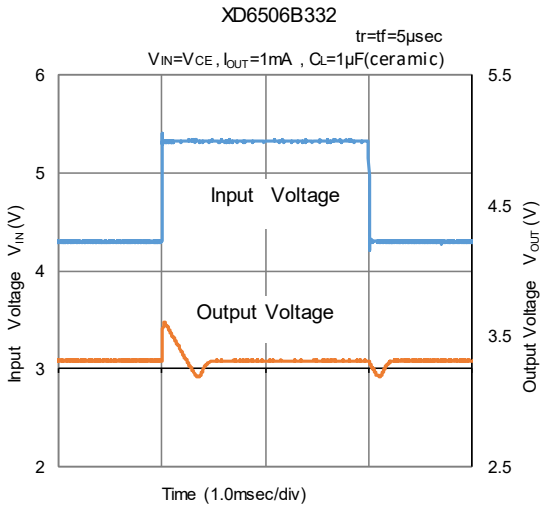
XD6506B122 tr=tf=5µsec
$V_{IN}=V_{CE}$, I_{OUT}=100mA , C_L=1µF(ceramic)

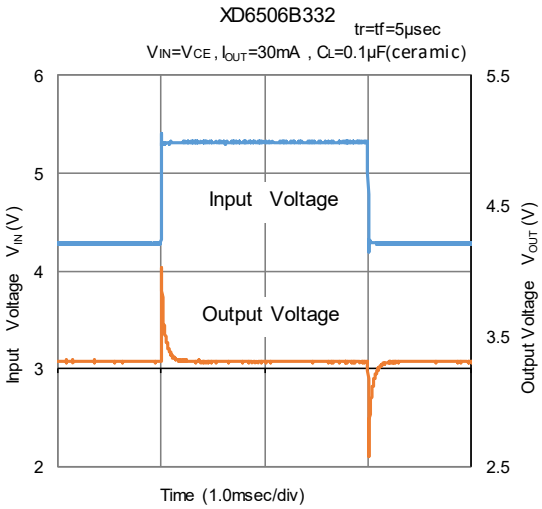


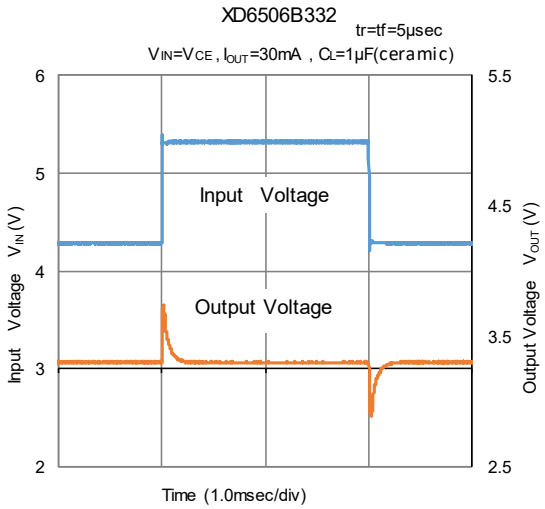
■ TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

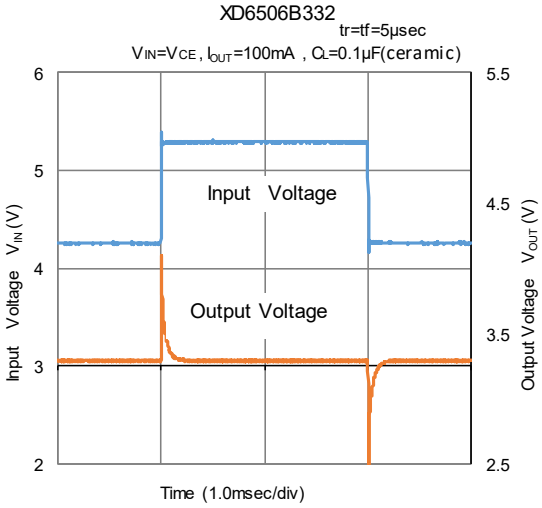
(8) Input Transient Response (Continued)

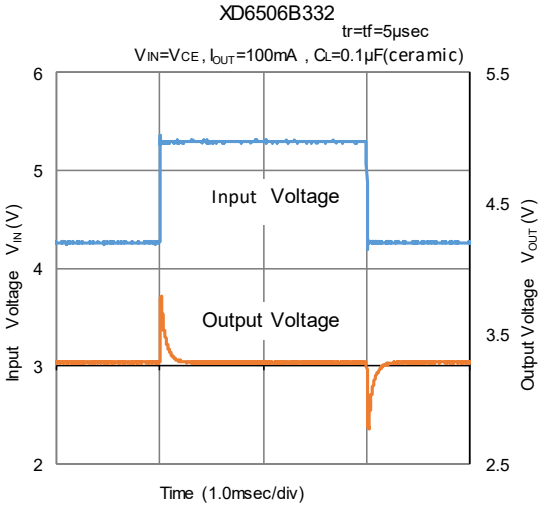








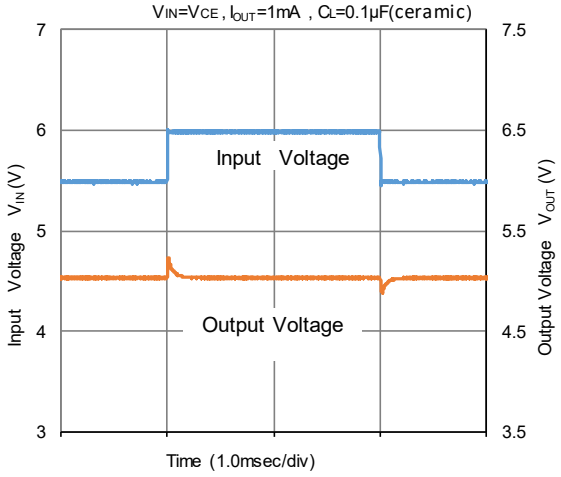




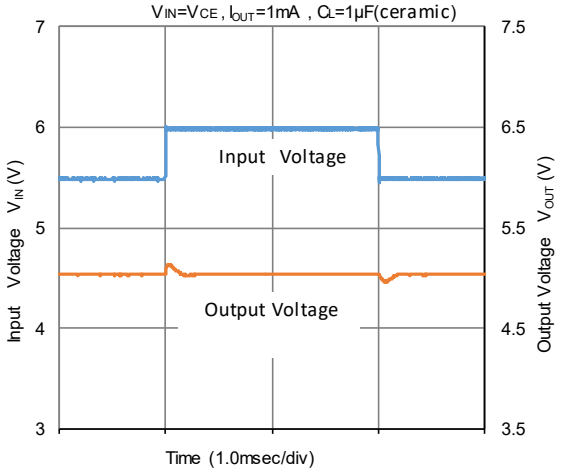
■ TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

(8) Input Transient Response (Continued)

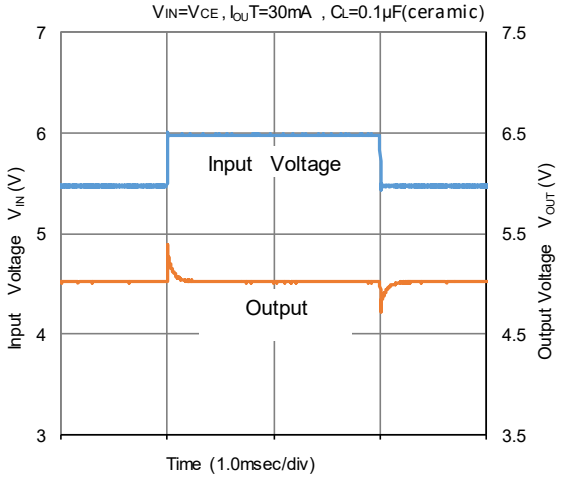
XD6506B502
tr=tf=5μsec
VIN=VCE, IOUT=1mA , CL=0.1μF(ceramic)



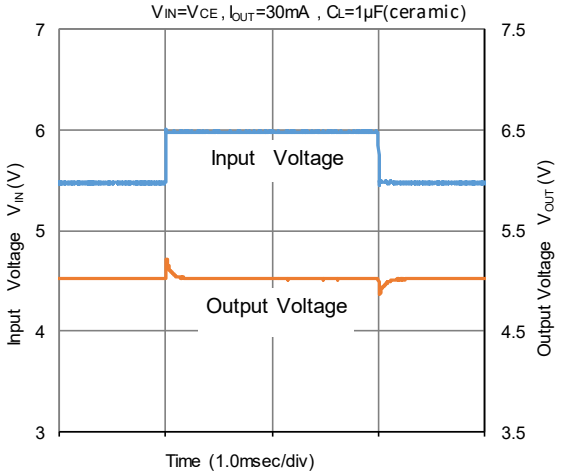
XD6506B502
tr=tf=5μsec
VIN=VCE, IOUT=1mA , CL=1μF(ceramic)



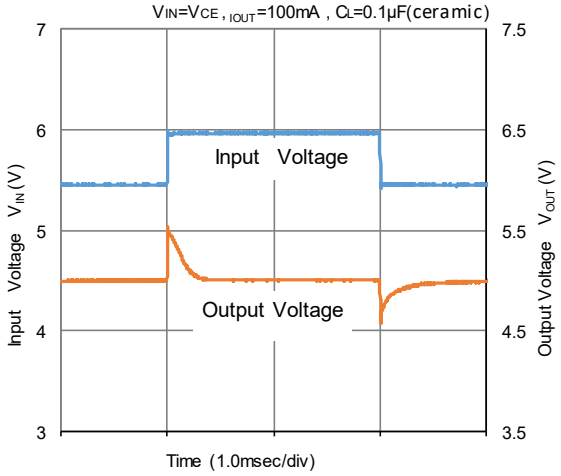
XD6506B502
tr=tf=5μsec
VIN=VCE, IOUT=30mA , CL=0.1μF(ceramic)



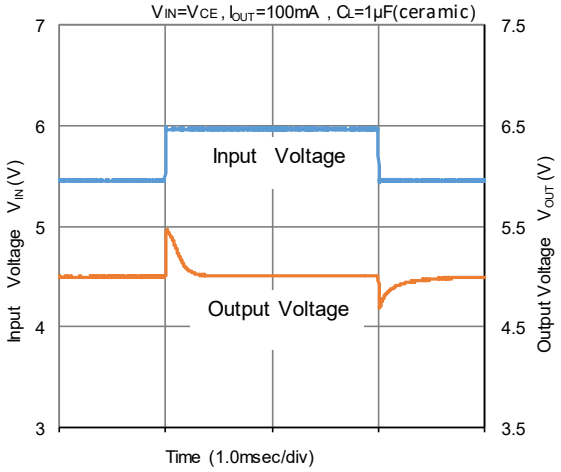
XD6506B502
tr=tf=5μsec
VIN=VCE, IOUT=30mA , CL=1μF(ceramic)



XD6506B502
tr=tf=5μsec
VIN=VCE, IOUT=100mA , CL=0.1μF(ceramic)



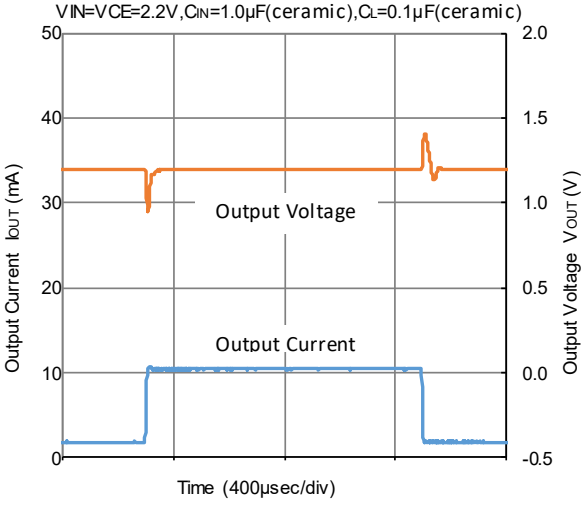
XD6506B502
tr=tf=5μsec
VIN=VCE, IOUT=100mA , CL=1μF(ceramic)



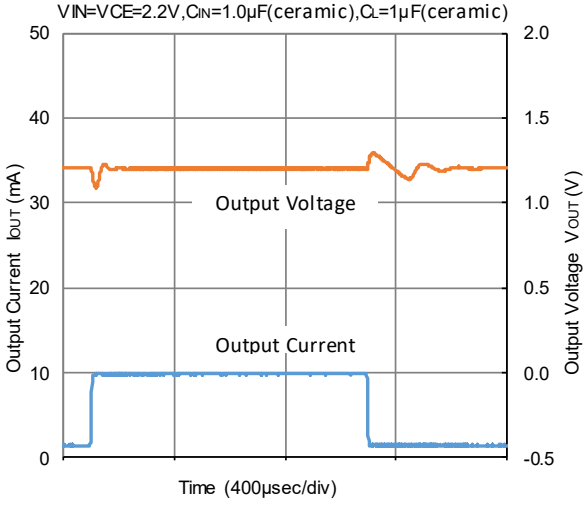
■ TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

(9) Load Transient Response

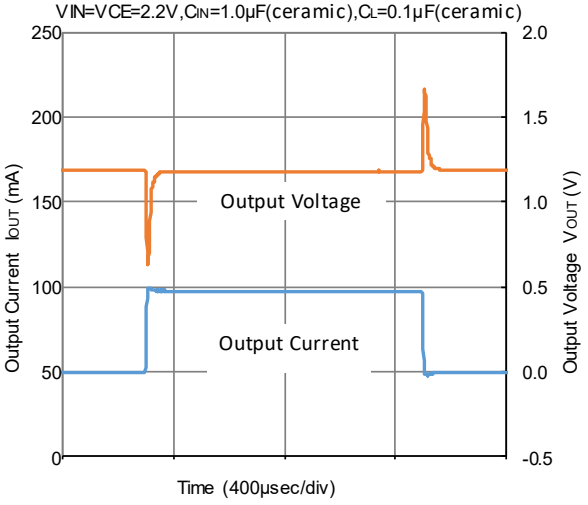
XD6506B122
tr=tf=5μsec
VIN=VCE=2.2V,CIN=1.0μF(ceramic),CL=0.1μF(ceramic)



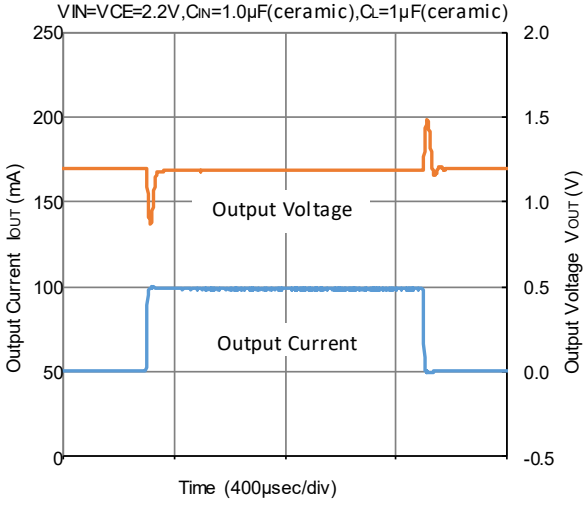
XD6506B122
tr=tf=5μsec
VIN=VCE=2.2V,CIN=1.0μF(ceramic),CL=1μF(ceramic)



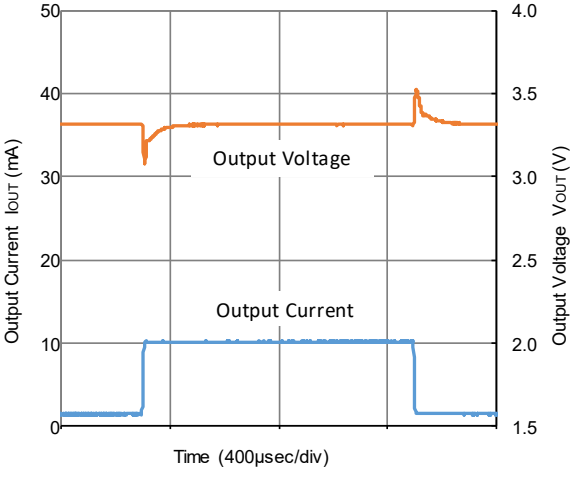
XD6506B122
tr=tf=5μsec
VIN=VCE=2.2V,CIN=1.0μF(ceramic),CL=0.1μF(ceramic)



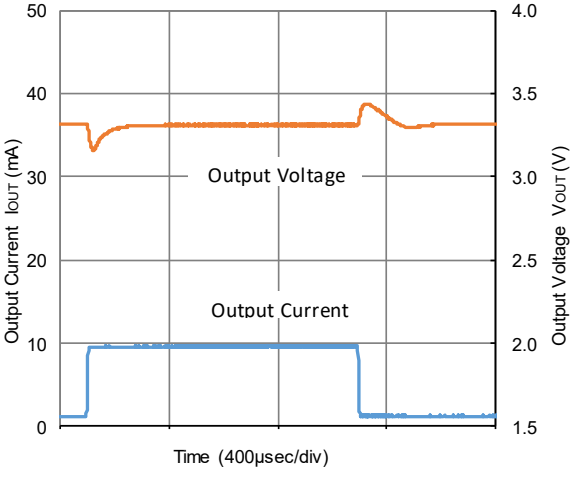
XD6506B122
tr=tf=5μsec
VIN=VCE=2.2V,CIN=1.0μF(ceramic),CL=1μF(ceramic)



XD6506B332
tr=tf=5μsec
VIN=VCE=4.3V,CIN=1.0μF(ceramic),CL=0.1μF(ceramic)

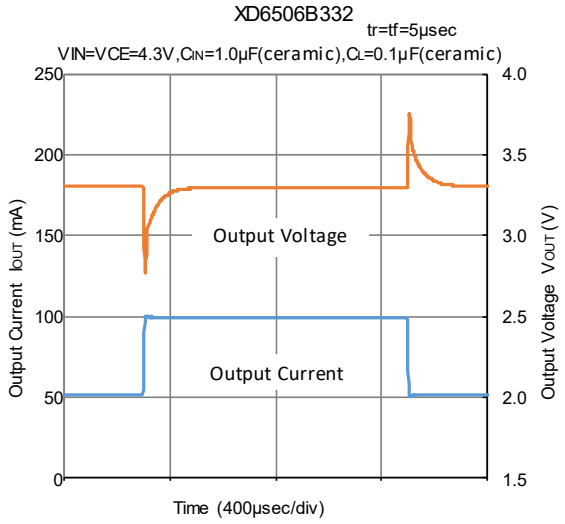


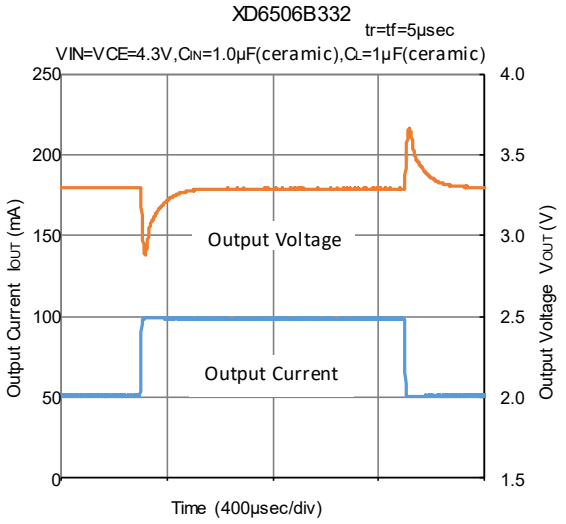
XD6506B332
tr=tf=5μsec
VIN=VCE=4.3V,CIN=1.0μF(ceramic),CL=1μF(ceramic)

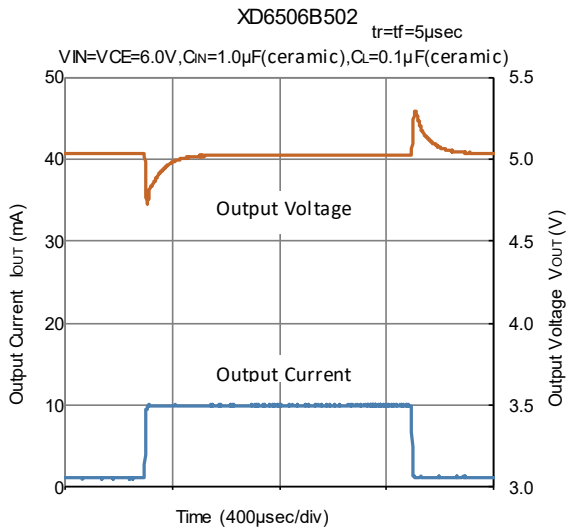


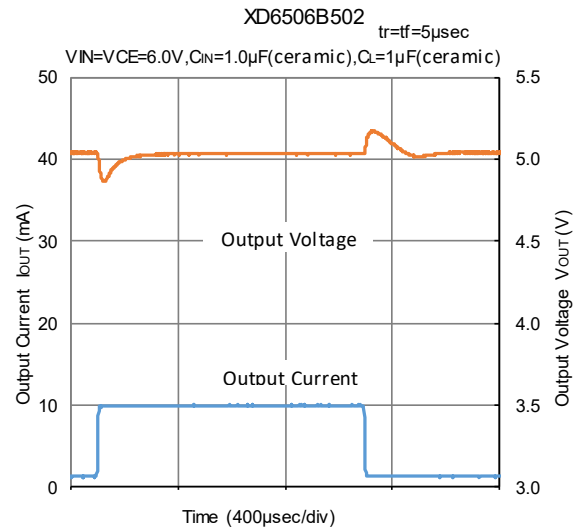
■ TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

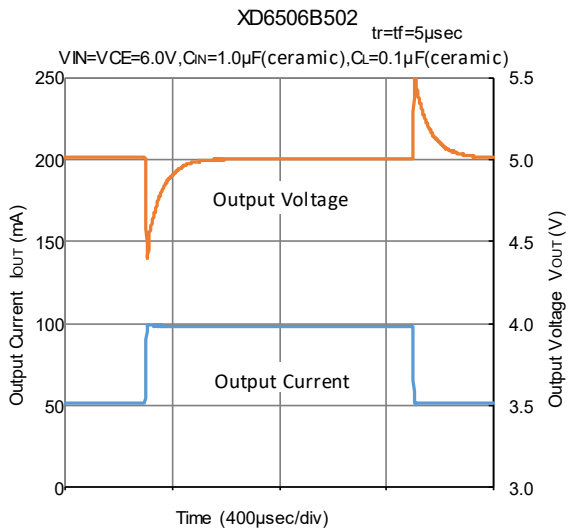
(9) Load Transient Response (Continued)

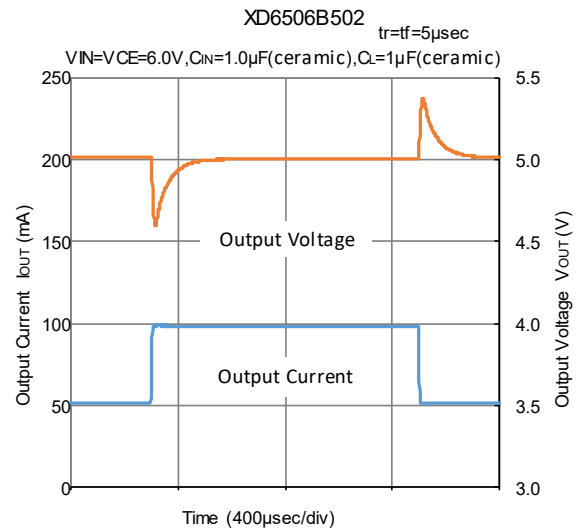










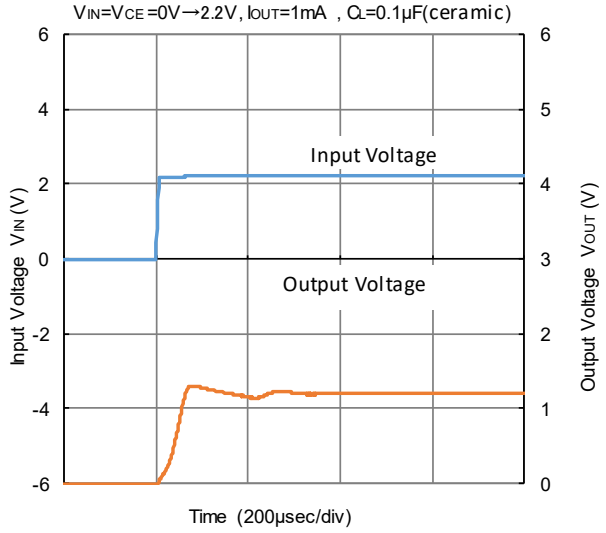


■ TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

(10) Rising Response Time

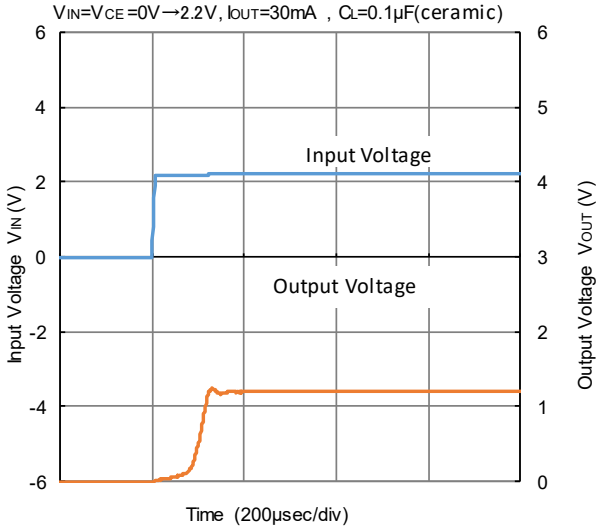
XD6506B122 tr=5μsec

$V_{IN}=V_{CE}$ =0V→2.2V, I_{OUT}=1mA , C_L=0.1μF(ceramic)



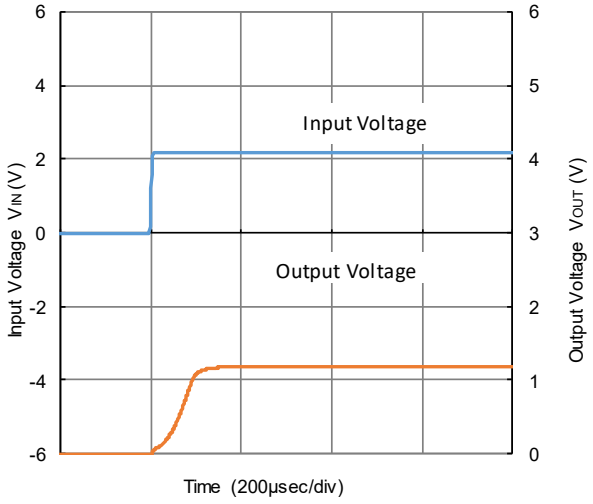
XD6506B122 tr=5μsec

$V_{IN}=V_{CE}$ =0V→2.2V, I_{OUT}=30mA , C_L=0.1μF(ceramic)



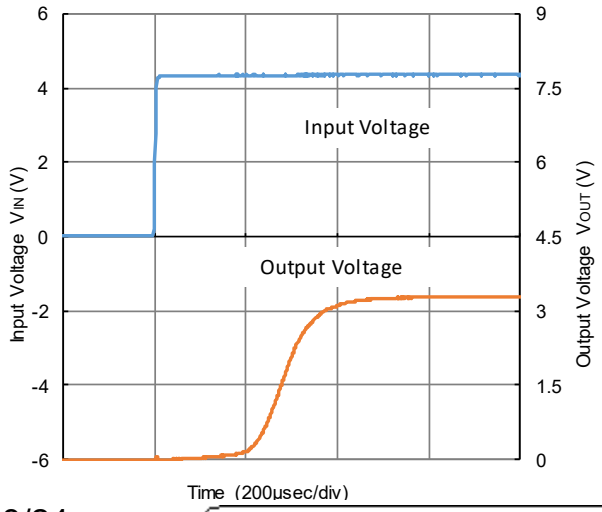
XD6506B122 tr=5μsec

$V_{IN}=V_{CE}$ =0V→2.2V, I_{OUT}=100mA , C_L=0.1μF(ceramic)



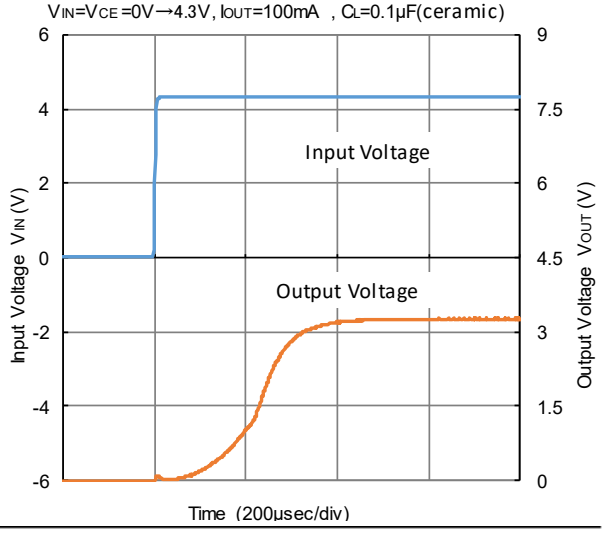
XD6506B332 tr=5μsec

$V_{IN}=V_{CE}$ =0V→4.3V, I_{OUT}=30mA , C_L=0.1μF(ceramic)



XD6506B332 tr=5μsec

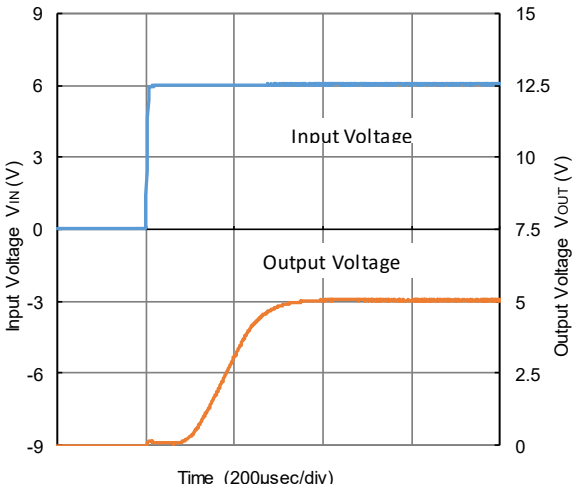
$V_{IN}=V_{CE}$ =0V→4.3V, I_{OUT}=100mA , C_L=0.1μF(ceramic)



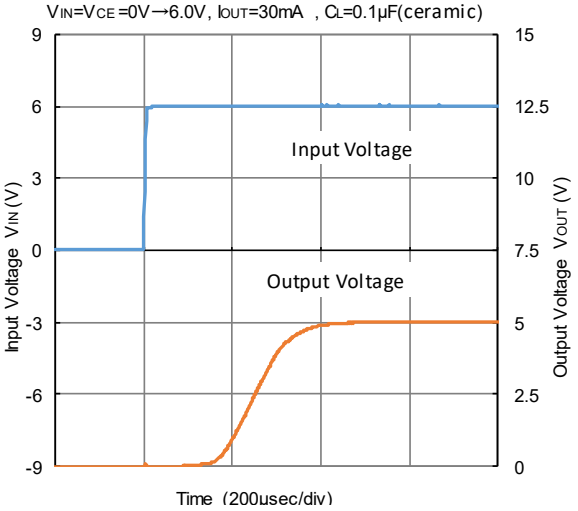
■ TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

(10) Rising Response Time (Continued)

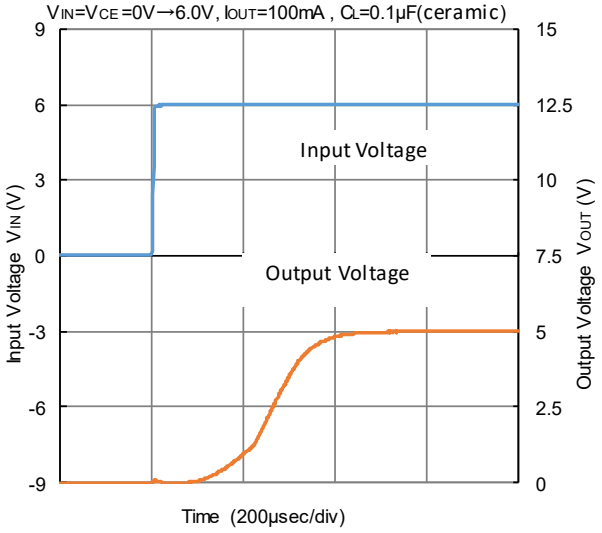
XD6506B502
tr=5μsec
V_{IN}=V_{CE}=0V→6.0V, I_{OUT}=1mA , C_L=0.1μF(ceramic)



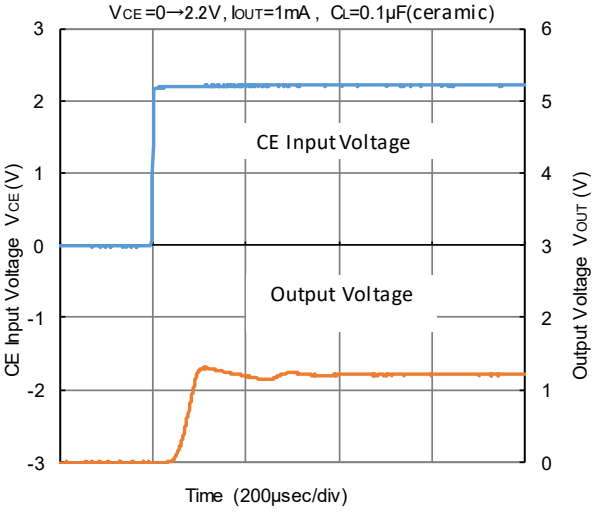
XD6506B502
tr=5μsec
V_{IN}=V_{CE}=0V→6.0V, I_{OUT}=30mA , C_L=0.1μF(ceramic)



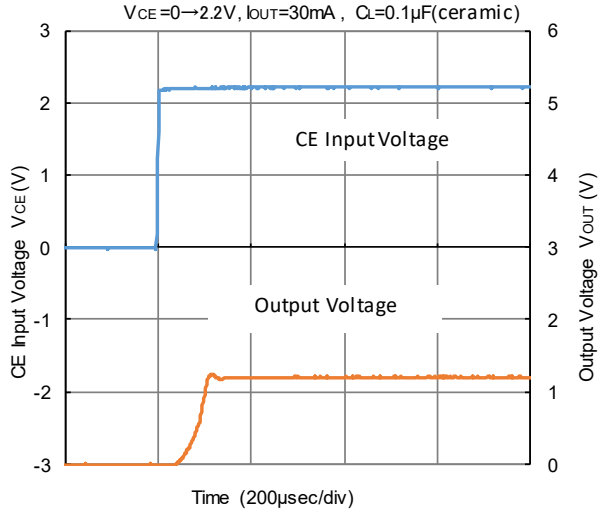
XD6506B502
tr=5μsec
V_{IN}=V_{CE}=0V→6.0V, I_{OUT}=100mA , C_L=0.1μF(ceramic)



XD6506B122
tr=5μsec ,V_{IN}=2.2V
V_{CE}=0→2.2V, I_{OUT}=1mA , C_L=0.1μF(ceramic)



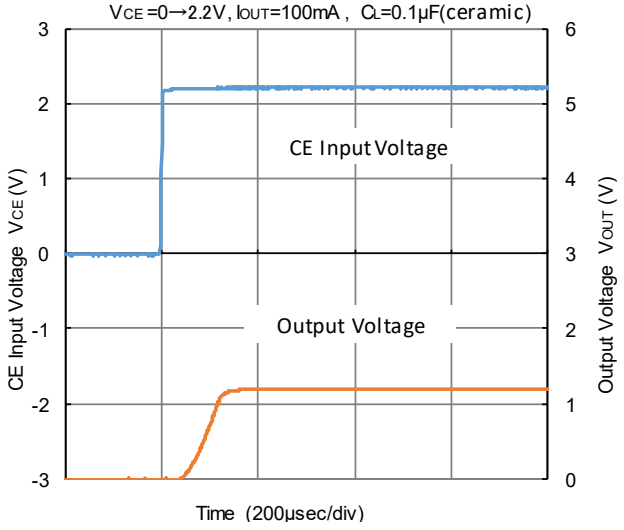
XD6506B122
tr=5μsec ,V_{IN}=2.2V
V_{CE}=0→2.2V, I_{OUT}=30mA , C_L=0.1μF(ceramic)



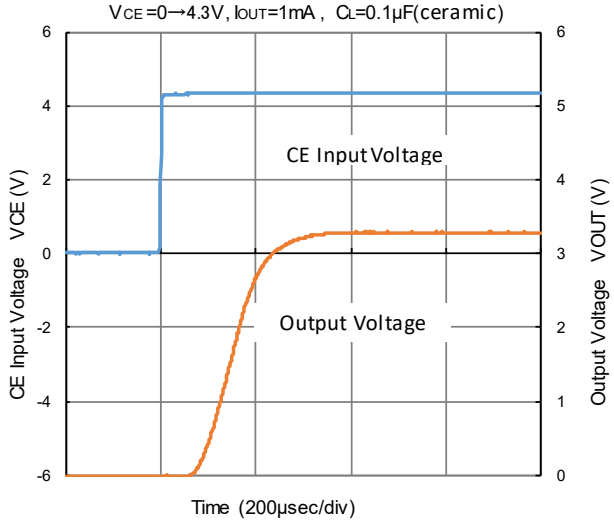
■ TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

(11) CE Rising Response Time (Continued)

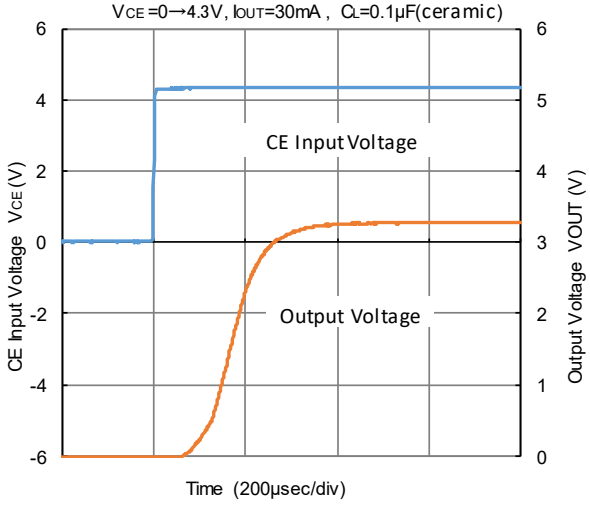
XD6506B122
tr=5μsec ,VIN=2.2V
VCE=0→2.2V, IOUT=100mA , CL=0.1μF(ceramic)



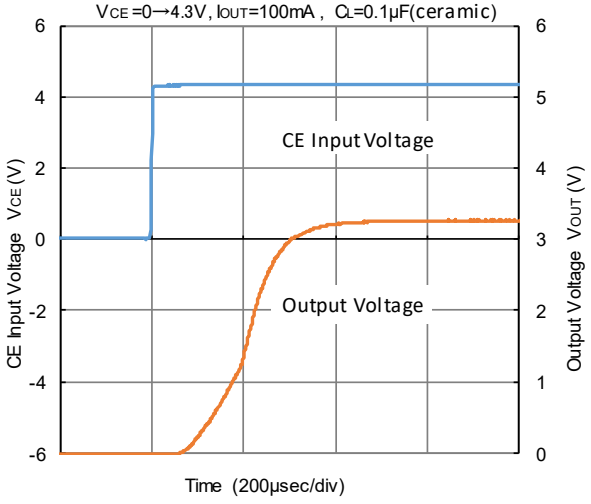
XD6506B332
tr=5μsec ,VIN=4.3V
VCE=0→4.3V, IOUT=1mA , CL=0.1μF(ceramic)



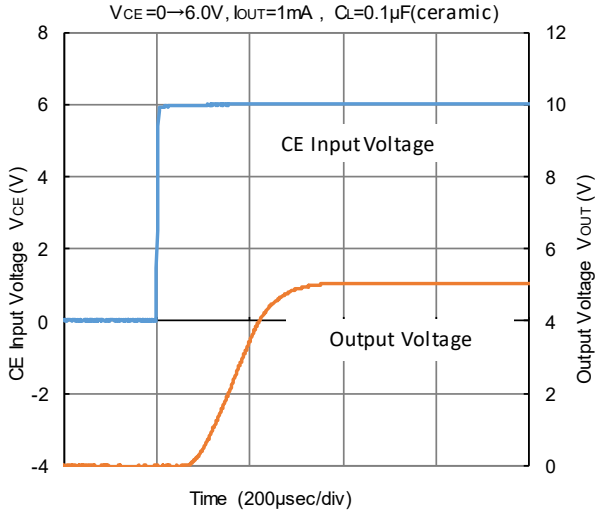
XD6506B332
tr=5μsec ,VIN=4.3V
VCE=0→4.3V, IOUT=30mA , CL=0.1μF(ceramic)



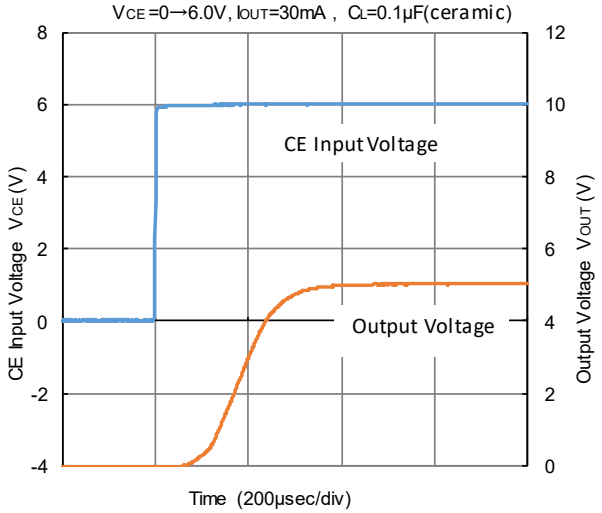
XD6506B332
tr=5μsec ,VIN=4.3V
VCE=0→4.3V, IOUT=100mA , CL=0.1μF(ceramic)



XD6506B502
tr=5μsec ,VIN=6.0V
VCE=0→6.0V, IOUT=1mA , CL=0.1μF(ceramic)

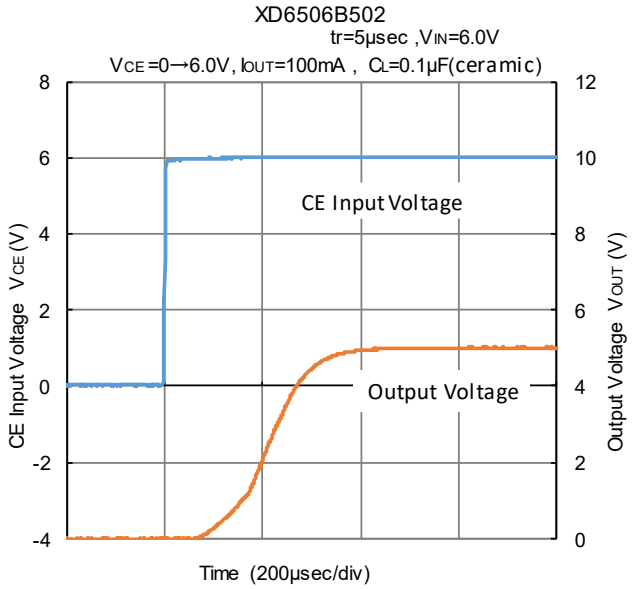


XD6506B502
tr=5μsec ,VIN=6.0V
VCE=0→6.0V, IOUT=30mA , CL=0.1μF(ceramic)

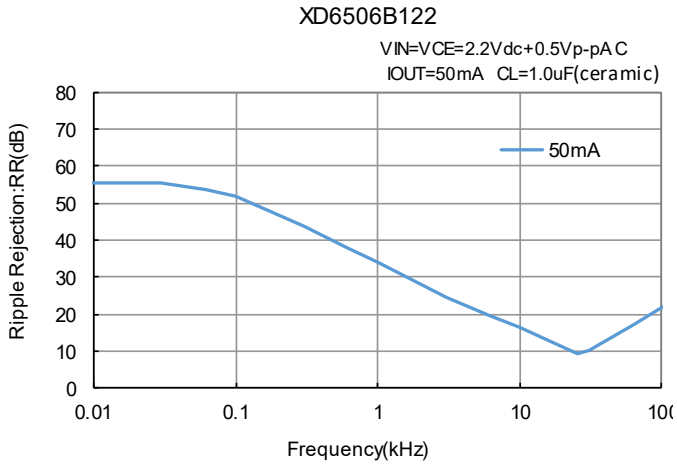


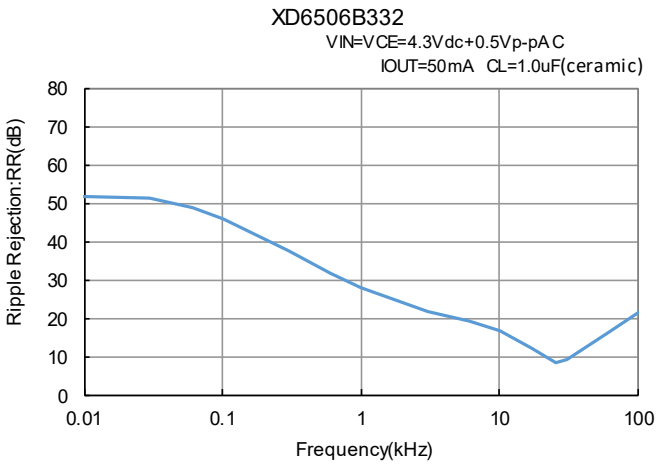
■ TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

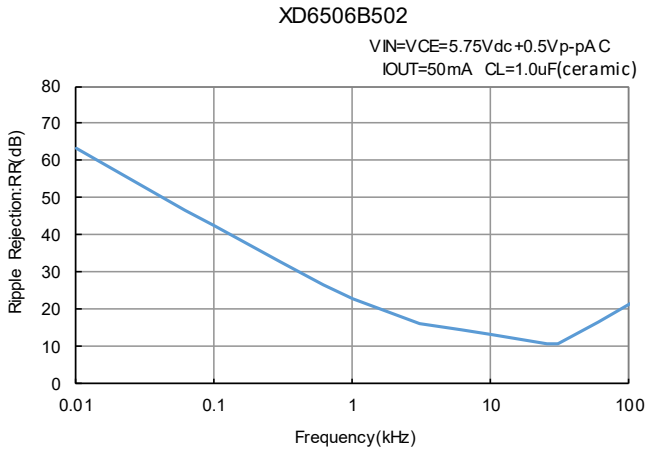
(11) CE Rising Response Time (Continued)



(12) Ripple Rejection Rate







■PACKAGING INFORMATION

For the latest package information go to, www.torexsemi.com/technical-support/packages

PACKAGE	OUTLINE / LAND PATTERN	THERMAL CHARACTERISTICS
SOT-25	SOT-25 PKG	SOT-25 Power Dissipation

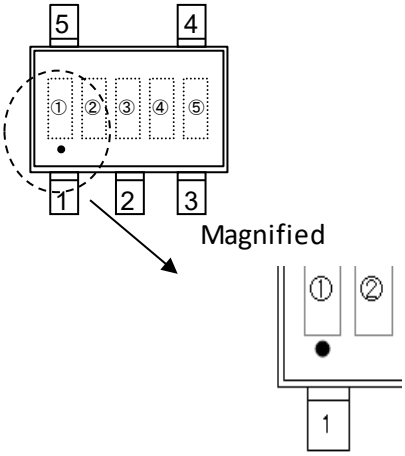
■MARKING RULE

●SOT-25

① represents the product series

MARK	PRODUCT SERIES
E	XD6506xxxxxx-Q

SOT-25(Under dot)



② represents the output voltage range

MARK	Type	VOLTAGE (V)	PRODUCT SERIES
0	with CE function	1.2~3.0	XD6506Bxxxxx-Q
1		3.1~5.0	

③ represents the output voltage

MARK	VOLTAGE(V)		MARK	VOLTAGE(V)	
0	-	3.1	F	1.6	4.6
1	-	3.2	H	1.7	4.7
2	-	3.3	K	1.8	4.8
3	-	3.4	L	1.9	4.9
4	-	3.5	M	2.0	5.0
5	-	3.6	N	2.1	-
6	-	3.7	P	2.2	-
7	-	3.8	R	2.3	-
8	-	3.9	S	2.4	-
9	-	4.0	T	2.5	-
A	-	4.1	U	2.6	-
B	1.2	4.2	V	2.7	-
C	1.3	4.3	X	2.8	-
D	1.4	4.4	Y	2.9	-
E	1.5	4.5	Z	3.0	-

④⑤ represents assembly lot number
01 to 09, 0A to 0Z, 11 to 9Z, A1 to A9, AA to AZ, B1 to ZZ repeated (G, I, J, O, Q, W excluded)
Note: No character inversion used.

1. The product and product specifications contained herein are subject to change without notice to improve performance characteristics. Consult us, or our representatives before use, to confirm that the information in this datasheet is up to date.

2. The information in this datasheet is intended to illustrate the operation and characteristics of our products. We neither make warranties or representations with respect to the accuracy or completeness of the information contained in this datasheet nor grant any license to any intellectual property rights of ours or any third party concerning with the information in this datasheet.

3. Applicable export control laws and regulations should be complied and the procedures required by such laws and regulations should also be followed, when the product or any information contained in this datasheet is exported.

4. The product is neither intended nor warranted for use in equipment of systems which require extremely high levels of quality and/or reliability and/or a malfunction or failure which may cause loss of human life, bodily injury, serious property damage including but not limited to devices or equipment used in 1) nuclear facilities, 2) aerospace industry, 3) medical facilities, 4) automobile industry and other transportation industry and 5) safety devices and safety equipment to control combustions and explosions, excluding when specified for in-vehicle use or other uses.
   Do not use the product for in-vehicle use or other uses unless agreed by us in writing in advance.

5. Although we make continuous efforts to improve the quality and reliability of our products; nevertheless Semiconductors are likely to fail with a certain probability. So in order to prevent personal injury and/or property damage resulting from such failure, customers are required to incorporate adequate safety measures in their designs, such as system fail safes, redundancy and fire prevention features.

6. Our products are not designed to be Radiation-resistant.

7. Please use the product listed in this datasheet within the specified ranges.

8. We assume no responsibility for damage or loss due to abnormal use.

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