



# N-Channel Enhancement Mode Power MOSFET

### **Description**

The RM6005S4 uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

# **General Features**

V<sub>DS</sub> =60V,I<sub>D</sub> =5A

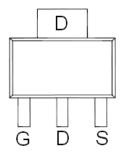
$$\begin{split} R_{DS(ON)} < 55 m\Omega &\ @\ V_{GS} = 10V \ (\text{Typ: } 46 m\Omega) \\ R_{DS(ON)} < 80 m\Omega &\ @\ V_{GS} = 4.5V \ (\text{Typ: } 60 m\Omega) \end{split}$$

# G H

Schematic diagram

# **Application**

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply
- Halogen-free
- P/N suffix V means AEC-Q101 qualified, e.g:RM6005S4V



SOT-223-3L view

# **Package Marking and Ordering Information**

Device Marking	Device	<b>Device Package</b>	Reel Size	Tape width	Quantity	
6005	RM6005S4	SOT-223-3L	Ø330mm	12mm	2500	units

### Absolute Maximum Ratings (T<sub>A</sub>=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	60	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous	I <sub>D</sub>	5	А
Drain Current-Continuous(T <sub>C</sub> =100°C)	I <sub>D</sub> (100°C)	3.5	А
Pulsed Drain Current	I <sub>DM</sub>	20	А
Maximum Power Dissipation	P <sub>D</sub>	2	W
Operating Junction and Storage Temperature Range	$T_{J}, T_{STG}$	-55 To 150	$^{\circ}$ C

# **Thermal Characteristic**

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ heta JA}$	62.5	°C/W

### **Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)**

= control ( A = c control cont						
Parameter Symbo		Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	$V_{GS}$ =0 $V$ $I_D$ =250 $\mu$ A	60	69	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}=60V, V_{GS}=0V$	-	-	1	μA

Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)				•		
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS},I_{D}=250\mu A$	1.2	2	2.5	V
Drain-Source On-State Resistance	P	$V_{GS}$ =10V, $I_D$ =4.5A		46	55	mΩ
Diain-Source On-State Resistance	R <sub>DS(ON)</sub>	$V_{GS}$ =4.5 $V$ , $I_D$ =4 $A$		60	80	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =5V,I <sub>D</sub> =4.5A	11	-	-	S
Dynamic Characteristics (Note4)				•		
Input Capacitance	C <sub>lss</sub>	V 05VV 0V		450		PF
Output Capacitance	Coss	$V_{DS}$ =25V, $V_{GS}$ =0V, F=1.0MHz		60		PF
Reverse Transfer Capacitance	C <sub>rss</sub>	T = 1.01VII 12		25		PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>		-	4.7	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{Ds}{=}30V,I_{D}{=}4.5A$	-	2.3	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{\text{GS}} = 10V, R_{\text{GEN}} = 3\Omega$	-	15.7	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	1.9	-	nS
Total Gate Charge	$Q_g$	$V_{DS}=30V, I_{D}=4.5A,$	-	8.5	-	nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 30V, I_{D} = 4.5A,$ $V_{GS} = 10V$	-	1.6	-	nC
Gate-Drain Charge	$Q_{gd}$	V GS=10 V	-	2.2	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	$V_{GS}$ =0 $V$ , $I_{S}$ =5 $A$	-	-	1.2	V
Diode Forward Current (Note 2)	Is		-	-	5	Α

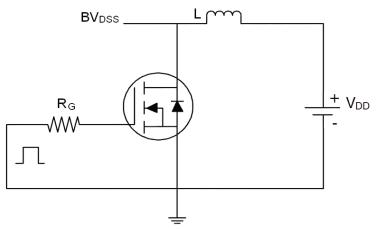
# Notes:

- **1.** Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
   Guaranteed by design, not subject to production

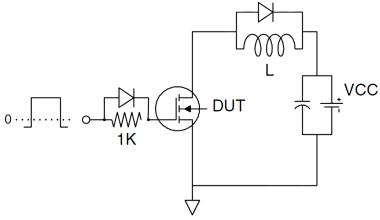


# **Test Circuit**

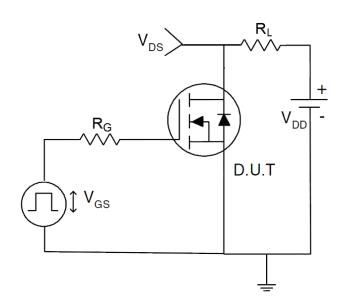
# 1) E<sub>AS</sub> Test Circuit



# 2) Gate Charge Test Circuit

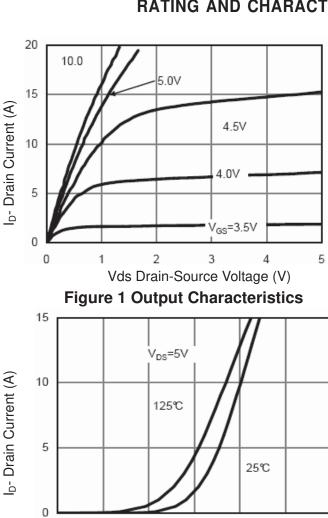


# 3) Switch Time Test Circuit





# RATING AND CHARACTERISTICS CURVES (RM6005S4)



Vgs Gate-Source Voltage (V)
Figure 2 Transfer Characteristics

3.5

4

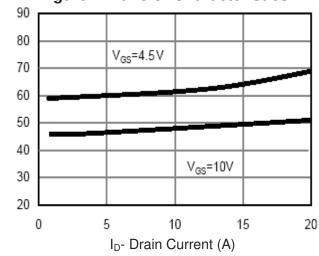
4.5

5

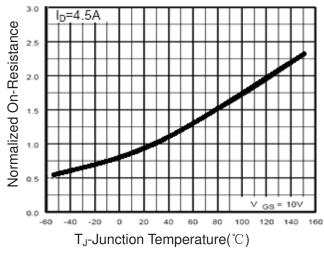
2.5

2

Rdson On-Resistance(mΩ)



**Figure 3 Rdson- Drain Current** 



**Figure 4 Rdson-Junction Temperature** 

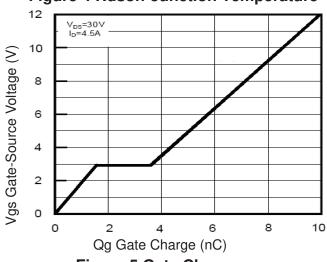


Figure 5 Gate Charge

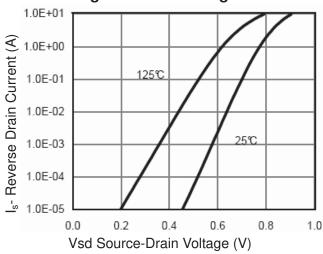


Figure 6 Source- Drain Diode Forward

# **RATING AND CHARACTERISTICS CURVES (RM6005S4)**

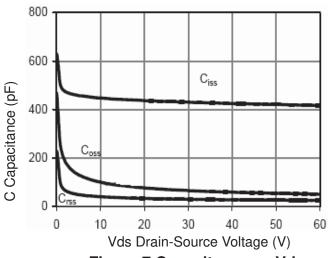


Figure 7 Capacitance vs Vds

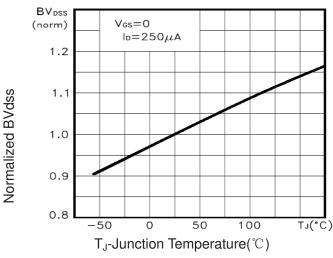


Figure 9 BV<sub>DSS</sub> vs Junction Temperature

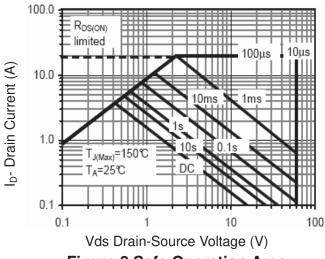


Figure 8 Safe Operation Area

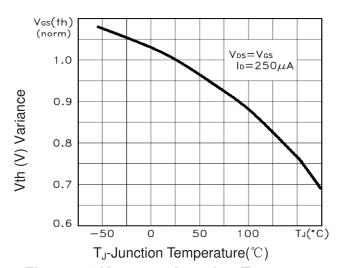
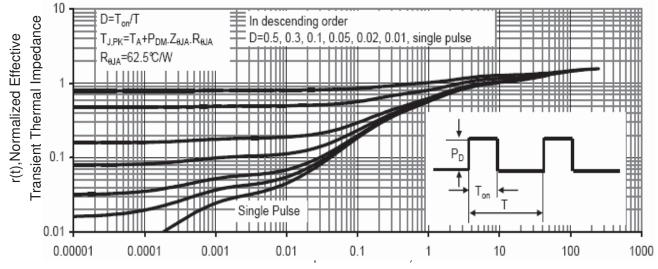
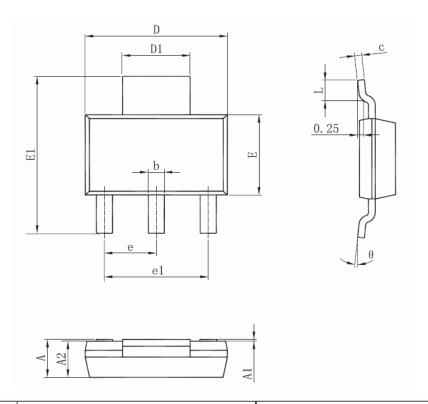


Figure 10 V<sub>GS(th)</sub> vs Junction Temperature



**Figure 11 Normalized Maximum Transient Thermal Impedance** 

# **SOT-223-3L Package Information**



Comb of	Dimensions Ir	n Millimeters	Dimensions In Inches		
Symbol	Min	Max	Min	Max	
Α	1.520	1.800	0.060	0.071	
A1	0.000	0.100	0.000	0.004	
A2	1.500	1.700	0.059	0.067	
b	0.660	0.820	0.026	0.032	
С	0.250	0.350	0.010	0.014	
D	6.200	6.400	0.244	0.252	
D1	2.900	3.100	0.114	0.122	
E	3.300	3.700	0.130	0.146	
E1	6.830	7.070	0.269	0.278	
е	2.300(BSC)		0.091(BSC)		
e1	4.500	4.700	0.177	0.185	
L	0.900	1.150	0.035	0.045	
θ	0°	10°	0°	10°	

# Notes

- 1. All dimensions are in millimeters.
- 2. Tolerance ±0.10mm (4 mil) unless otherwise specified
- 3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 4. Dimension L is measured in gauge plane.
- $5. \ Controlling \ dimension \ is \ millimeter, \ converted \ inch \ dimensions \ are \ not \ necessarily \ exact.$



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