

## Product Summary

| BV <sub>DSS</sub> | R <sub>DS(ON)</sub>      | Package               | I <sub>D</sub> MAX<br>T <sub>C</sub> = +25°C |
|-------------------|--------------------------|-----------------------|--|
| 950V              | 7Ω@V <sub>GS</sub> = 10V | ITO220AB<br>(Type TH) | 2.5A   |

## Description

This new generation complementary dual MOSFET features low on-resistance and fast switching, making it ideal for high efficiency power management applications.

## Applications

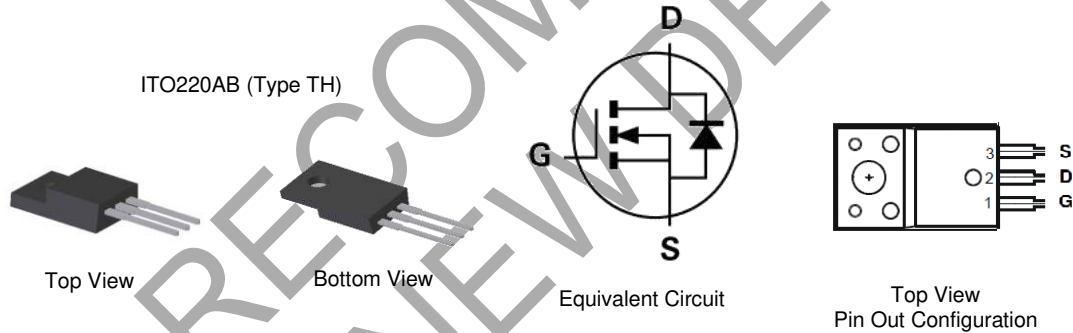
- Motor Control
- Backlighting
- DC-DC Converters
- Power Management Functions

## Features

- Low Input Capacitance
- High BV<sub>DSS</sub> Rating for Power Application
- Low Input/Output Leakage
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](mailto:contact@diodes.com) or your local Diodes representative. <https://www.diodes.com/quality/product-definitions/>**

## Mechanical Data

- Case: ITO220AB
- Case Material: Molded Plastic, "Green" Molding Compound, UL Flammability Classification Rating 94V-0
- Terminals: Matte Tin Finish Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 Ⓔ
- Terminal Connections: See Diagram Below
- Weight: 1.85 grams (Approximate)

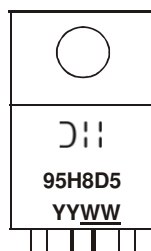


## Ordering Information (Note 4)

| Part Number   | Case               | Packaging      |
|---------------|--------------------|----------------|
| DMN95H8D5HCTI | ITO220AB (Type TH) | 50 Pieces/Tube |

- Notes:
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
  2. See [http://www.diodes.com/quality/lead\\_free/](http://www.diodes.com/quality/lead_free/) for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information



= Manufacturer's Marking  
 95H8D5 = Product Type Marking Code  
 YYWW = Date Code Marking  
 YY or YY = Last Two Digits of Year (ex: 20 = 2020)  
 WW or WW = Week Code (01 to 53)

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

| Characteristic  | Symbol           | Value                   | Unit |
|---|------------------|-------------------------|------|
| Drain-Source Voltage  | V <sub>DSS</sub> | 950                     | V    |
| Gate-Source Voltage   | V <sub>GSS</sub> | ±30                     | V    |
| Continuous Drain Current (Notes 5)<br>V <sub>GS</sub> = 10V | I <sub>D</sub>   | T <sub>C</sub> = +25°C  | 2.5  |
|   |                  | T <sub>C</sub> = +100°C | 1.5  |
| Pulsed Drain Current (Note 6)                               | I <sub>DM</sub>  | 3                       | A    |
| Avalanche Current, L = 60mH (Note 7)                        | I <sub>AS</sub>  | 1.8                     | A    |
| Avalanche Energy, L = 60mH (Note 7)                         | E <sub>AS</sub>  | 97                      | mJ   |
| Peak Diode Recovery dv/dt (Note 7)                          | dv/dt            | 3.3                     | V/ns |

**Thermal Characteristics**

| Characteristic                                | Symbol                            | Max                     | Unit |
|---|-----------------------------------|-------------------------|------|
| Power Dissipation (Note 5)                    | P <sub>D</sub>                    | T <sub>C</sub> = +25°C  | 30   |
|   |                                   | T <sub>C</sub> = +100°C | 12   |
| Thermal Resistance, Junction to Case (Note 5) | R <sub>θJC</sub>                  | 4.2                     | °C/W |
| Operating and Storage Temperature Range       | T <sub>J</sub> , T <sub>STG</sub> | -55 to +150             | °C   |

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

| Characteristic                          | Symbol              | Min | Typ  | Max | Unit | Test Condition  |
|---|---------------------|-----|------|-----|------|---|
| <b>OFF CHARACTERISTICS (Note 8)</b>     |                     |     |      |     |      |   |
| Drain-Source Breakdown Voltage          | BV <sub>DSS</sub>   | 950 | —    | —   | V    | V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA  |
| Zero Gate Voltage Drain Current         | I <sub>DSS</sub>    | —   | —    | 1   | μA   | V <sub>DS</sub> = 950V, V <sub>GS</sub> = 0V  |
| Gate-Source Leakage                     | I <sub>GSS</sub>    | —   | —    | 100 | nA   | V <sub>GS</sub> = ±30V, V <sub>DS</sub> = 0V  |
| <b>ON CHARACTERISTICS (Note 8)</b>      |                     |     |      |     |      |   |
| Gate Threshold Voltage                  | V <sub>GS(TH)</sub> | 3.0 | —    | 5.0 | V    | V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA                                  |
| Static Drain-Source On-Resistance       | R <sub>DS(ON)</sub> | —   | 5.5  | 7.0 | Ω    | V <sub>GS</sub> = 10V, I <sub>D</sub> = 1A  |
| Diode Forward Voltage                   | V <sub>SD</sub>     | —   | —    | 1.2 | V    | V <sub>GS</sub> = 0V, I <sub>S</sub> = 2A   |
| <b>DYNAMIC CHARACTERISTICS (Note 7)</b> |                     |     |      |     |      |   |
| Input Capacitance                       | C <sub>iss</sub>    | —   | 470  | —   | pF   | V <sub>DS</sub> = 25V, f = 1.0MHz,<br>V <sub>GS</sub> = 0V                                  |
| Output Capacitance                      | C <sub>oss</sub>    | —   | 45   | —   |      |   |
| Reverse Transfer Capacitance            | C <sub>rss</sub>    | —   | 0.6  | —   |      |   |
| Gate Resistance                         | R <sub>g</sub>      | —   | 1.2  | —   | Ω    | V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1.0MHz                                      |
| Total Gate Charge                       | Q <sub>g</sub>      | —   | 7.9  | —   | nC   | V <sub>DD</sub> = 720V, I <sub>D</sub> = 2A,<br>V <sub>GS</sub> = 10V                       |
| Gate-Source Charge                      | Q <sub>gs</sub>     | —   | 2.5  | —   |      |   |
| Gate-Drain Charge                       | Q <sub>gd</sub>     | —   | 2.9  | —   |      |   |
| Turn-On Delay Time                      | t <sub>D(ON)</sub>  | —   | 16   | —   | ns   | V <sub>DD</sub> = 450V, R <sub>g</sub> = 25Ω, I <sub>D</sub> = 2A,<br>V <sub>GS</sub> = 10V |
| Turn-On Rise Time                       | t <sub>r</sub>      | —   | 21   | —   |      |   |
| Turn-Off Delay Time                     | t <sub>D(OFF)</sub> | —   | 17.6 | —   |      |   |
| Turn-Off Fall Time                      | t <sub>f</sub>      | —   | 17   | —   |      |   |
| Body Diode Reverse Recovery Time        | t <sub>RR</sub>     | —   | 375  | —   | ns   | di/dt = 100A/μs, V <sub>DS</sub> = 100V,  |
| Body Diode Reverse Recovery Charge      | Q <sub>RR</sub>     | —   | 2.9  | —   | μC   | I <sub>F</sub> = 2A   |

- Notes:
- Device mounted on infinite heatsink.
  - Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
  - Guaranteed by design. Not subject to production testing.
  - Short duration pulse test used to minimize self-heating effect.

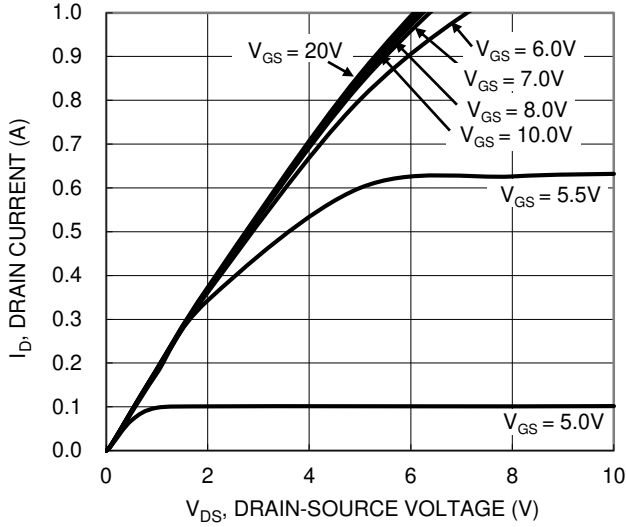


Figure 1. Typical Output Characteristic

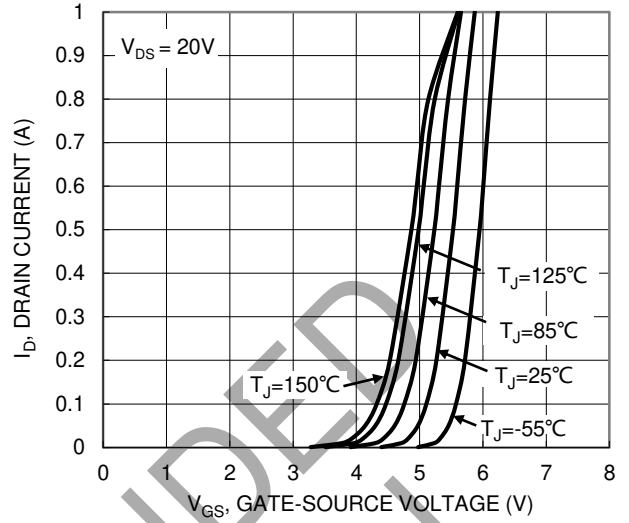


Figure 2. Typical Transfer Characteristic

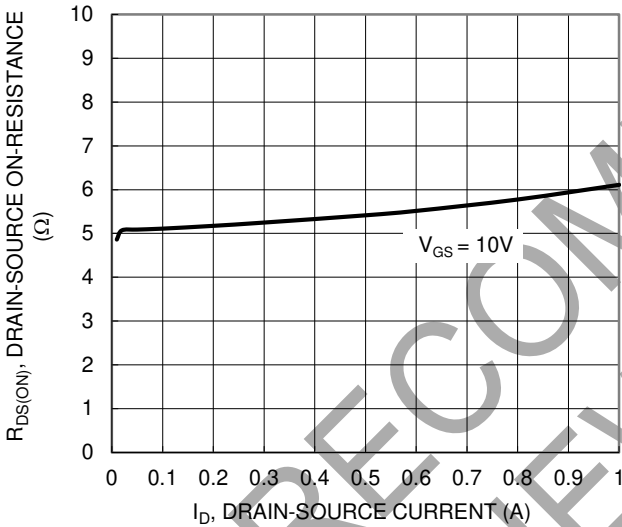


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

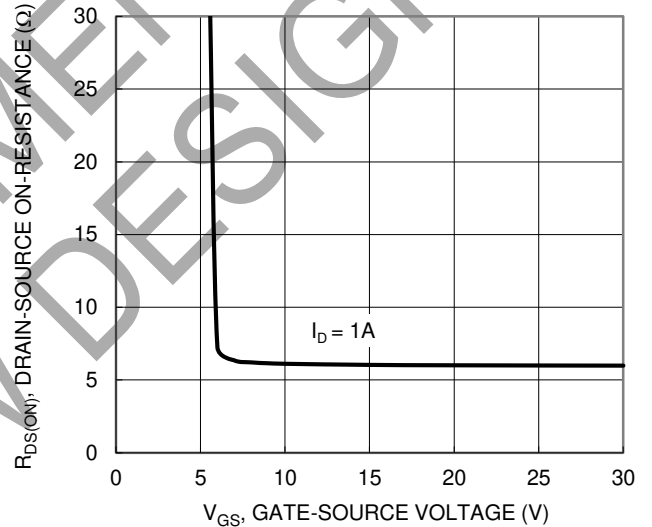


Figure 4. Typical Transfer Characteristic

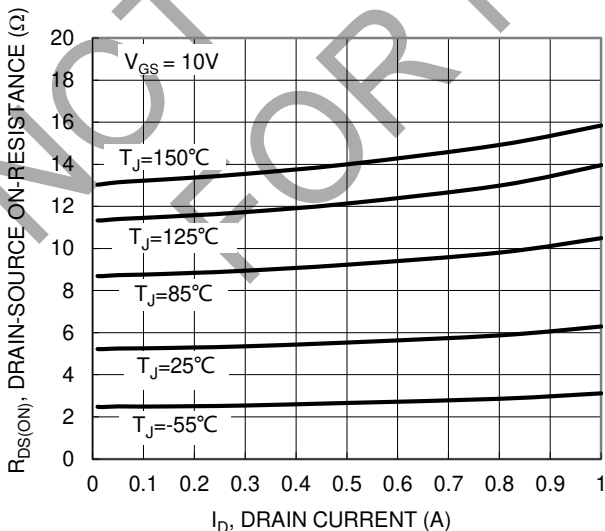


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

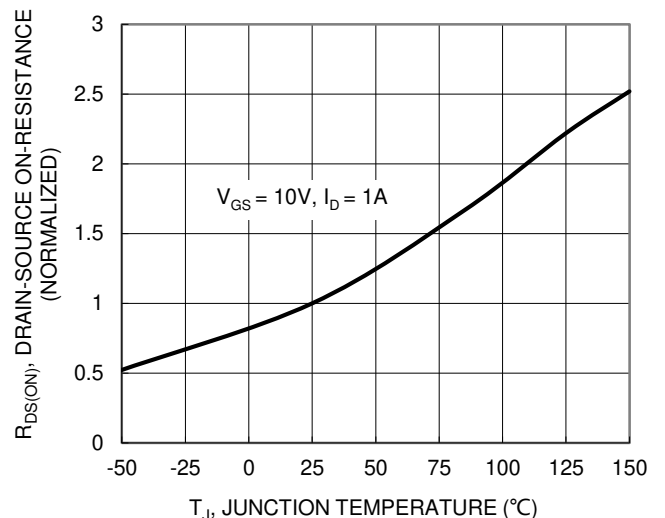


Figure 6. On-Resistance Variation with Temperature

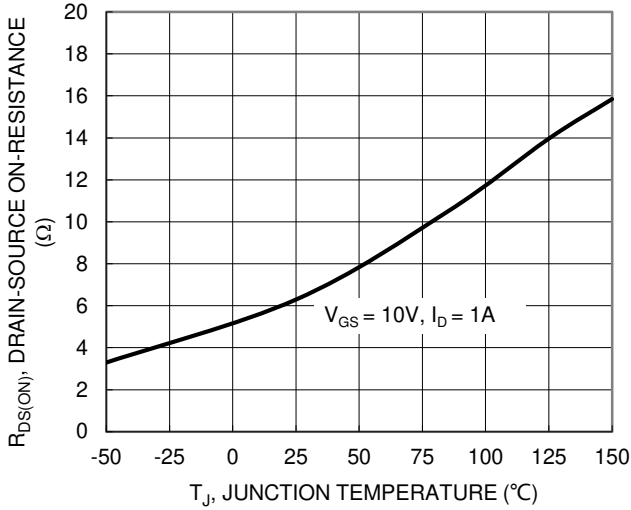


Figure 7. On-Resistance Variation with Temperature

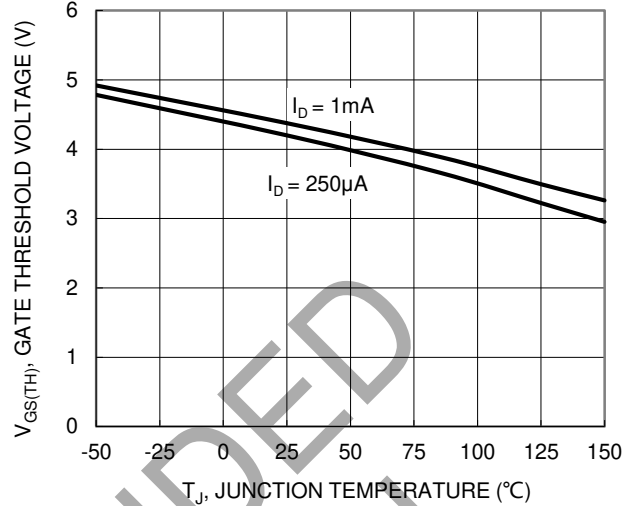


Figure 8. Gate Threshold Variation vs. Junction Temperature

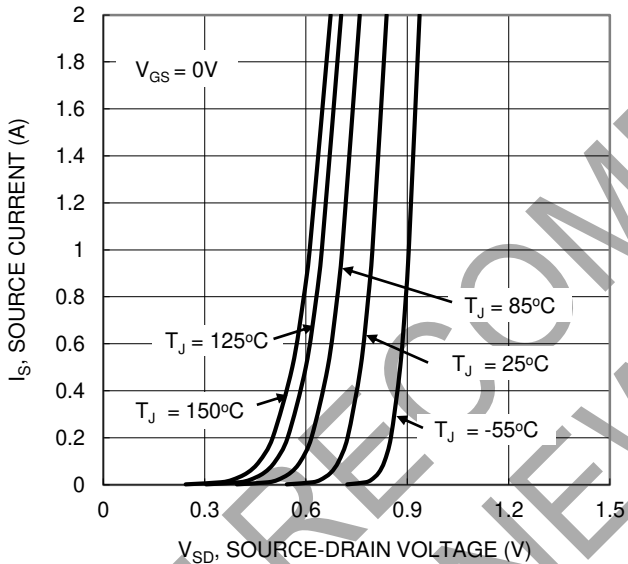


Figure 9. Diode Forward Voltage vs. Current

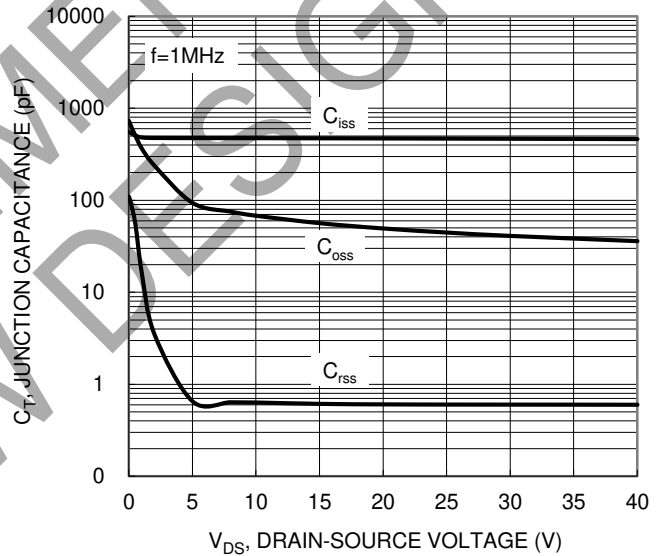


Figure 10. Typical Junction Capacitance

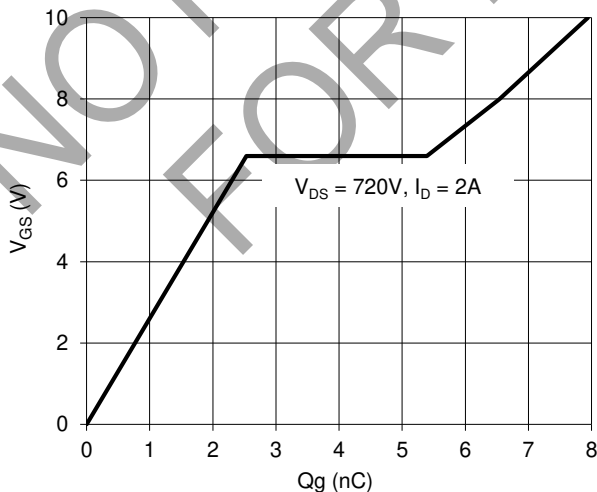


Figure 11. Gate Charge

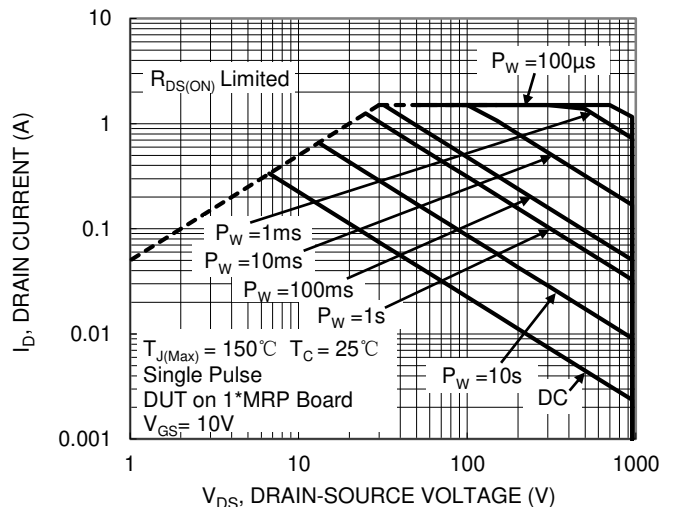


Figure 12. SOA, Safe Operation Area

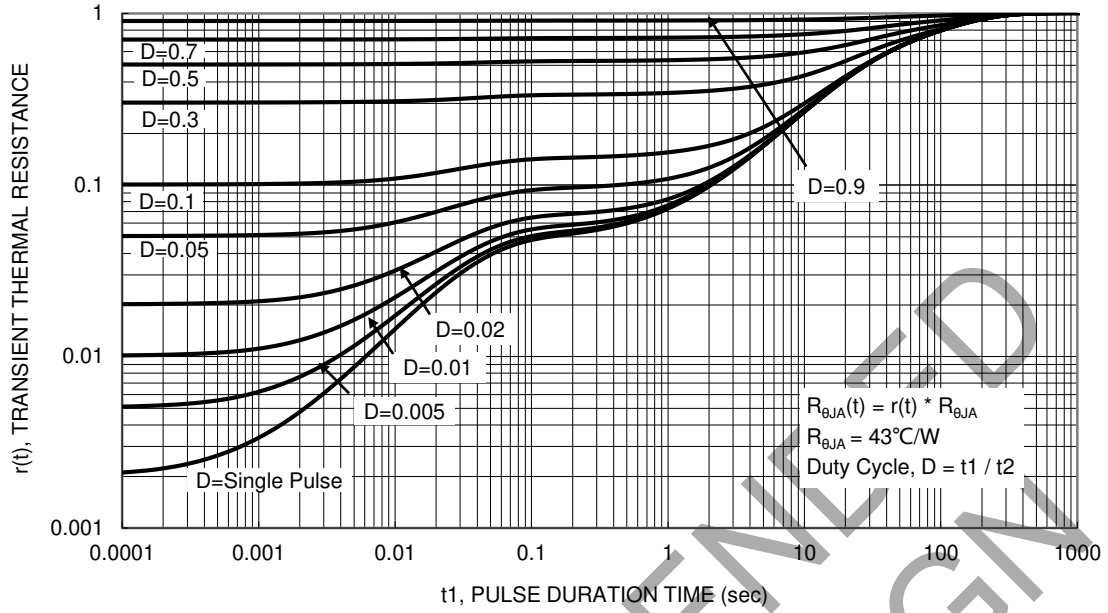


Figure 13. Transient Thermal Resistance

NOT RECOMMENDED FOR NEW DESIGN



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