

Product Summary

| | | |
|-----------------------------|-----------------------------|----------------------------|
| V_{BR} (Min) | I_{PP} (Max) | C_T (Typ) |
| 8.0V | 40A | 200pF |

Description

This new generation TVS is designed to protect sensitive electronics from the damage due to ESD and Surge. The combination of small size and high ESD surge capability makes it ideal for use in portable applications such as cellular phones, digital cameras, and MP3 players.

Applications

- Cellular Handsets
- Portable Electronics
- Computers and Peripheral

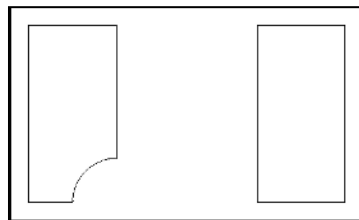
Features

- Low Profile Package (0.50mm Typical) and Ultra-Small PCB Footprint Area (1.1mm × 0.7mm Max) Suitable for Compact Portable Electronics
- One Channel of ESD and Surge Protection
- Provides ESD Protection per IEC 61000-4-2 Standard: Air ±30kV, Contact ±30kV
- Provides Surge and Lightning Protection per IEC 61000-4-5 Standard: I_{PP} Max 40A
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- Halogen and Antimony Free. "Green" Device (Note 3)**
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](mailto:contact@diodes.com) or your local Diodes representative. <https://www.diodes.com/quality/product-definitions/>**

Mechanical Data

- Case: U-DFN1006-2
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (e4)
- Weight: 0.001 grams (Approximate)

U-DFN1006-2 (Type B)



Bottom View



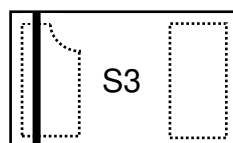
Device Schematic

Ordering Information (Note 4)

| Part Number | Compliance | Marking | Reel Size (inches) | Tape Width (mm) | Quantity |
|---------------|------------|---------|--------------------|-----------------|--------------------|
| D7V9S1U2LP-7B | Standard | S3 | 7 | 8 | 10,000/Tape & Reel |

- Notes:
- No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 - See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 - Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 - For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



S3 or $\bar{S}3$ = Product Type Marking Code
Bar Denotes Pin 1 or Cathode Side

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Value | Unit | Conditions |
|----------------------------------|--------------------------|-------|------|------------------------|
| Peak Pulse Current | I _{PP} | 40 | A | 8/20μs (Note 7) |
| ESD Protection—Contact Discharge | V _{ESD_CONTACT} | ±30 | kV | IEC 61000-4-2 Standard |
| ESD Protection—Air Discharge | V _{ESD_AIR} | ±30 | kV | IEC 61000-4-2 Standard |

Thermal Characteristics

| Characteristic | Symbol | Value | Unit |
|--|-----------------------------------|-------------|------|
| Package Power Dissipation (Note 5) | P _D | 250 | mW |
| Thermal Resistance, Junction to Ambient (Note 5) | R _{θJA} | 500 | °C/W |
| Operating and Storage Temperature Range | T _J , T _{STG} | -55 to +150 | °C |

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Min | Typ | Max | Unit | Test Conditions |
|-------------------------------|------------------|-----|------|------|------|--|
| Reverse Working Voltage | V _{RWM} | — | — | 7.9 | V | — |
| Reverse Current (Note 6) | I _R | — | — | 1.0 | μA | V _R = V _{RWM} |
| Reverse Breakdown Voltage | V _{BR} | 8.0 | — | 9 | V | I _R = 1mA |
| Reverse Clamping Voltage | V _{CL} | — | 9.0 | 9.5 | V | I _{PP} = 10A, t _p = 8/20μs |
| | | — | 12.5 | 13.0 | | I _{PP} = 40A, t _p = 8/20μs |
| ESD Clamping Voltage (Note 8) | V _C | — | 8.6 | — | V | I _{PP} = 4A, t _p = 10/100ns |
| | | — | 9.3 | — | | I _{PP} = 16A, t _p = 10/100ns |
| Capacitance | C _T | — | 200 | 240 | pF | V _R = 0V, f = 1MHz |

- Notes:
- Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes Incorporated's suggested pad layout, which can be found on our website at <http://www.diodes.com/package-outlines.html>.
 - Short duration pulse test used to minimize self-heating effect.
 - Clamping voltage value is based on an 8x20μs peak pulse current (I_{PP}) waveform.
 - Transmission Line Pulse Test (TLP) settings: t_p=100ns, t_r=10ns, I_{TLP} and V_{TLP} averaging window is from 70ns to 90ns.

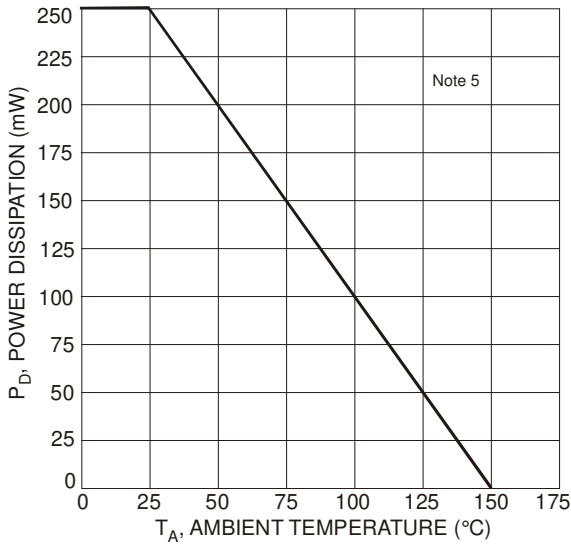


Figure 1 Power Derating Curve

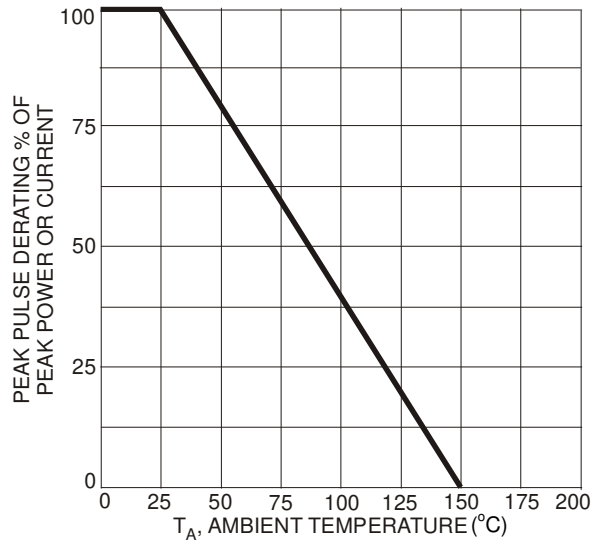


Figure 2 Pulse Derating Curve

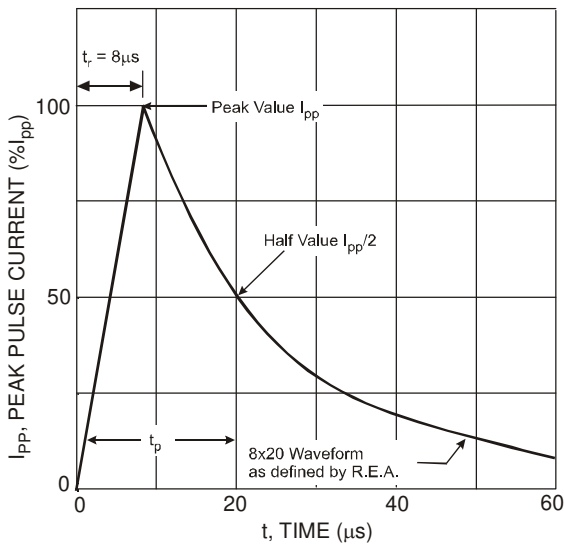


Figure 3 Typical 8 × 20µs Pulse Waveform

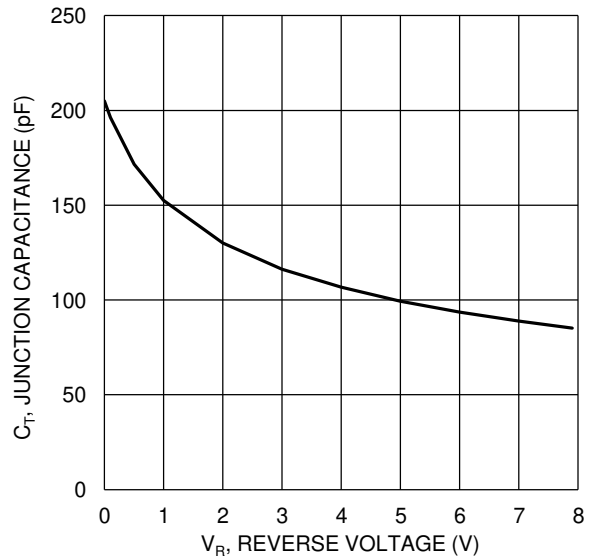


Figure 4 Typical Pin1 to Pin2 Capacitance

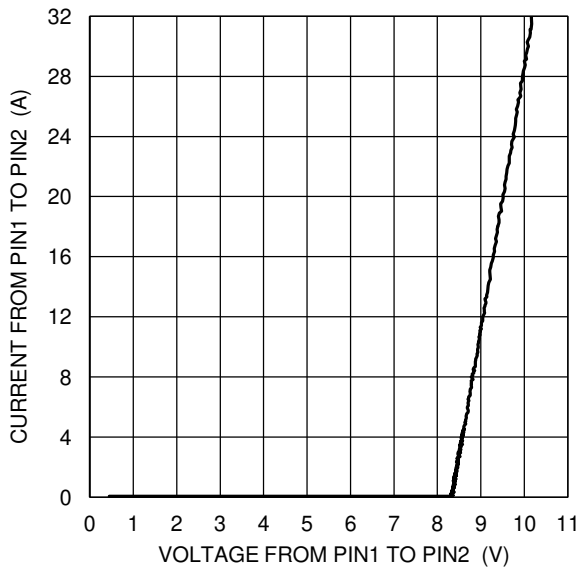


Figure 5 Pin1 to Pin2 TLP Curve

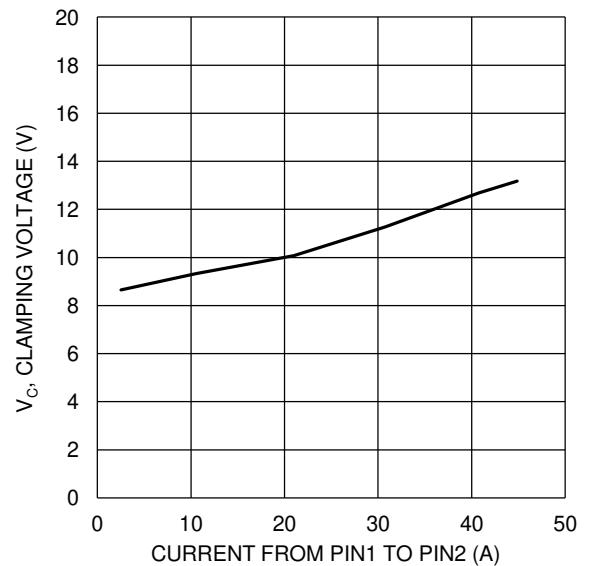
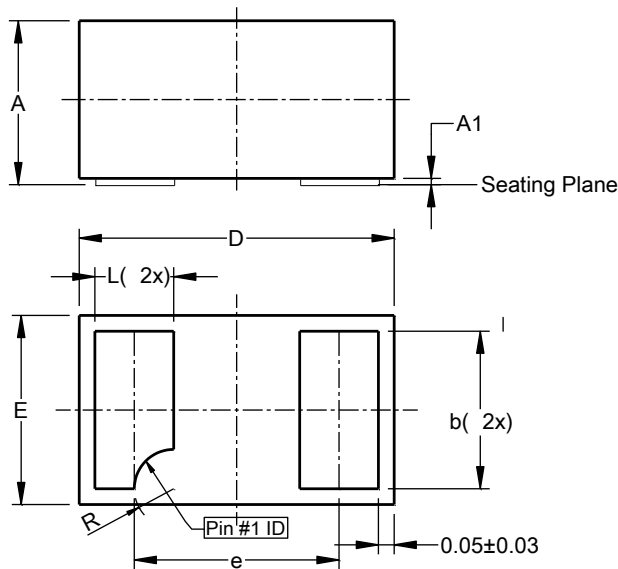


Figure 6 Clamping Voltage Characteristic ($t_p=8/20\mu s$)

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

U-DFN1006-2 (Type B)

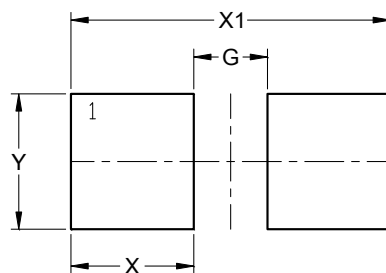


| U-DFN1006-2 (Type B) | | | |
|-------------------------|---------|-------|-------|
| Dim | Min | Max | Typ |
| A | 0.45 | 0.55 | 0.50 |
| A1 | 0.00 | 0.05 | 0.03 |
| b | 0.45 | 0.55 | 0.50 |
| D | 0.95 | 1.05 | 1.00 |
| E | 0.55 | 0.65 | 0.60 |
| e | 0.65BSC | | |
| L | 0.20 | 0.30 | 0.25 |
| R | 0.075 | 0.175 | 0.125 |
| All Dimensions in mm | | | |

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

U-DFN1006-2 (Type B)



| Dimensions | Value (in mm) |
|------------|---------------|
| G | 0.300 |
| X | 0.500 |
| X1 | 1.300 |
| Y1 | 0.550 |

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