



30V P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(on) max}	I _D Τ _A = +25°C
	50mΩ @ V _{GS} = -10V	-3.7A
-30V	60mΩ @ V _{GS} = -4.5V	-3.3A
	85mΩ @ V _{GS} = -2.5V	-2.7A

Features

- Low Input Capacitance
- Low On-Resistance
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP (Note 4)

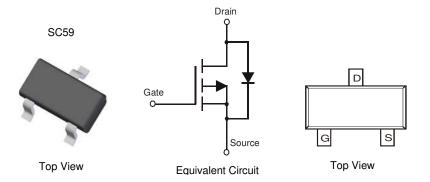
Description and Applications

This new generation Small-Signal enhancement mode MOSFET features low on-resistance and fast switching, making it ideal for highefficiency power management applications.

- Motor Control
- Backlighting
- **DC-DC Converters**
- Power Management Functions

Mechanical Data

- Case: SC-59
- Case Material Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe;
- Solderable per MIL-STD-202, Method 208@3
- Weight: 0.014 grams (Approximate)



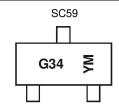
Ordering Information (Note 5)

Part Number	Case	Packaging
DMG3401LSNQ-7	SC59	3,000/Tape & Reel
DMG3401LSNQ-13	SC59	10,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- . Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product_grade_definitions/. 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



G34 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: Y = 2011)M = Month (ex: 9 = September)

Date Code Key

Year	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020	2021	2022
Code	Υ	Z	Α	В	С	D	Е	F	G	Н		J
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage	V_{DSS}	-30	V		
Gate-Source Voltage	V_{GSS}	±12	V		
Continuous Drain Current (Note 6) V _{GS} = -10V	I _D	-3.0 -2.3	Α		
Continuous Drain Current (Note 7) V _{GS} = -10V	I _D	-3.7 -2.9	Α		
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	I _{DM}	-30	Α		
Maximum Body Diode Continuous Current (Note 7)			Is	-1.5	А

Thermal Characteristics

Characteristic		Symbol	Value	Units
Total Power Dissipation	(Note 6)	D	0.8	W
Total Fower Dissipation	(Note 7)	P_{D}	1.2	
Thermal Resistance. Junction to Ambient	(Note 6)	0	159	°C/W
I memai nesistance, junction to Ambient	(Note 7)	$R_{ hetaJA}$	105	
Thermal Resistance, Junction to Case	(Note 7)	$R_{ heta JC}$	36	
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

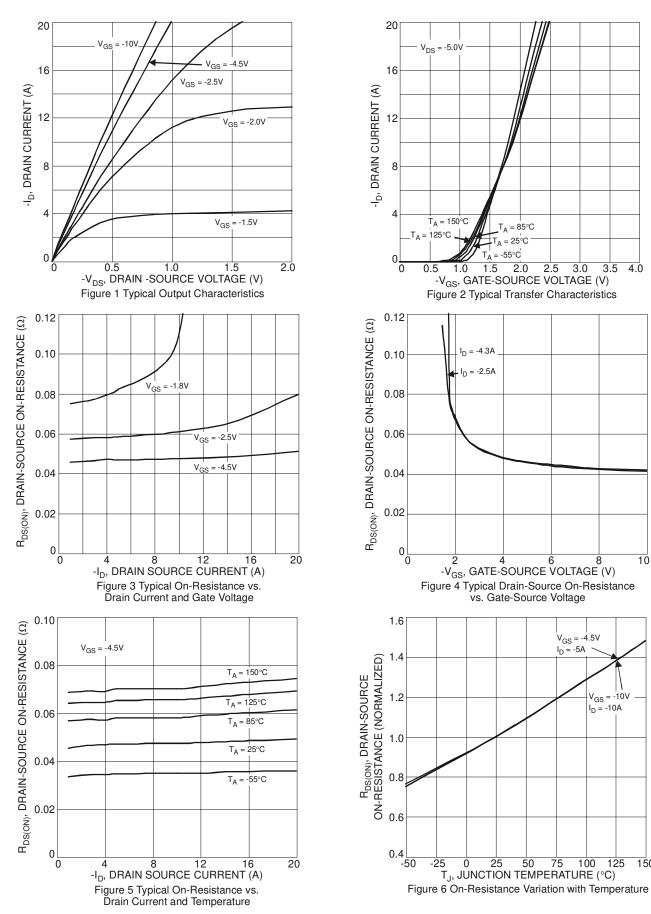
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

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Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	-30	-	-	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	-	-	-1.0	μΑ	$V_{DS} = -30V, V_{GS} = 0V$	
Gate-Body Leakage	I _{GSS}	-	-	±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)			_			•	
Gate Threshold Voltage	V _{GS(th)}	-0.5	-1.0	-1.3	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
		-	41	50		$V_{GS} = -10V, I_D = -4A$	
Static Drain-Source On-Resistance	R _{DS} (ON)	-	47	60	mΩ	$V_{GS} = -4.5V$, $I_D = -3.5A$	
		-	60	85		$V_{GS} = -2.5V, I_D = -2.5A$	
Forward Transfer Admittance	Y _{fs}	-	12	-	S	$V_{DS} = -5V, I_{D} = -4A$	
Diode Forward Voltage	V_{SD}	-	-0.8	-1.0	V	$V_{GS} = 0V, I_S = -1A$	
DYNAMIC CHARACTERISTICS (Note 9)			_			•	
Input Capacitance	C _{iss}	-	1,326	-			
Output Capacitance	Coss	-	103	-	pF	$V_{DS} = -15V$, $V_{GS} = 0V$, $f = 1.0MHz$	
Reverse Transfer Capacitance	C _{rss}	-	71	-			
Gate Resistance	R_{g}	-	7.3	-	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V _{GS} = -4.5V)	Qg	-	11.6	-			
Total Gate Charge (V _{GS} = -10V)	Qg	-	25.1	-	nC	157 1 40	
Gate-Source Charge	Q _{gs}	-	2	-	nc nc	$V_{DD} = -15V, I_{D} = -4A$	
Gate-Drain Charge	Q _{gd}	-	1.7	-	1		
Turn-On Delay Time	t _{D(on)}	-	8	-			
Turn-On Rise Time	t _r	-	13	-	20	$V_{DS} = -15V, V_{GS} = -10V,$	
Turn-Off Delay Time	t _{D(off)}	-	71	-	nS	$R_{GEN} = 6\Omega$, $R_L = 3.75\Omega$	
Turn-Off Fall Time	t _f	-	38	-			

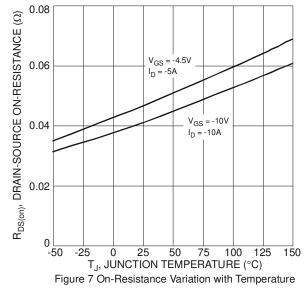
Notes:

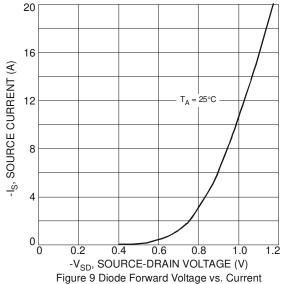
- 6. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
 7. Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper pad layout
- 8 .Short duration pulse test used to minimize self-heating effect.
- 9. Guaranteed by design. Not subject to production testing.

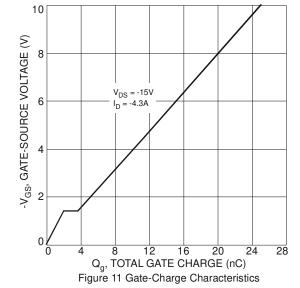












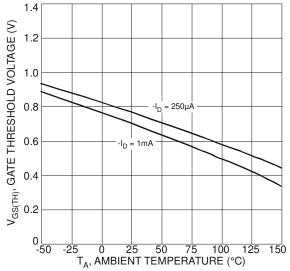
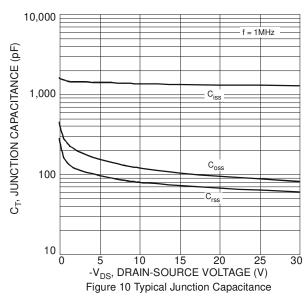


Figure 8 Gate Threshold Variation vs. Ambient Temperature



100

RDS(n)

PW = 10µS

Limited

Limited

PW = 10µS

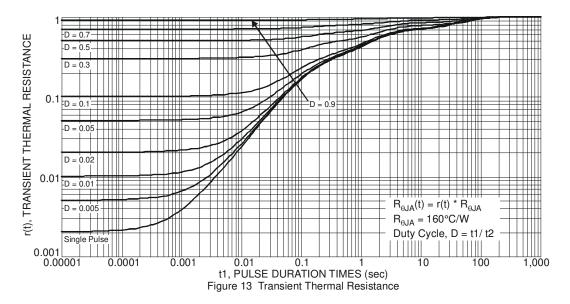
PW = 10mS

PW = 100mS

PW = 100µS

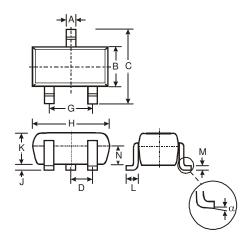
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Package Outline Dimensions

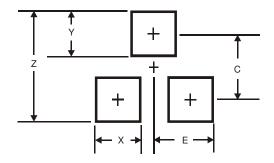
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



SC59						
Dim	Min	Max	Тур			
Α	0.35	0.50	0.38			
В	1.50	1.70	1.60			
С	2.70	3.00	2.80			
D	-	-	0.95			
G	-	-	1.90			
Н	2.90	3.10	3.00			
J	0.013	0.10	0.05			
K	1.00	1.30	1.10			
L	0.35	0.55	0.40			
М	0.10	0.20	0.15			
N	0.70	0.80	0.75			
α	0°	8°	-			
All Dimensions in mm						

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	3.4
Х	0.8
Υ	1.0
С	2.4
E	1.35



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