

TYPE SN75433

DUAL PERIPHERAL POSITIVE-OR DRIVER

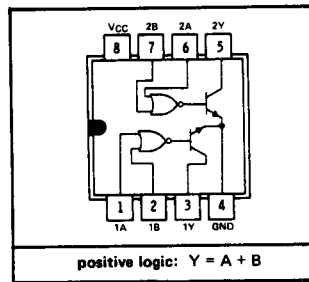
logic

**FUNCTION TABLE
(EACH DRIVER)**

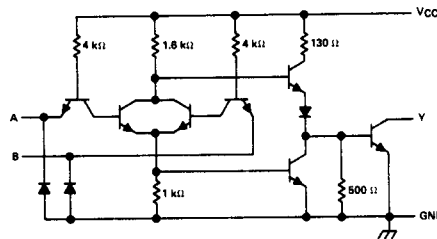
A	B	Y
L	L	L (on state)
L	H	H (off state)
H	L	H (off state)
H	H	H (off state)

H = high level, L = low level

**JG OR P
DUAL-IN-LINE PACKAGE (TOP VIEW)**



schematic (each driver)



Resistor values shown are nominal.

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS	MIN	TYP [‡]	MAX	UNIT	
V _{IH} High-level input voltage		2			V	
V _{IL} Low-level input voltage			0.8		V	
V _{IK} Input clamp voltage	V _{CC} = 4.75 V, I _I = -12 mA			-1.5	V	
I _{OH} High-level output current	V _{CC} = 4.75 V, V _{IH} = 2 V, V _{OH} = 15 V			100	μA	
V _{OL} Low-level output voltage	V _{CC} = 4.75 V, V _{IL} = 0.8 V, I _{OL} = 100 mA		0.25	0.4	V	
	V _{CC} = 4.75 V, V _{IL} = 0.8 V, I _{OL} = 300 mA		0.5	0.7		
I _I Input current at maximum input voltage	V _{CC} = 5.25 V, V _I = 5.5 V			1	mA	
I _{IH} High-level input current	V _{CC} = 5.25 V, V _I = 2.4 V			40	μA	
I _{IL} Low-level input current	V _{CC} = 5.25 V, V _I = 0.4 V			-1	-1.6	mA
I _{CC} H Supply current, outputs high	V _{CC} = 5.25 V, V _I = 5 V		8	11	mA	
I _{CC} L Supply current, outputs low	V _{CC} = 5.25 V, V _I = 0		54	68	mA	

[‡]All typical values are at V_{CC} = 5 V, T_A = 25°C.

switching characteristics, V_{CC} = 5 V, T_A = 25°C

PARAMETER	TEST FIGURE	TEST CONDITIONS	MIN	TYP	MAX	UNIT
t _{PLH} Propagation delay time, low-to-high-level output	3	I _O ≈ 100 mA, C _L = 15 pF, R _L = 50 Ω		10	20	ns
t _{PHL} Propagation delay time, high-to-low-level output				15	25	ns
t _{TLH} Transition time, low-to-high-level output				3	8	ns
t _{THL} Transition time, high-to-low-level output				9	12	ns
V _{OH} High-level output voltage after switching	4	V _S = 15 V, I _O ≈ 150 mA		V _S - 10		mV