Spec. No.: FCC-K-HTS-0003 /13

Date: 2017. 1. 10

# Specification

Title: CHIP FUSE; RECTANGULAR TYPE

Style: FCC10,16,20,32, FHC10,16,20,32

RoHS COMPLIANCE ITEM
Halogen and Antimony Free

Product specification contained in this specification are subject to change at any time without notice If you have any questions or a Purchasing Specification for any quality Agreement is necessary, please contact our sales staff.



Hokkaido Research Center Approval by: T. Sannomiya Drawing by: M. Shibuya

Note: Stock conditions

Temperature:  $+5^{\circ}\text{C} \sim +35^{\circ}\text{C}$ Relative humidity:  $25\% \sim 75\%$ 

The period of guarantee: Within 2 year from shipmen t by the company.

Solderability shall be satisfied.

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#### 1. Scope

1.1 This specification covers the detail requirements for chip fuses; rectangular type, style of FCC10,16,20,32, FHC10,16,20,32.

# 1.2 Applicable documents

UL248-1-2000 Low-Voltage Fuses-Part1: General Requirements

UL248-14-2000 Low-Voltage Fuses-Part14: Supplemental Fuses

CSA C22.2 No.248.1–2000 Low-Voltage Fuses-Part1: General Requirements

CSA C22.2 No.248.14-2000 Low-Voltage Fuses-Part14: Supplemental Fuses

IEC60127-1 Miniature fuses-part 1: Definitions for miniature fuses and general requirements for miniature fuse-links

IEC60127-4 Miniature fuses-Part4: Universal modular fuse-links (UMF)

# 2. Classification

Type designation shall be the following form.

(Example

1 Chip fuses; rectangular type \_\_\_\_\_\_\_ Style

2 Size

3 Rated current

4 Optional code

Symbol	Content			
AB	Standard			
AD	Stariuaru			

5 Packaging form

В	Bulk (loose package)
PA	Press pocket taping
TP	Paper taping

# 3. Safety standard approval

- UL248-1 and UL248-14
- CSA C22.2, No. 248.1–00 and CSA C22.2, No. 248.14–00

The file number to be designated by UL and C-UL shall be as follows: E176847

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# 4. Rating

The ratings shall be in accordance with Table-1.

# 4.1 Optional code: AB

Table-1(1)

	Rated current		Internal resistance value	Rated	Breaking	Time / cui	rrent characteristic	
Style	Symbol	(A)	Marking symbol	(m $\Omega$ max.)	voltage (V)	capacity (A)	Current	Pre-arcing time
	201	0.2	Z	2400				
F0040	251	0.25	С	1000				
	321	0.315	D	750				
	401	0.4	E	620				
	501	0.5	F	340				
	631	0.63	I	290	D000			
FCC10	751	0.75	A	220	DC30	0.5	0000/	_
	801	0.8	K	210		35	200%	5 s max.
	102	1.0	L	150				
	132	1.25	М	120				
	152	1.5	Н	100	DC24			
	162	1.6	N	90				
E 1040	202	2.0	S	55				
FHC10	252	2.5	Т	40	DC24			
	201	0.2	ZB	3,200		35		
	251	0.25	CB	1,800				
	321	0.315	DB	1,000				5 s max.
	401	0.4	EB	750			200%	
	501	0.5	FB	330				
	631	0.63	IB	280				
FCC16	751	0.75	AB	210	DC36			
	801	8.0	KB	200				
	102	1.0	LB	130				
	132	1.25	MB	110				
	152	1.5	HB	95				
	162	1.6	NB	85				
	202	2.0	SB	70				
FHC16	252	2.5	TB	40	DC32			
	501	0.5	FB	330				
	631	0.63	IB	270				
	801	0.8	KB	190				
FCC20	102	1.0	LB	130	DC50	50	200%	5 s max.
	132	1.25	MB	100		30	200%	Jamas.
	162	1.6	NB	80				
	202	2.0	SB	65				
FHC20	252	2.5	TB	40	DC32		]	

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# 4.2 Optional code: AD

Table-1(2)

Ch do	R	ated curre	nt	Internal resistance value	Rated voltage	Breaking	Time / cu	rrent characteristic
Style	Symbol	(A)	Marking symbol	(mΩ max.)	(V)	capacity (A)	Current	Pre-arcing time
	151	0.15	0	2700	DC32			
FCC10	201	0.2	Z	1000				
	251	0.25	С	750				
	321	0.315	D	620				
	401	0.4	Е	340				
	501	0.5	F	290	DC30			
	631	0.63	I	210		35	250%	5 s max.
	801	8.0	K	150		35	25070	J 3 IIIax.
	102	1.0	L	120				
	132	1.25	M	90				
	162	1.6	N	55				
FHC10	202	2.0	S	40	DC24			
111010	252	2.5	Т	36	DC24			
	322	3.15	U	26				
	151	0.15	OD	4000	DC50 DC36	35		5 s max.
	201	0.2	ZD	1800				
	251	0.25	CD	1000				
	321	0.315	DD	750				
	401	0.4	ED	330			250%	
	501	0.5	FD	280				
FCC16	631	0.63	ID	200				
	801	0.8	KD	130				
	102	1.0	LD	110				
	132	1.25	MD	85				
	162	1.6	ND	70				
	202	2.0	SD	55				
	252	2.5	TD	45	DC32			
FHC16	322	3.15	UD	26	DC24			
111010	402	4.0	XD	19	D024			
	401	0.4	401	330				
	501	0.5	501	270				
	631	0.63	631	190				
	801	8.0	801	130				
FCC20	102	1.0	102	100	DC50			
	132	1.25	132	80		50	250%	5 s max.
	162	1.6	162	65			20070	o o max.
	202	2.0	202	55				
	252	2.5	252	40				
	322	3.15	UD	26	DC32			
FHC20	402	4.0	XD	19				
	502	5.0	YD	14	DC24			

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Table-1(3)

Style	Style Rated current Symbol (A) Marking symbol		Internal Rated resistance value voltage		Breaking	Time / current characteristic			
Style			(mΩ max.)	(V)	capacity (A)	Current	Pre-arcing time		
	201	0.2	201	1800				5 s max.	
	251	0.25	251	1000					
	321	0.315	321	750					
	401	0.4	401	350					
	501	0.5	501	295			250%		
	631	0.63	631	200					
FCC32	801	8.0	801	140	140 DC64				
	102	1.0	102	110		50			
	132	1.25	132	85		30		25070	J S I I I dx.
	152	1.5	152	78					
	162	1.6	162	75					
	202	2.0	202	65					
	252	2.5	252	45					
	322	3.15	UD	26					
FHC32	402	4.0	XD	19	DC32				
	502	5.0	YD	14					

<sup>4.3</sup> Working temperature range: -55 to +125(°C)

# 5. Packaging form

The standard packaging form shall be in accordance with Table-2.

# Table-2

Symbol	Pac	kaging form	Standard packaging quantity / units	Application
В	Bulk (loose package)		1,000 pcs.	FCC10,16,20,32, FHC10,16,20,32
PA	Press pocket taping (paper taping)	8mm width, 2mm pitches	10,000 pcs.	FCC10, FHC10
TP	Paper taping	8mm width, 4mm pitches	5,000 pcs.	FCC16,20,32, FHC16,20,32

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Drawing No:

# 6. Dimensions

6.1 The resistor shall be of the design and physical dimensions in accordance with Figure-1 and Table-3.

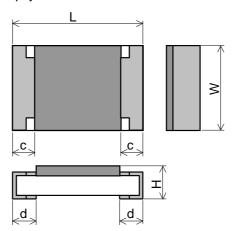


Figure-1

		Unit:mm			
Style	L	W	Н	С	d
FCC10, FHC10	1.0±0.05	0.5±0.05	0.4±0.05	0.2±0.1	0.25±0.10
FCC16, FHC16	1.6±0.1	0.8 +0.15 -0.05	0.45±0.10	0.3±0.15	0.3±0.1
FCC20, FHC20	2.0±0.1	1.25±0.10	0.6±0.1	0.4±0.2	0.4±0.2
FCC32	3.2±0.2	1.6±0.15	0.6±0.1	0.5±0.25	0.5±0.25
FHC32	3.2±0.2	1.0±0.15	0.65±0.10	0.5±0.25	0.5±0.25

# 6.2 Net weight (Reference)

Style	Net weight(mg)
FCC10, FHC10	0.8
FCC16, FHC16	2
FCC20, FHC20	6
FCC32	10
FHC32	11

# 7. Marking

The Marking symbol of Sub-clause 4.1 shall be marked on over coat side.

# (Example)

Style	Optional code	Marking symbol	Content
FCC10		N	FCC10 162 AB
FHC10		T	FHC10 252 AB
FCC16	AB	SB	FCC16 202 AB
FHC16	٨٥	TB	FHC16 252 AB
FCC20		SB	FCC20 202 AB
FHC20		TB	FHC20 252 AB
FCC10		N	FCC10 162 AD
FHC10		U	FHC10 322 AD
FCC16		ND	FCC16 162 AD
FHC16	AD	UD	FHC16 322 AD
FCC20	٨٥	162	FCC20 162 AD
FHC20		UD	FHC20 322 AD
FCC32		162	FCC32 162 AD
FHC32		UD	FHC32 322 AD

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#### 8. Performance

8.1 Unless otherwise specified, the standard range of atmospheric conditions for tests is as follows;

Ambient temperature: 5 °C to 35 °C, Relative humidity: 45 % to 85 %, Air presser: 86 kPa to 106 kPa

If there is any doubt the results, measurements shall be made within the following:

Ambient temperature: 20 °C  $\pm$  2 °C, Relative humidity: 60 % to 70 %, Air presser: 86 kPa to 106 kPa

8.2 The performance shall be satisfied in Table-4.

Table 4(1)

		1aDI <del>C 4</del> (1)			
No.	Test items	Condition of test			quirements
1	Temperature rise	The fuse shall be mounted on the test substrate as shown in Figure–2.  Measurement temp.: 10 °C to 30 °C	75 °C ma	ıx.	
		Test current: Rated current			
		The temperature at the hottest point on the surface of			
		the fuse shall be measured after temperature			
		equilibrium has been attained.			
2	Current carrying capacity	The fuse shall be mounted on the test substrate as	Without o	pening	
		shown in Figure–2.			
		Test current: 110 % of Rated current			
		Test temp.: 70 °C ± 2 °C			
3	Time / current characteristic	Test period: 1h  The fuse shall be mounted on the test substrate as	Optional	Current	Pre-arcing
3	Time / carrent characteristic	shown in Figure—2.	code	Curicin	time
ļ		Test current shall be applied for continuously.	AB	200%	5 s max.
		,	AD	250%	5 s max.
				I	
4	Terminal bond strength of	JIS C 60068-2-21 Ue1	Change of	of internal r	esistance:
	the face plating	The fuse shall be mounted on the test substrate as	±3%		
		shown in Figure–2.		dence o	f mechanical
		Bending value: 3 mm (Among the fulcrums: 90 mm)	damage.		
5	Desistance to coldering	Duration: 10 s ± 1 s	Change	of internal r	aniatanan
5	Resistance to soldering heat	Test by a piece. Temp. of solder bath: 260 °C ± 5 °C	±10%	of internal r	esisiai ice.
ļ	Heat	Immersion time: 10 s ± 1 s	No evid	dence of	appearance
		After immersion into solder, leaving the room temp.	damage	301100 01	арреалинос
ļ		for 1h or more, and then measure the internal			
		resistance.			
		Reflow soldering			
		Pre-heating: 150 °C ~ 180 °C, 120 s max.			
ļ		Peak: 260 °C ± 5 °C, 10 s max.			
		Refrow cycle: 2 times			
		After immersion into solder, leaving the room temp.			
		for 1h or more, and then measure the internal			
	0.11.12%	resistance.	T		1. 1. "
6	Solderability	JIS C 60068-2-58			al immersed shall ered with a new
		Test by a piece Flux: Rosin–Methanol	coating of		Cica will a liew
ļ		Temp. of solder: bath: 235 °C ± 5 °C	3 01		
		Immersion time: 2 s ± 0.5 s			
		1111110101011 til 110. 2 0 ± 0.0 3			

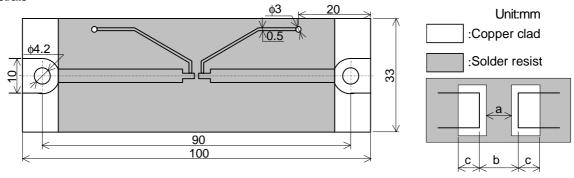
CHIP FUSES; RECTANGULAR TYPE

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Table-4(2)

No.	Test items	Condition of test	Performance requirements
7	Rapid change temperature	JIS C 60068-2-14 Na	Change of internal resistance:
		The fuse shall be mounted on the test substrate as	±10%
		shown in Figure–2.	No evidence of appearance
		Lower temperature: –55 °C	damage
		Upper temperature: +125 °C	
		Duration of exposure at each temperature: 30 min.	
		Number of cycles: 5 cycles	
8	Endurance test	The fuse shall be mounted on the test substrate as	The voltage drop across the fuse
		shown in Figure–2.	after the test shall not have
		Test condition: Nominal ambient temp. and Relative	increased by more than 10 % of
		humidity.	the value measured before test.
		Test potential:	
		1. Cycle of 1 h "ON" and 15 min. "OFF" at 1.05 times	
		rated current for 100 cycles.	
		2. After above the test, 1.25 times rated current for	
		1h.	

#### 9. Test substrate



Style	Α	b	С
FCC10, FHC10	0.3	0.6	0.65
FCC16, FHC16	0.6	1.0	0.5
FCC20, FHC20	0.9	1.3	0.7
FCC32, FHC32	1.8	2.2	0.85

Figure-2 FCC, FHC TEST SUBSTRATE

Remark 1). Material: Epoxide woven glass

Thickness: 1. 6mm Thickness of copper clad: 0. 035mm

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Drawing No:

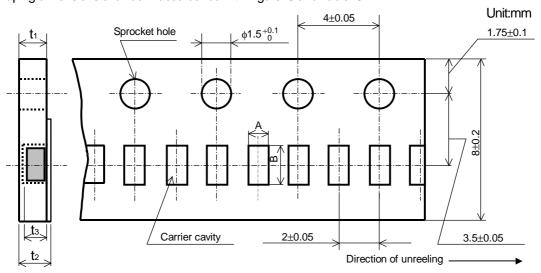
#### 10. Taping

10.1 Applicable documents JIS C 0806-3: 2014, EIAJ ET-7200C: 2010

#### 10.2 Taping dimensions

10.2.1 Press pocket taping(8mm width, 2mm pitches)

Taping dimensions shall be in accordance with Figure-3 and Table-5.



 Figure—3

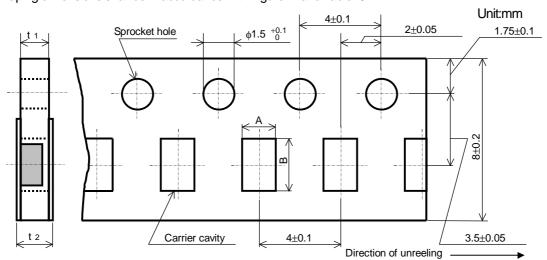
 Table—5
 Unit:mm

 Style
 A
 B
 t<sub>1</sub>
 t<sub>2</sub>
 t<sub>3</sub>

 FCC10, FHC10
 0.65±0.1
 1.15±0.1
 0.6±0.05
 0.7max.
 0.5±0.05

# 10.2.2 Paper taping (8mm width, 4mm pitches)

Taping dimensions shall be in accordance with Figure-4 and Table-6.



Figure\_4

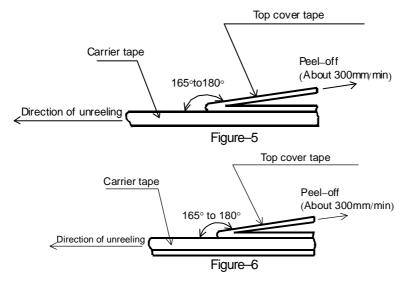
Table-6			Unit:mm	
Style	Α	В	t 1	<b>t</b> 2
FCC16, FHC16	1.15±0.15	1.9±0.2	0.6±0.1	0.8 max.
FCC20, FHC20	1.65±0.15	2.5±0.2	0.8±0.1	1.0 max.
FCC32, FHC32	2.0±0.15	3.6±0.2	0.0±0.1	1.0 max.

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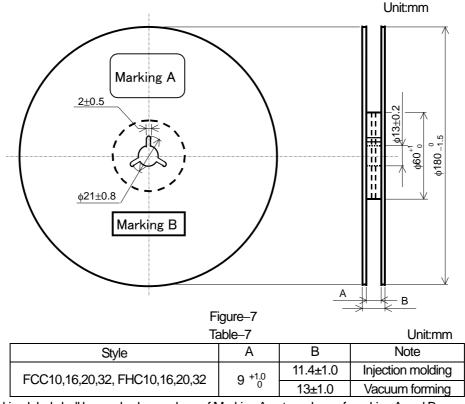
- 1). The cover tapes shall not cover the sprocket holes.
- 2). Tapes in adjacent layers shall not stick together in the packing.
- 3). Components shall not stick to the carrier tape or to the cover tape.
- 4). Pitch tolerance over any 10 pitches ±0.2mm.
- 5). The peel strength of the top cover tape shall be with in 0.1N to 0.5N on the test method as shown in the following FCC10, FHC10:Figure–5,FCC16,20,32, FHC16,20,32 : Figure–6.
- 6). When the tape is bent with the minimum radius for 25 mm, the tape shall not be damaged and the components shall maintain their position and orientation in the tape.
- In no case shall there be two or more consecutive components missing.
   The maximum number of missing components shall be one or 0.1%, whichever is greater.
- 8). The fuses shall be faced to upward at the over coating side in the carrier cavity.



FCC10,16,20,32, FHC10,16,20,32 Page: 10/10

#### 10.3 Reel dimension

Reel dimensions shall be in accordance with the following Figure–7 and Table–7. Plastic reel (Based on EIAJ ET–7200C)



Note: Marking label shall be marked on a place of Marking A or two place of marking A and B.

# 10.4 Leader and trailer tape.

(Example)

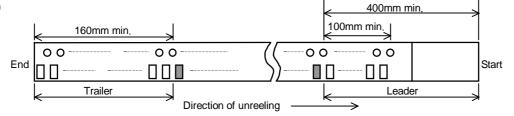


Figure-8

# 11. Marking on package

The label of a minimum package shall be legibly marked with follows.

# 11.1 Marking A

- (1) Classification (Style, Rated current, Optional code, Packaging form) (2) Quantity (3) Lot number
- (5) Manufacturer's name or trade mark (6) UL and /or C–UL recognized component mark (7) Others 11.2 Marking B (KAMAYA Control label)

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# 12. Recommended Derating for Rated Current

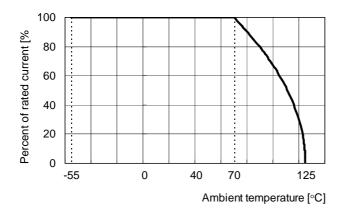
Nominal Derating

Option Code AB: Nominal Derating ≤ 70% of Rated Current

Option Code AD: Nominal Derating ≤ 80% of Rated Current

•Temperature Derating

Please refer to the following graph regarding the current derating value for ambient temperature.



Ex.) • If Optional code: AB (Rated Current: 1.0A) is used under ambient temperature 70°C Kamaya recommends, less than the current value derated as below,

Rated Current: 1.0A× (Nominal Derating: 70% × Temperature Derating: 100%) =0.7A

 If Optional code: AD (Rated Current: 1.0A) is used under ambient temperature 70°C Kamaya recommends, less than the current value derated as below,
 Rated Current: 1.0A× (Nominal Derating: 80% × Temperature Derating: 100%) =0.8A