

## 1. General description

Planar passivated sensitive gate four quadrant triac in a TO92 plastic package. This sensitive gate "series E" triac is intended for interfacing with low power drivers including microcontrollers.

## 2. Features and benefits

- Direct interfacing to logic level ICs
- Direct interfacing with low power gate drivers and microcontrollers
- High blocking voltage capability
- Planar passivated for voltage ruggedness and reliability
- Sensitive gate in four quadrants
- Triggering in all four quadrants

## 3. Applications

- Air conditioner indoor fan control
- General purpose low power motor control
- General purpose switching and phase control

## 4. Quick reference data

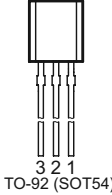
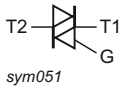
Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Absolute maximum rating</b>						
$V_{DRM}$	repetitive peak off-state voltage		-	-	600	V
$I_{T(RMS)}$	RMS on-state current	full sine wave; $T_{lead} \leq 51.2 \text{ }^\circ\text{C}$ ; <a href="#">Fig. 1</a> ; <a href="#">Fig. 2</a> ; <a href="#">Fig. 3</a>	-	-	1	A
$I_{TSM}$	non-repetitive peak on-state current	full sine wave; $T_{j(init)} = 25 \text{ }^\circ\text{C}$ ; $t_p = 20 \text{ ms}$ ; <a href="#">Fig. 4</a> ; <a href="#">Fig. 5</a>	-	-	12.5	A
		full sine wave; $T_{j(init)} = 25 \text{ }^\circ\text{C}$ ; $t_p = 16.7 \text{ ms}$	-	-	13.7	A
$T_j$	junction temperature		-	-	125	$^\circ\text{C}$
<b>Static characteristics</b>						
$I_{GT}$	gate trigger current	$V_D = 12 \text{ V}$ ; $I_T = 0.1 \text{ A}$ ; T2+ G+; $T_j = 25 \text{ }^\circ\text{C}$ ; <a href="#">Fig. 7</a>	-	-	10	mA
		$V_D = 12 \text{ V}$ ; $I_T = 0.1 \text{ A}$ ; T2+ G-; $T_j = 25 \text{ }^\circ\text{C}$ ; <a href="#">Fig. 7</a>	-	-	10	mA
		$V_D = 12 \text{ V}$ ; $I_T = 0.1 \text{ A}$ ; T2- G-; $T_j = 25 \text{ }^\circ\text{C}$ ; <a href="#">Fig. 7</a>	-	-	10	mA
		$V_D = 12 \text{ V}$ ; $I_T = 0.1 \text{ A}$ ; T2- G+; $T_j = 25 \text{ }^\circ\text{C}$ ; <a href="#">Fig. 7</a>	-	-	10	mA

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$I_H$	holding current	$V_D = 12\text{ V}$ ; $T_j = 25\text{ °C}$ ; <a href="#">Fig. 9</a>	-	1.3	10	mA
$V_T$	on-state voltage	$I_T = 1.4\text{ A}$ ; $T_j = 25\text{ °C}$ ; <a href="#">Fig. 10</a>	-	1.2	1.5	V
<b>Dynamic characteristics</b>						
$dV_D/dt$	rate of rise of off-state voltage	$V_{DM} = 402\text{ V}$ ; $T_j = 125\text{ °C}$ ; ( $V_{DM} = 67\%$ of $V_{DRM}$ ); exponential waveform; $R_{GT1(ext)} = 1\text{ k}\Omega$	50	-	-	V/ $\mu$ s
$dV_{com}/dt$	rate of change of commutating voltage	$V_D = 400\text{ V}$ ; $T_j = 125\text{ °C}$ ; $dI_{com}/dt = 0.5\text{ A/ms}$ ; $I_T = 1\text{ A}$ ; gate open circuit	5	-	-	V/ $\mu$ s

## 5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	T2	main terminal 2	 <p>TO-92 (SOT54)</p>	 <p>sym051</p>
2	G	gate		
3	T1	main terminal 1		

## 6. Ordering information

Table 3. Ordering information

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
BT131-600E	TO92	BT131-600E,412	Bulk	1000	SOT54	14-Nov-2013
BT131-600E	TO92	BT131-600EQP	Reel	2000	SOT54	14-Nov-2013

## 7. Marking

Table 4. Marking codes

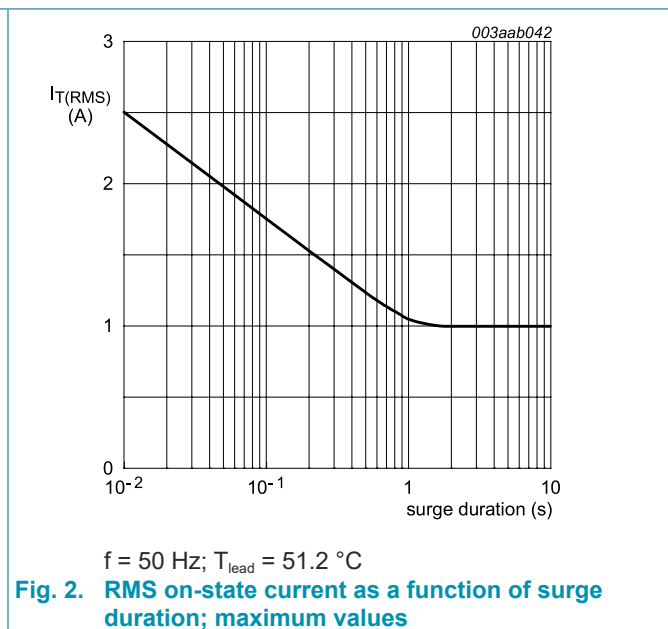
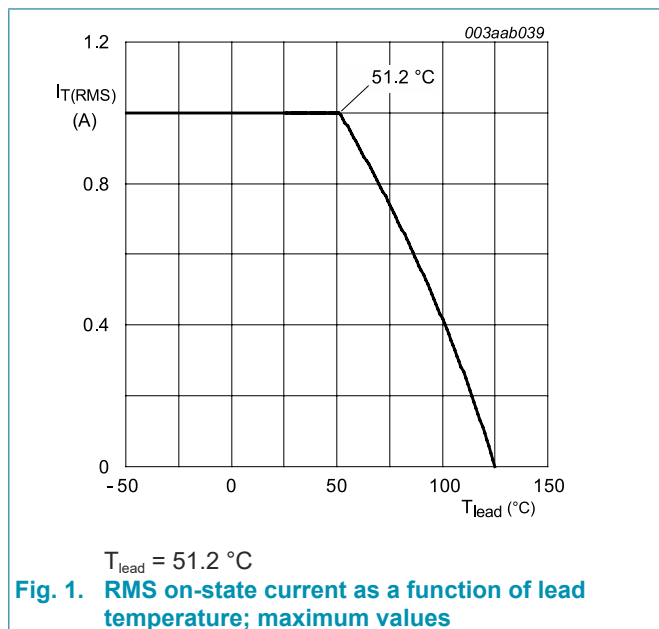
Type number	Marking codes
BT131-600E	131-6E

## 8. Limiting values

**Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{DRM}$	repetitive peak off-state voltage		-	600	V
$I_{T(RMS)}$	RMS on-state current	full sine wave; $T_{lead} \leq 51.2\text{ °C}$ ; <a href="#">Fig 1</a> ; <a href="#">Fig 2</a> ; <a href="#">Fig 3</a>	-	1	A
$I_{TSM}$	non-repetitive peak on-state current	full sine wave; $T_{j(init)} = 25\text{ °C}$ ; $t_p = 20\text{ ms}$ ; <a href="#">Fig 4</a> ; <a href="#">Fig 5</a>	-	12.5	A
		full sine wave; $T_{j(init)} = 25\text{ °C}$ ; $t_p = 16.7\text{ ms}$	-	13.7	A
$I^2t$	$I^2t$ for fusing	$t_p = 10\text{ ms}$ ; SIN	-	0.78	A <sup>2</sup> s
$di_T/dt$	rate of rise of on-state current	$I_G = 20\text{ mA}$	-	50	A/ $\mu$ s
			-	50	A/ $\mu$ s
			-	10	A/ $\mu$ s
			-	50	A/ $\mu$ s
$I_{GM}$	peak gate current		-	2	A
$P_{GM}$	peak gate power		-	5	W
$P_{G(AV)}$	average gate power	over any 20 ms period	-	0.1	W
$T_{stg}$	storage temperature		-40	150	°C
$T_j$	junction temperature		-	125	°C



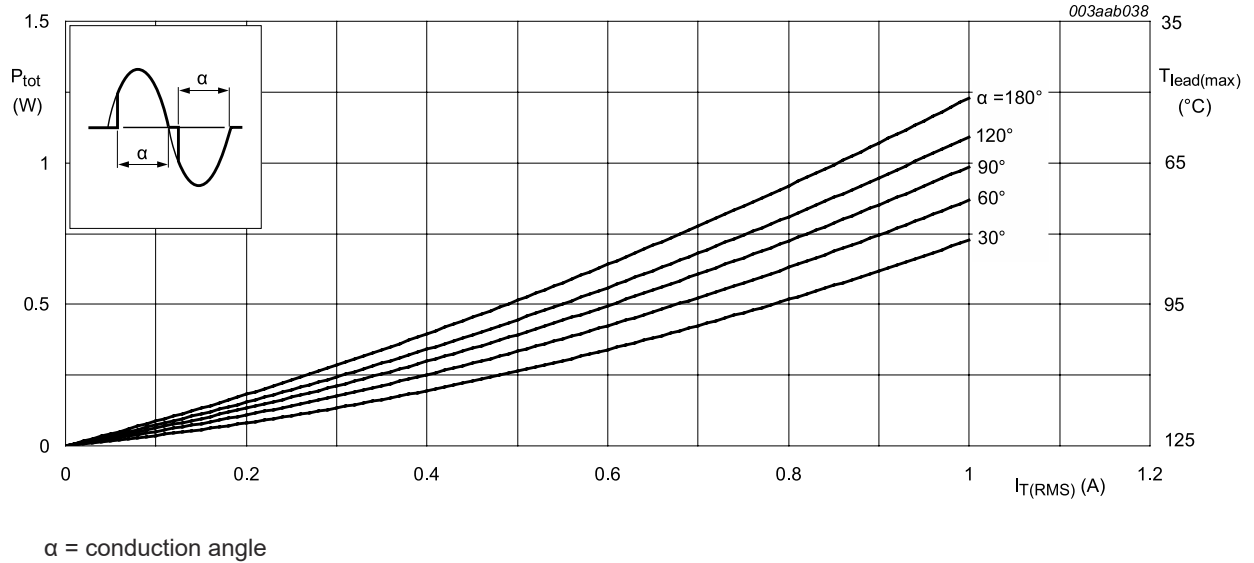


Fig. 3. Total power dissipation as a function of RMS on-state current; maximum value

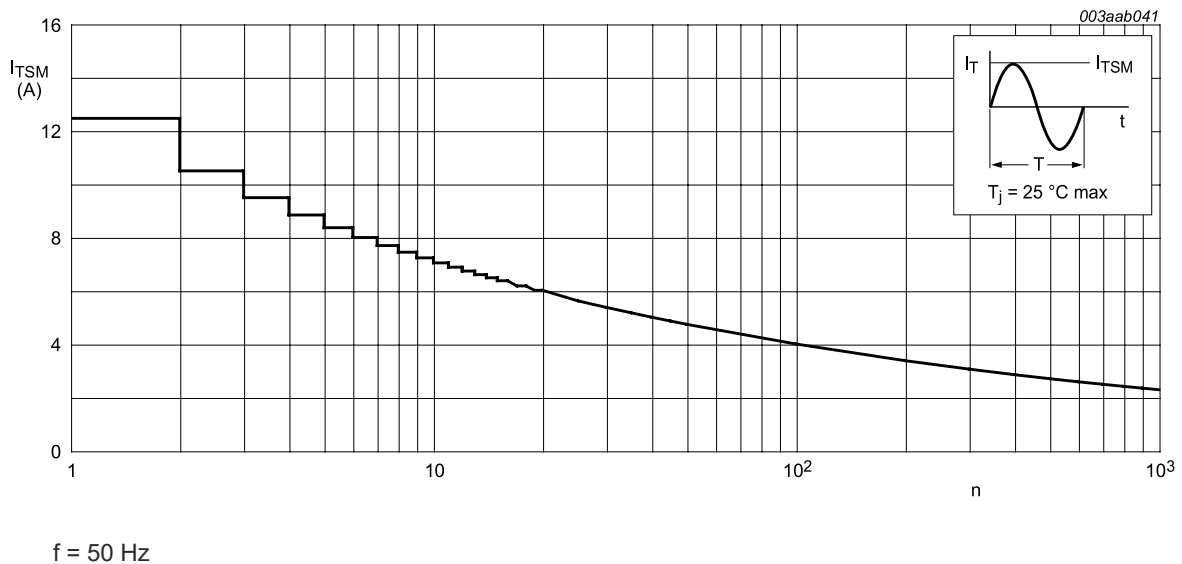
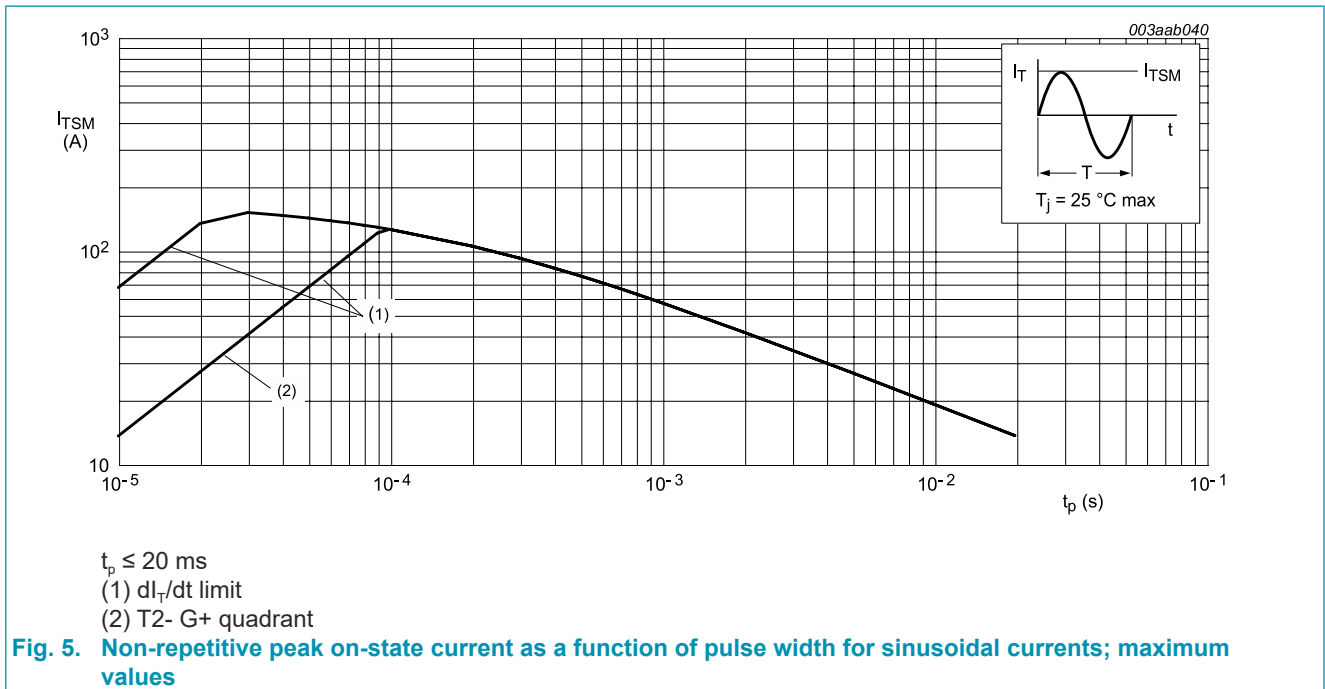


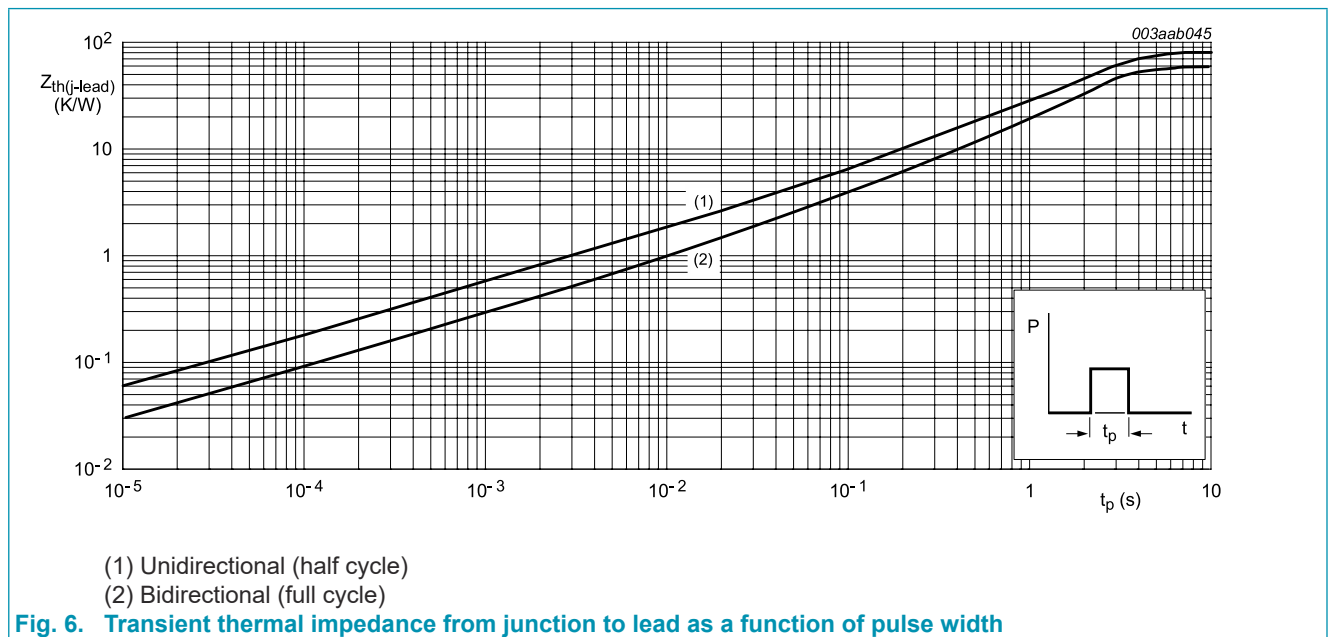
Fig. 4. Non-repetitive peak on-state current as a function of the number of sinusoidal current cycles; maximum values



## 9. Thermal characteristics

Table 6. Thermal characteristics

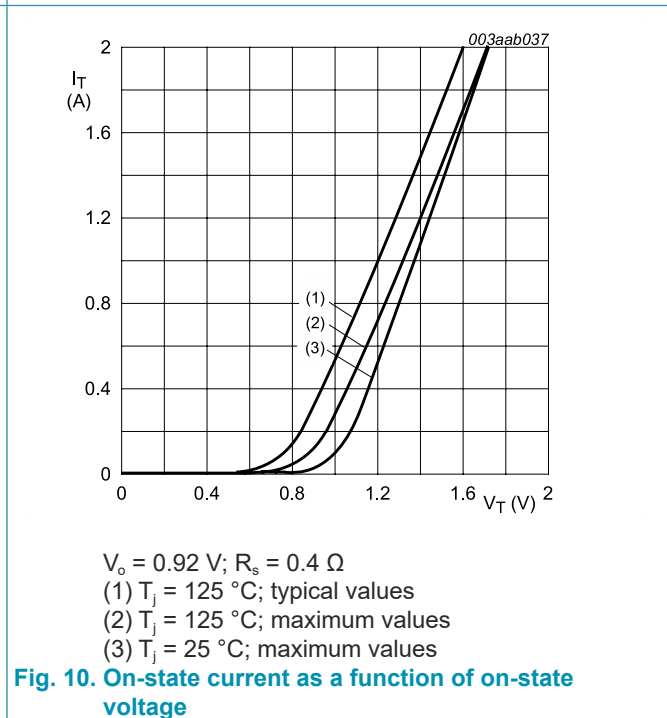
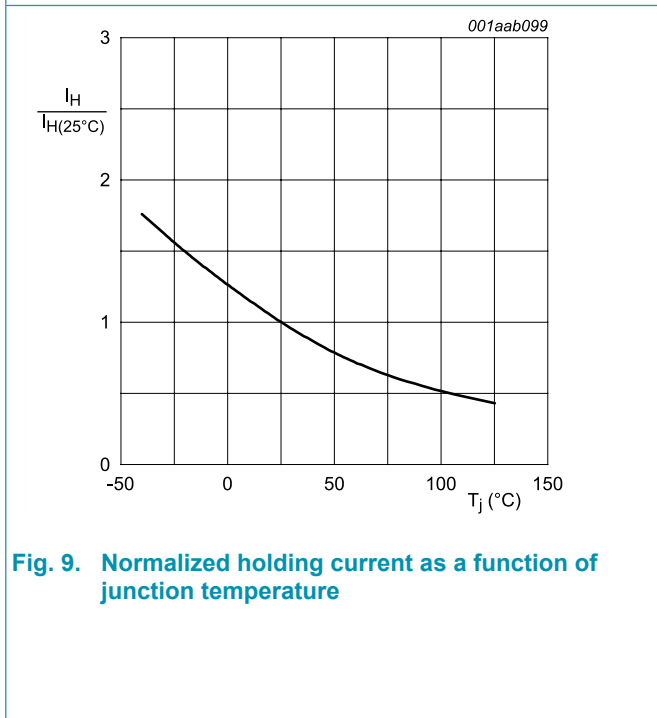
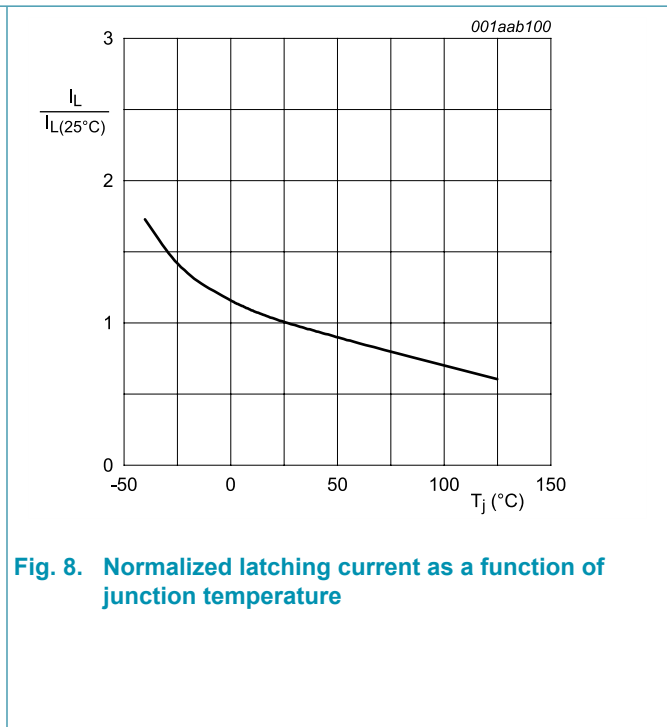
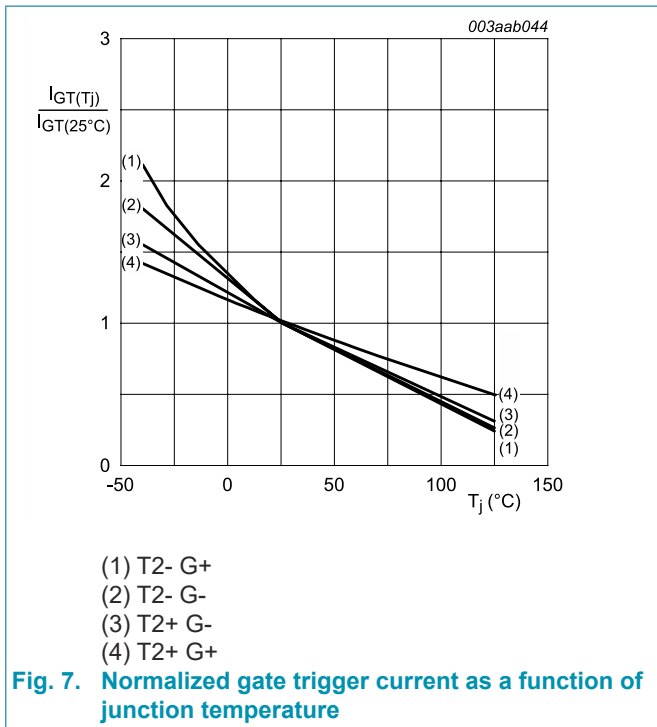
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-lead)}$	thermal resistance from junction to lead	full cycle; Fig 6	-	-	60	K/W
		half cycle; Fig 6	-	-	80	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient free air	printed circuit board mounted: lead length = 4 mm	-	150	-	K/W



## 10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Static characteristics</b>						
$I_{GT}$	gate trigger current	$V_D = 12\text{ V}; I_T = 0.1\text{ A}; T_2+ G+;$ $T_J = 25\text{ }^\circ\text{C};$ <a href="#">Fig. 7</a>	-	-	10	mA
		$V_D = 12\text{ V}; I_T = 0.1\text{ A}; T_2+ G-;$ $T_J = 25\text{ }^\circ\text{C};$ <a href="#">Fig. 7</a>	-	-	10	mA
		$V_D = 12\text{ V}; I_T = 0.1\text{ A}; T_2- G-;$ $T_J = 25\text{ }^\circ\text{C};$ <a href="#">Fig. 7</a>	-	-	10	mA
		$V_D = 12\text{ V}; I_T = 0.1\text{ A}; T_2- G+;$ $T_J = 25\text{ }^\circ\text{C};$ <a href="#">Fig. 7</a>	-	-	10	mA
$I_L$	latching current	$V_D = 12\text{ V}; I_G = 0.1\text{ A}; T_2+ G+;$ $T_J = 25\text{ }^\circ\text{C};$ <a href="#">Fig. 8</a>	-	-	15	mA
		$V_D = 12\text{ V}; I_G = 0.1\text{ A}; T_2+ G-;$ $T_J = 25\text{ }^\circ\text{C};$ <a href="#">Fig. 8</a>	-	-	25	mA
		$V_D = 12\text{ V}; I_G = 0.1\text{ A}; T_2- G-;$ $T_J = 25\text{ }^\circ\text{C};$ <a href="#">Fig. 8</a>	-	-	15	mA
		$V_D = 12\text{ V}; I_G = 0.1\text{ A}; T_2- G+;$ $T_J = 25\text{ }^\circ\text{C};$ <a href="#">Fig. 8</a>	-	-	15	mA
$I_H$	holding current	$V_D = 12\text{ V}; T_J = 25\text{ }^\circ\text{C};$ <a href="#">Fig. 9</a>	-	1.3	10	mA
$V_T$	on-state voltage	$I_T = 1.4\text{ A}; T_J = 25\text{ }^\circ\text{C};$ <a href="#">Fig. 10</a>	-	1.2	1.5	V
$V_{GT}$	gate trigger voltage	$V_D = 12\text{ V}; I_T = 0.1\text{ A}; T_J = 25\text{ }^\circ\text{C};$ <a href="#">Fig. 11</a>	-	0.7	1	V
		$V_D = 400\text{ V}; I_T = 0.1\text{ A}; T_J = 125\text{ }^\circ\text{C}$	0.2	0.3	-	V
$I_D$	off-state current	$V_D = 600\text{ V}; T_J = 125\text{ }^\circ\text{C}$	-	0.1	0.5	mA
<b>Dynamic characteristics</b>						
$dV_D/dt$	rate of rise of off-state voltage	$V_{DM} = 402\text{ V}; T_J = 125\text{ }^\circ\text{C}; (V_{DM} = 67\%$ of $V_{DRM});$ exponential waveform; $R_{GT1(ext)} = 1\text{ k}\Omega$	50	-	-	V/ $\mu$ s
$dV_{com}/dt$	rate of change of commutating voltage	$V_D = 400\text{ V}; T_J = 125\text{ }^\circ\text{C}; dI_{com}/dt = 0.5\text{ A/}$ ms; $I_T = 1\text{ A};$ gate open circuit	5	-	-	V/ $\mu$ s
$t_{gt}$	gate-controlled turn-on time	$I_{TM} = 1.5\text{ A}; V_D = 600\text{ V}; I_G = 0.1\text{ A}; dI_G/$ dt = $5\text{ A}/\mu$ s	-	2	-	$\mu$ s





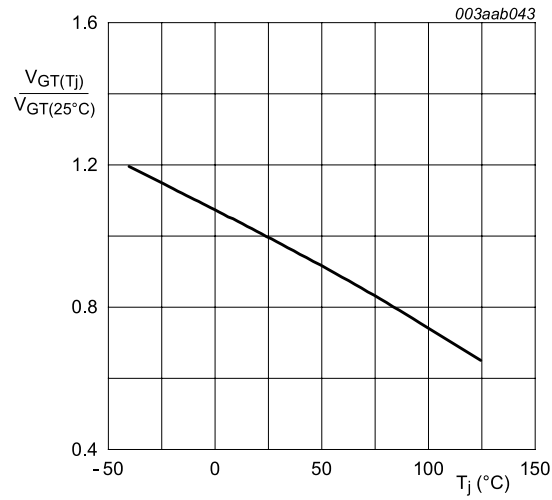
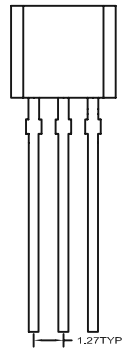


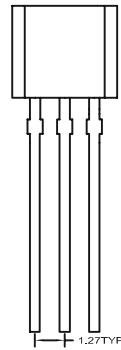
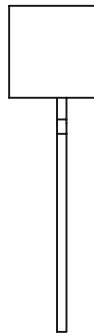
Fig. 11. Normalized gate trigger voltage as a function of junction temperature

### 11. Package outline

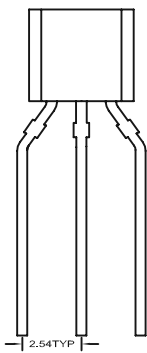
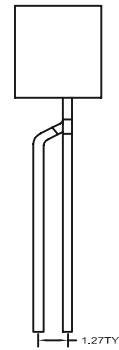
#### SOT54 PACKAGE OUTLINE



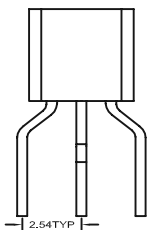
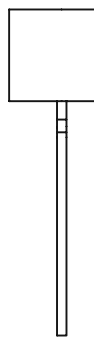
SOT54  
Bulk Pack - 412



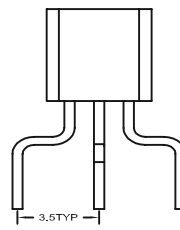
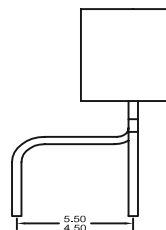
SOT54 LEADS ON CIRCLE  
Bulk Pack - 112



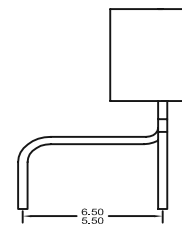
SOT54 WIDE PITCH  
Tape/ Reel Pack - 116  
Ammo Pack - 126



SOT54 LEAD BEND L01  
Bulk Pack - 412



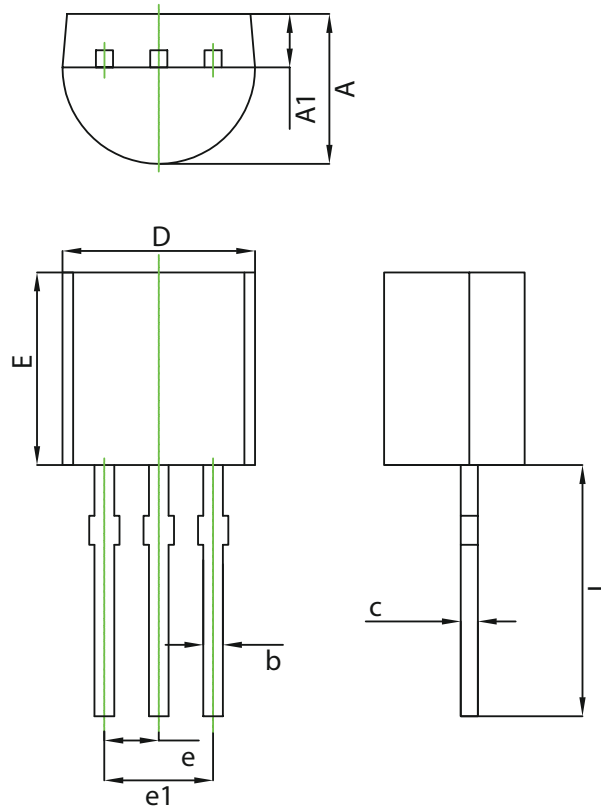
SOT54 LEAD BEND L02  
Bulk Pack - 412



Remark: Detailed dimensions refer to POD drawing.

Plastic single-ended leaded(through hole) package; 3 leads

TO92



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	3.300	3.700	0.130	0.146
A1	1.100	1.400	0.043	0.055
b	0.380	0.550	0.015	0.022
c	0.360	0.510	0.014	0.020
D	4.300	4.700	0.169	0.185
E	4.300	4.700	0.169	0.185
e	1.270 TYP.		0.050 TYP.	
e1	2.440	2.640	0.096	0.104
L	14.100	14.500	0.555	0.571

## 12. Legal information

### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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For more information, please visit: <http://www.ween-semi.com>

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Date of release: 19 October 2021

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