



Intel® Quark™ Microcontroller D1000

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Ordering and spec information

Intel® Quark™ Microcontroller D1000, T&R

Spec Code	SLKMJ
Ordering Code	DMNIAD01SLVBT
Step	B1
RCP	\$2.54

Trade compliance information

ECCN	3A991.A.2
CCATS	NA
US HTS	8542310001

PCN/MDDS Information

SLKMJ

938719	PCN MDDS
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Refer to Datasheet for formal definitions of product properties and features.

"Announced" SKUs are not yet available. Please refer to the Launch Date for market availability.

Some products can support AES New Instructions with a Processor Configuration update, in particular, i7-2630QM/i7-2635QM, i7-2670QM/i7-2675QM, i5-2430M/i5-2435M, i5-2410M/i5-2415M. Please contact OEM for the BIOS that includes the latest Processor configuration update.

‡ This feature may not be available on all computing systems. Please check with the system vendor to determine if your system delivers this feature, or reference the system specifications (motherboard, processor, chipset, power supply, HDD, graphics controller, memory, BIOS, drivers, virtual machine monitor-VMM, platform software, and/or operating system) for feature compatibility. Functionality, performance, and other benefits of this feature may vary depending on system configuration.

See <http://www.intel.com/content/www/us/en/architecture-and-technology/hyper-threading/hyper-threading-technology.html?wapkw=hyper+threading> for more information including details on which processors support Intel® HT Technology.

Max Turbo Frequency refers to the maximum single-core processor frequency that can be achieved with Intel® Turbo Boost Technology. See www.intel.com/technology/turboboost/ for more information.

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System and Maximum TDP is based on worst case scenarios. Actual TDP may be lower if not all I/Os for chipsets are used.

Low Halogen: Applies only to brominated and chlorinated flame retardants (BFRs/CFRs) and PVC in the final product. Intel components as well as purchased components on the finished assembly meet JS-709 requirements, and the PCB / substrate meet IEC 61249-2-21 requirements. The replacement of halogenated flame retardants and/or PVC may not be better for the environment.

For benchmarking data see <http://www.intel.com/performance>.

Intel processor numbers are not a measure of performance. Processor numbers differentiate features within each processor family, not across different processor families. See <http://www.intel.com/content/www/us/en/processors/processor-numbers.html> for details.

Processors that support 64-bit computing on Intel® architecture require an Intel 64 architecture-enabled BIOS.



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Product Collection	Intel® Quark™ Microcontroller D1000 Series
Code Name	Products formerly Silver Butte
Vertical Segment	Embedded
Processor Number	D1000
Status	Launched
Launch Date ?	Q3'15
Recommended Customer Price ?	\$2.54
Use Conditions ?	Embedded Broad Market Commercial Temp, Embedded Broad Market Extended Temp

Performance

# of Cores ?	1
# of Threads ?	1
Processor Base Frequency ?	33 MHz
TDP ?	0.025 W
Configurable TDP-up Frequency ?	32 MHz
Configurable TDP-up ?	0.025 W
Configurable TDP-down Frequency ?	1 MHz
Configurable TDP-down ?	0.0016 W
VID Voltage Range ?	1.62v - 3.63v

Supplemental Information

Embedded Options Available ?	Yes
Datasheet	View now
Description	Datasheet describes all publicly disclosed specifications including electrical characteristics, mechanical, and component functionality, a list of major features, a functional description, and an architectural overview.
Additional Information URL	View now

I/O Specifications

General Purpose IO	SPI, I2C, 24GPIO
UART	2

Package Specifications

Sockets Supported ?	QFN40
Operating Temperature Range	-40°C to 85°C
Package Size	6mm x 6mm
Low Halogen Options Available	See MDDS

Advanced Technologies

Instruction Set ?	32-bit
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More support options for Intel® Quark™ Microcontroller D1000



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