

# RD3G03BBG Nch 40V 65A Power MOSFET

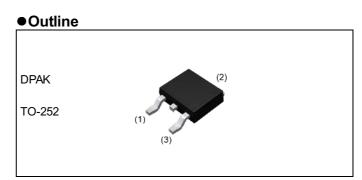
V <sub>DSS</sub>	40V
R <sub>DS(on)</sub> (Max.)	6.5mΩ
I <sub>D</sub>	±65A
P <sub>D</sub>	50W

# Features

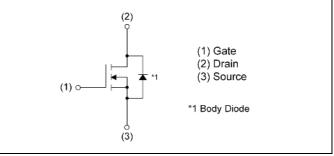
- 1) Low on resistance
- 2) High power package (TO-252)
- 3) Pb-free plating ; RoHS compliant
- 4) Halogen free

Application

Switching



## ●Inner circuit



# Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
Туре	Tape width (mm)	16
	Quantity (pcs)	2500
	Taping code	TL1
	Marking	RD3G03BBG

# • Absolute maximum ratings (T<sub>a</sub> = 25°C ,unless otherwise specified)

Para	meter	Symbol	Value	Unit	
Drain - Source voltage		V <sub>DSS</sub>	40	V	
Continuous dusis sumont	Silicon limit (V <sub>GS</sub> =10V)	۱ <sub>D</sub> *1	±65	А	
Continuous drain current	T <sub>a</sub> = 25°C (V <sub>GS</sub> =10V)	۱ <sub>D</sub> *2	±35	А	
Pulsed drain current		I <sub>DP</sub> *3	±260	А	
Gate - Source voltage		V <sub>GSS</sub>	±20	V	
Avalanche current, single pulse		$I_{AS}^{*4}$	17.5	А	
Avalanche energy, single p	ulse	$E_{AS}^{*4}$	23	mJ	
Power dissipation		P <sub>D</sub> *2	50	W	
Junction temperature		Tj	150	°C	
Operating junction and stor	age temperature range	T <sub>stg</sub>	-55 to +150	°C	

## •Thermal resistance

Parameter	Symbol	Values			Unit
Falameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	$R_{thJC}^{*2}$	-	-	2.5	°C/W

# • Electrical characteristics (T<sub>a</sub> = 25°C)

Deremeter	Currence of	Conditions	Values			Unit	
Parameter	Symbol Conditions –		Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	$V_{(BR)DSS}$ $V_{GS} = 0V, I_D = 1mA$		40	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}} I_{D} = 1 \text{mA}$ referenced to 25°C		-	28.9	-	mV/°C	
Zero gate voltage drain current	$I_{DSS}$ $V_{DS} = 40V, V_{GS} = 0V$		-	-	1	μA	
Gate - Source leakage current	I <sub>GSS</sub>	$I_{GSS}$ $V_{GS}$ = ±20V, $V_{DS}$ = 0V		-	±100	nA	
Gate threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 1mA$	1.0	-	2.5	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_{i}}   I_{D} = 1 \text{mA}$ referenced to 25°C		-	-4.6	-	mV/°C	
Static drain - source	<b>D</b> *5	V <sub>GS</sub> = 10V, I <sub>D</sub> = 35A	-	5.0	6.5		
on - state resistance	R <sub>DS(on)</sub> *5	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 17.5A	-	7.6	10.6	mΩ	
Gate resistance	R <sub>G</sub>	G -		2.1	-	Ω	
Forward Transfer Admittance	Y <sub>fs</sub>  * <sup>5</sup>	-		-	-	S	

\*1 Limited by silicon chip capability.

\*2 T<sub>c</sub>=25°C, Limited only by maximum temperature allowed.

\*3 Pw  $\leq$  10µs , Duty cycle  $\leq$  1%

\*4 L  $\simeq$  0.1mH, V<sub>DD</sub> = 20V, R<sub>G</sub> = 25 $\Omega$ , Starting T<sub>j</sub> = 25°C Fig.3-1,3-2

\*5 Pulsed





# •Electrical characteristics (T<sub>a</sub> = 25°C)

Deremeter	Cumphed	Conditions	Values			Unit	
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	1170	-		
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 20V	-	540	-	pF	
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	65	-		
Turn - on delay time	t <sub>d(on)</sub> *5	$V_{DD} \simeq 20V, V_{GS}$ = 10V	-	12	-		
Rise time	t <sub>r</sub> *5	I <sub>D</sub> = 17.5A	-	14	-	-	
Turn - off delay time	t <sub>d(off)</sub> *5	R <sub>L</sub> ≃ 1.14Ω	-	42	-	ns	
Fall time	t <sub>f</sub> *5	R <sub>G</sub> = 10Ω	-	16	-		

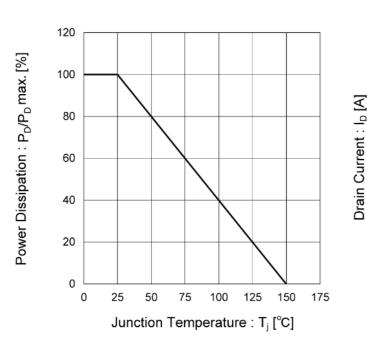
# • Gate charge characteristics ( $T_a = 25^{\circ}C$ )

Deremeter	Sumbol	ymbol Conditions		Values			L lucit
Parameter	Symbol			Min.	Тур.	Max.	Unit
Tatal acts shows	Q <sub>g</sub> *5		V <sub>GS</sub> = 10V	-	18.2	-	
Total gate charge		$V_{DD} \simeq 20V$		-	9.3	-	nC
Gate - Source charge	Q <sub>gs</sub> *5	I <sub>D</sub> = 35A	V <sub>GS</sub> = 4.5V	-	2.9	-	nc
Gate - Drain charge	Q <sub>gd</sub> *5			-	3.4	-	

# •Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Deremeter	Parameter Symbol Conditions		Values			Unit
			Min.	Тур.	Max.	Unit
Continuous forward current	I <sub>S</sub>	T <sub>a</sub> = 25℃	-	-	35	А
Pulse forward current	$I_{SP}^{*3}$	$T_{a} = 25 C$	-	-	260	А
Forward voltage	$V_{SD}^{*5}$	V <sub>GS</sub> = 0V, I <sub>S</sub> = 35A	-	-	1.2	V
Reverse recovery time	t <sub>rr</sub> *5	I <sub>S</sub> = 35A, V <sub>GS</sub> =0V	-	34	-	ns
Reverse recovery charge	Q <sub>rr</sub> *5	di/dt = 100A/µs	-	33	-	nC





# Fig.1 Power Dissipation Derating Curve

Fig.2 Maximum Safe Operating Area

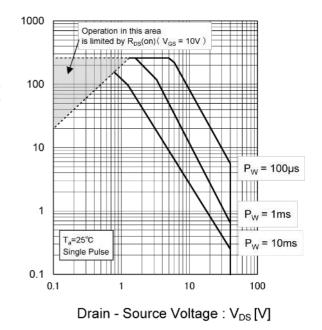
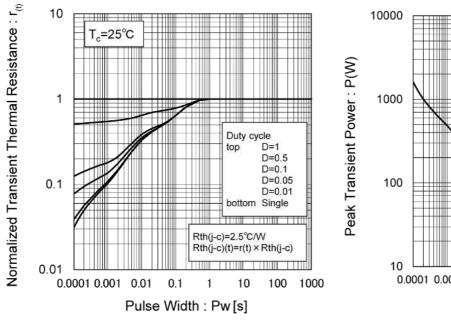
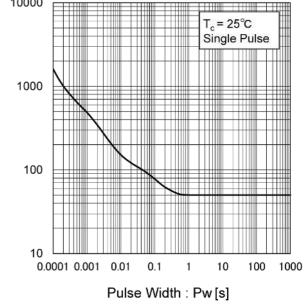


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

Fig.4 Single Pulse Maximum Power Dissipation







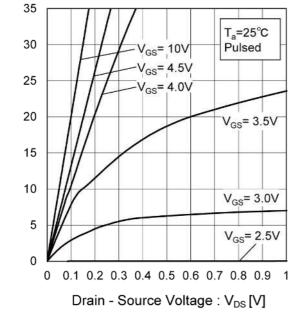
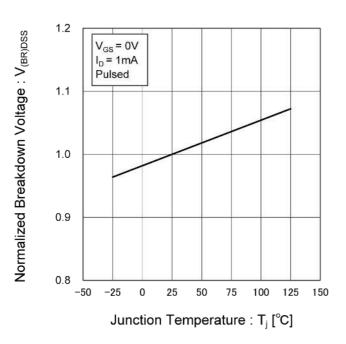


Fig.5 Typical Output Characteristics(I)

# Fig.7 Normalized Breakdown Voltage vs. Junction Temperature





Drain - Source Voltage : V<sub>DS</sub> [V]

# Fig.6 Typical Output Characteristics(II)

<sub>GS</sub>= 10V

V<sub>GS</sub>= 4.5V

′<sub>GS</sub>= 4.0∨

V<sub>GS</sub>= 3.5V

35

30

25

20

15

10

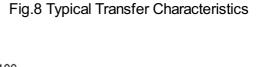
0

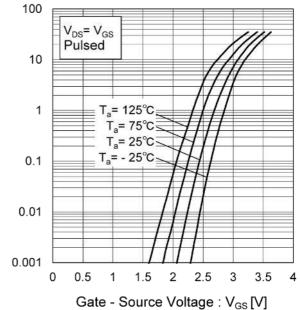
0 1 2 3 4 5 6 7 8 9 10

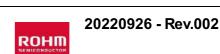
Drain Current : I<sub>D</sub> [A]

Drain Current : I<sub>D</sub> [A]

5/10







T<sub>a</sub>=25°C

V<sub>GS</sub>= 3.0V

Pulsed

# • Electrical characteristic curves

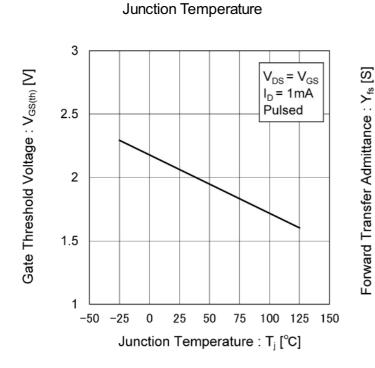


Fig.9 Gate Threshold Voltage vs.

# Fig.10 Forward Transfer Admittance vs. Drain Current

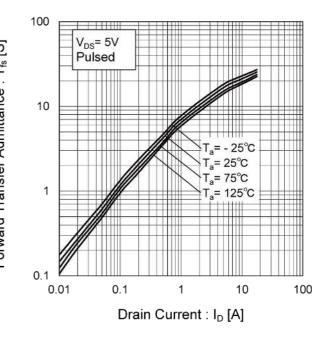
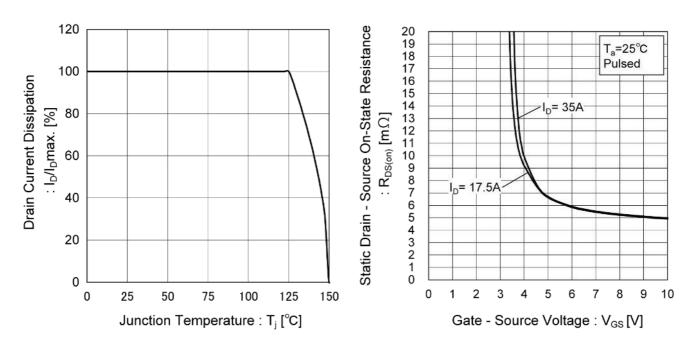


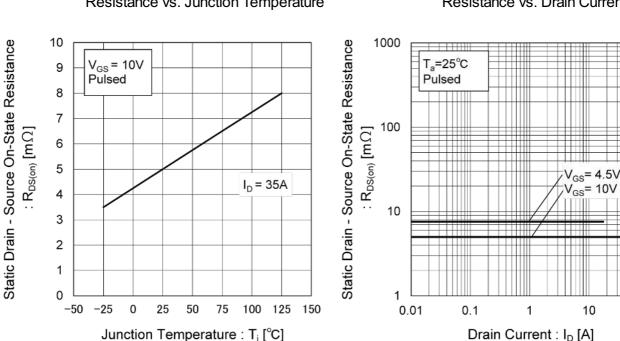
Fig.11 Drain Current Derating Curve

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage



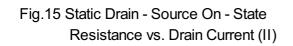


100



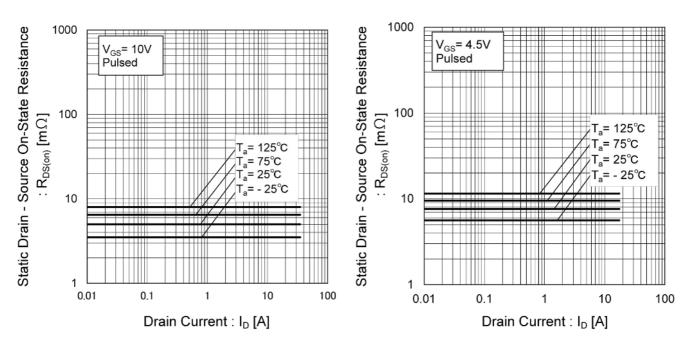
# Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

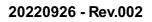
Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)



Junction Temperature : T<sub>j</sub> [°C]

Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)





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# •Electrical characteristic curves

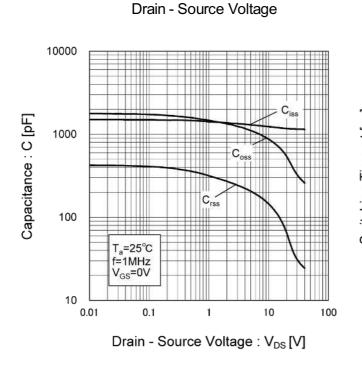


Fig.17 Typical Capacitances vs.

# Fig.18 Switching Characteristics

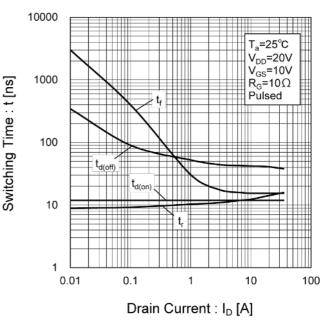


Fig.19 Typical Gate Charge

Gate - Source Voltage : V<sub>GS</sub> [V]

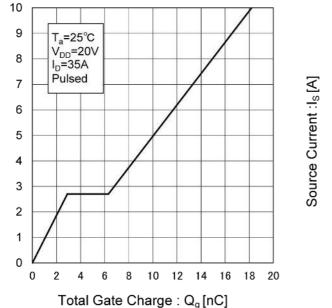
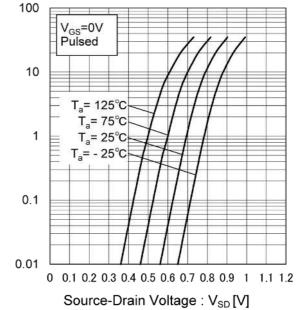


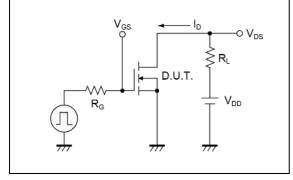
Fig.20 Source Current vs. Source Drain Voltage



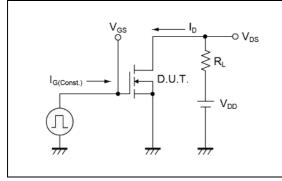
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## Measurement circuits

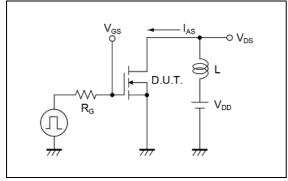
Fig.1-1 Switching Time Measurement Circuit



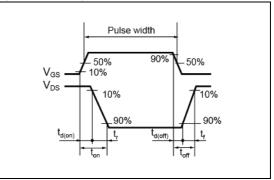
## Fig.2-1 Gate Charge Measurement Circuit



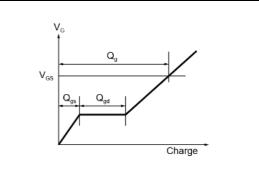
#### Fig.3-1 Avalanche Measurement Circuit



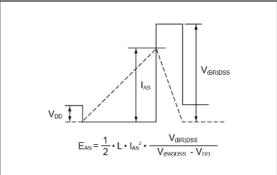
#### Fig.1-2 Switching Waveforms



## Fig.2-2 Gate Charge Waveform

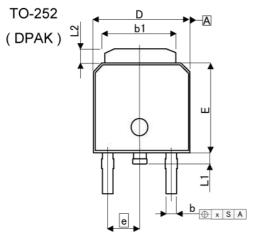


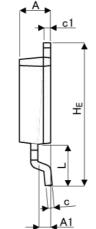
#### Fig.3-2 Avalanche Waveform

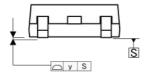


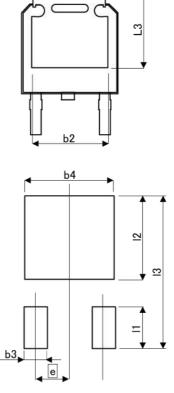


# Dimensions









Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIME	ETERS	INC	HES	
	MIN	MAX	MIN	MAX	
A	2.20	2.40	0.087	0.094	
A1	0.70	1.10	0.028	0.043	
b	0.60	0.90	0.024	0.035	
b1	5.20	5.50	0.205	0.217	
b2	4.	80	0.1	89	
С	0.40	0.60	0.016	0.024	
c1	0.40	0.60	0.016	0.024	
D	6.40	6.80	0.252	0.268	
е	2.	30	0.0	91	
E	6.00	6.40	0.236	0.252	
HE	9.40	10.40	0.370	0.409	
L	2.	90	0.1	114	
L1	0.60	1.00	0.024	0.039	
L2	0.70	1.30	0.028	0.051	
L3	5.	5.30		209	
x	÷	0.25	(1)	0.010	
у	73	0.10	15//	0.004	
	MILIME	ETERS	INC	HES	
DIM	MIN	MAX	MIN	MAX	
b3	+	1.15	2 <del>4</del> 0	0.045	
b4	5	5.55	4.5.3	0.219	
11	÷ (	2.77	( <b>1</b> )	0.109	
12		5.50	3.7.0	0.217	
13	2	10.40	( <b>1</b> )	0.409	

Dimension in mm/inches



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(Note1) Medical Equipment Classification of the Specific Applications
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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASS II b	CLASSII
CLASSⅣ	CLASSII	CLASSⅢ	CLASSI

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  - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

#### Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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