

## P-Channel 12 V (D-S) MOSFET

PRODUCT SUMMARY		
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)
- 12	0.085 at V <sub>GS</sub> = - 4.5 V	- 3.3
	0.115 at V <sub>GS</sub> = - 2.5 V	- 2.9
	0.160 at V <sub>GS</sub> = - 1.8 V	- 2.4

### FEATURES

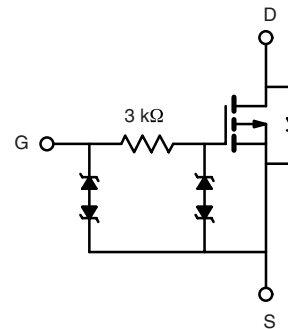
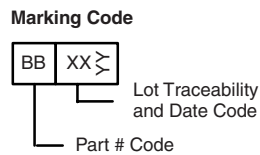
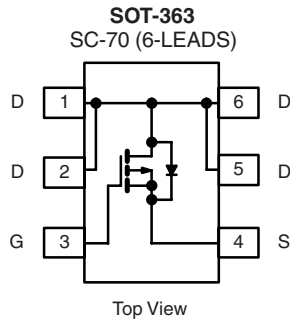
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET: 1.8 V Rated
- ESD Protected: 3000 V
- Thermally Enhanced SC-70 Package
- Compliant to RoHS Directive 2002/95/EC



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**  
Available

### APPLICATIONS

- Load Switching
- PA Switch
- Level Switch



**Ordering Information:** Si1417EDH-T1-E3 (Lead (Pb)-free)  
Si1417EDH-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS T <sub>A</sub> = 25 °C, unless otherwise noted				
Parameter	Symbol	5 s	Steady State	Unit
Drain-Source Voltage	V <sub>DS</sub>	- 12		V
Gate-Source Voltage	V <sub>GS</sub>	± 12		
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	I <sub>D</sub>	T <sub>A</sub> = 25 °C	- 3.3	- 2.7
		T <sub>A</sub> = 85 °C	- 2.4	- 1.9
Pulsed Drain Current	I <sub>DM</sub>	- 8		A
Continuous Diode Current (Diode Conduction) <sup>a</sup>	I <sub>S</sub>	- 1.4	- 0.9	
Maximum Power Dissipation <sup>a</sup>	P <sub>D</sub>	T <sub>A</sub> = 25 °C	1.56	1.0
		T <sub>A</sub> = 85 °C	0.81	0.52
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C

THERMAL RESISTANCE RATINGS				
Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>a</sup>	R <sub>thJA</sub>	t ≤ 5 s	60	80
		Steady State	100	125
Maximum Junction-to-Foot (Drain)	R <sub>thJF</sub>	34	45	°C/W

Notes:

a. Surface mounted on 1" x 1" FR4 board.

SPECIFICATIONS $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static</b>						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$	-0.45			V
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 4.5\text{ V}$			$\pm 1.5$	$\mu\text{A}$
		$V_{DS} = 0\text{ V}, V_{GS} = \pm 12\text{ V}$			$\pm 10$	$\text{mA}$
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -9.6\text{ V}, V_{GS} = 0\text{ V}$			-1	$\mu\text{A}$
		$V_{DS} = -9.6\text{ V}, V_{GS} = 0\text{ V}, T_J = 85\text{ }^\circ\text{C}$			-5	
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{DS} = -5\text{ V}, V_{GS} = -4.5\text{ V}$	-4			A
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(on)}$	$V_{GS} = -4.5\text{ V}, I_D = -3.3\text{ A}$		0.070	0.085	$\Omega$
		$V_{GS} = -2.5\text{ V}, I_D = -2.9\text{ A}$		0.095	0.115	
		$V_{GS} = -1.8\text{ V}, I_D = -1.0\text{ A}$		0.133	0.160	
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = -10\text{ V}, I_D = -3.3\text{ A}$		8		S
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	$I_S = -1.4\text{ A}, V_{GS} = 0\text{ V}$		-0.80	-1.1	V
<b>Dynamic<sup>b</sup></b>						
Total Gate Charge	$Q_g$	$V_{DS} = -6\text{ V}, V_{GS} = -4.5\text{ V}, I_D = -3.3\text{ A}$		5.8	8	nC
Gate-Source Charge	$Q_{gs}$		1.3			
Gate-Drain Charge	$Q_{gd}$		1.5			
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = -6\text{ V}, R_L = 6\text{ }\Omega$ $I_D \cong -1\text{ A}, V_{GEN} = -4.5\text{ V}, R_g = 6\text{ }\Omega$		0.60	1.0	$\mu\text{s}$
Rise Time	$t_r$		1.4	2.1		
Turn-Off Delay Time	$t_{d(off)}$		4.9	7.5		
Fall Time	$t_f$		4.9	7.5		

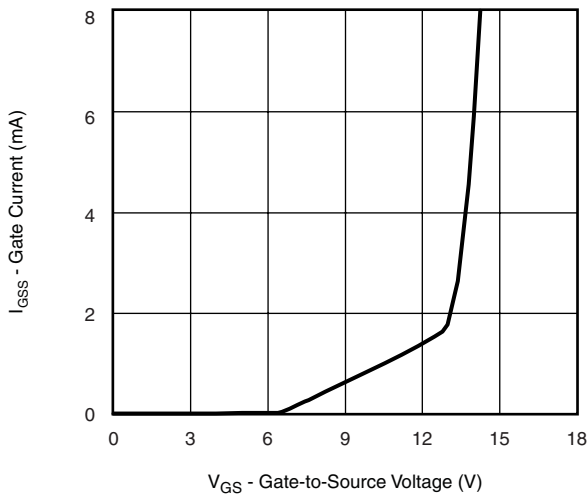
Notes:

a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .

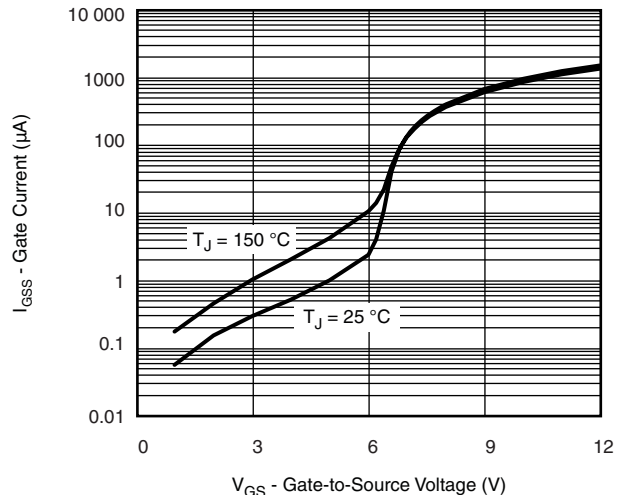
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**TYPICAL CHARACTERISTICS**  $25\text{ }^\circ\text{C}$ , unless otherwise noted

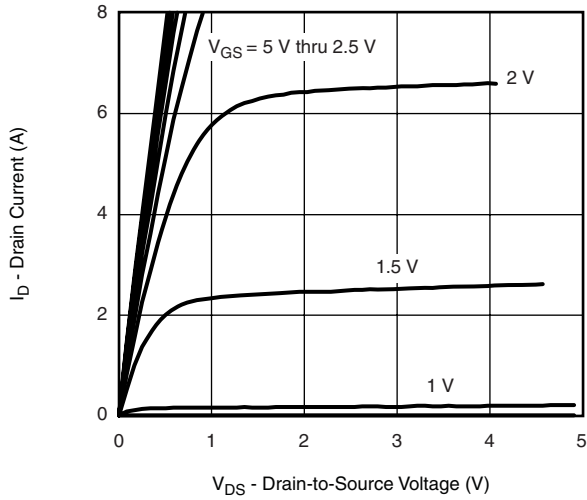


Gate-Current vs. Gate-Source Voltage

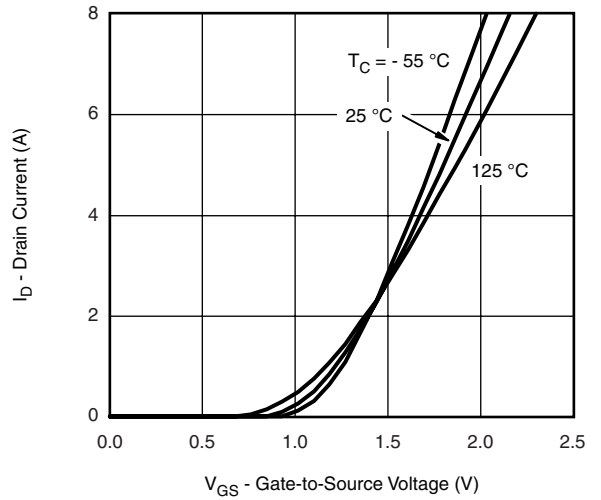


Gate-Current vs. Gate-Source Voltage

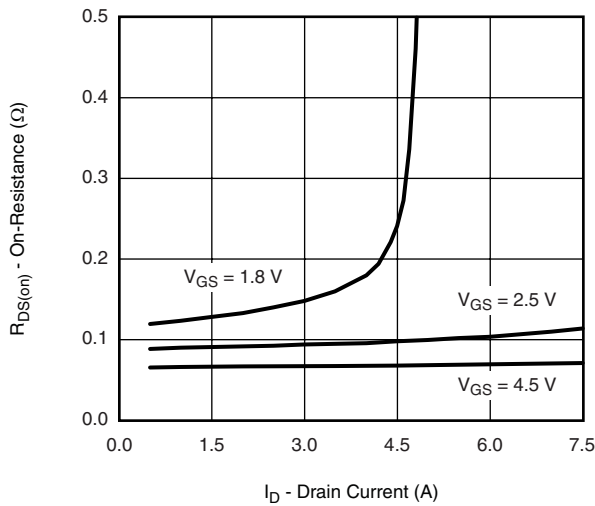
**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



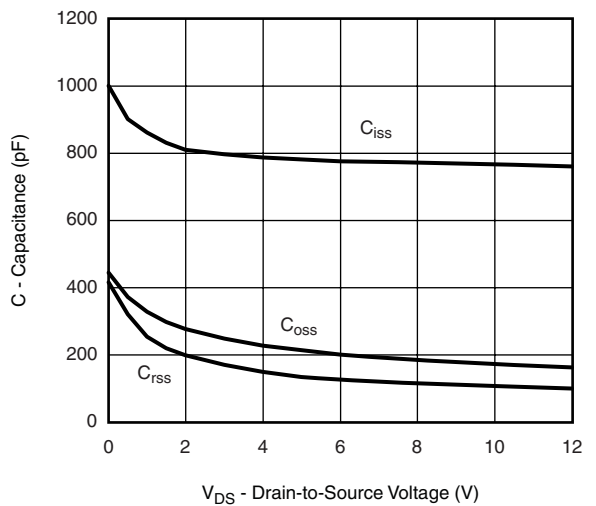
**Output Characteristics**



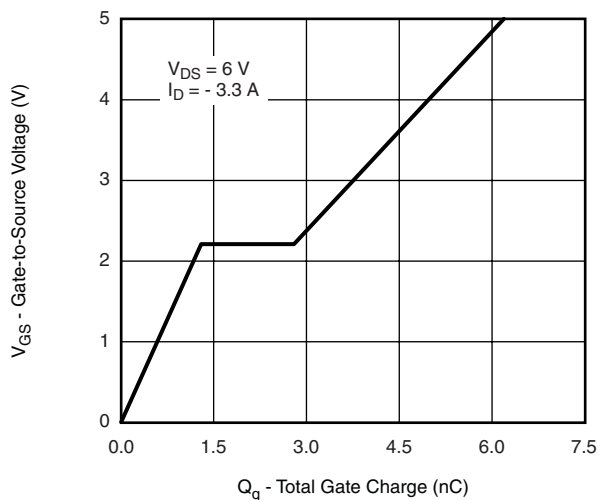
**Transfer Characteristics**



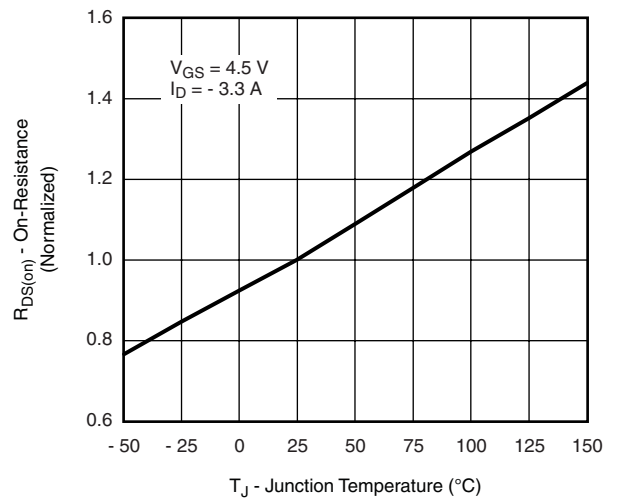
**On-Resistance vs. Drain Current**



**Capacitance**

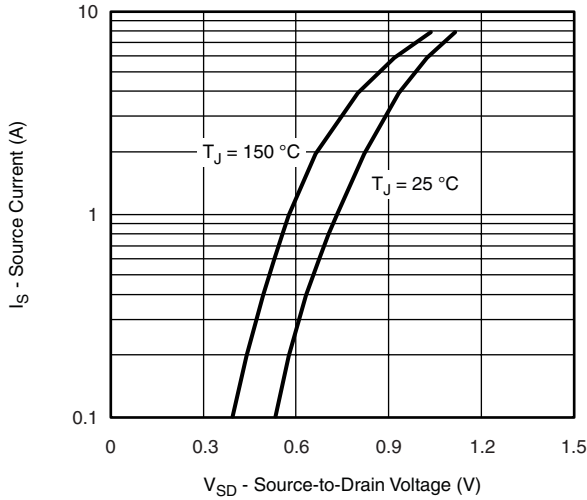


**Gate Charge**

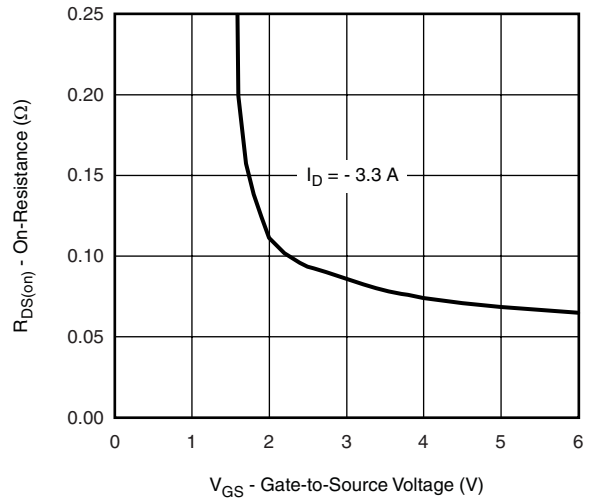


**On-Resistance vs. Junction Temperature**

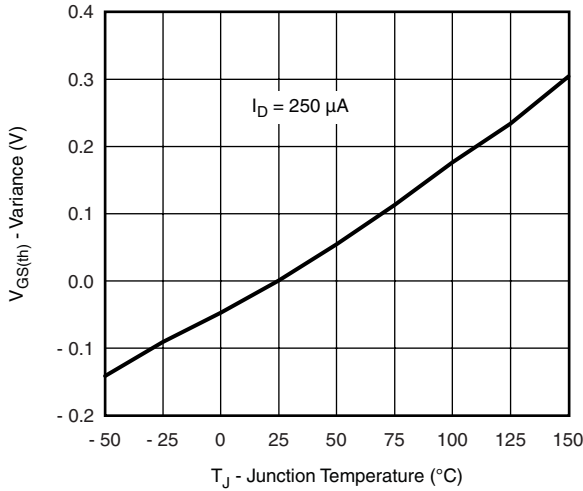
**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



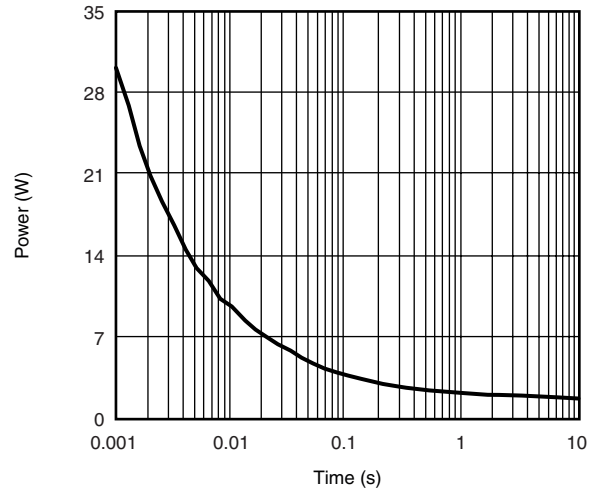
**Source-Drain Diode Forward Voltage**



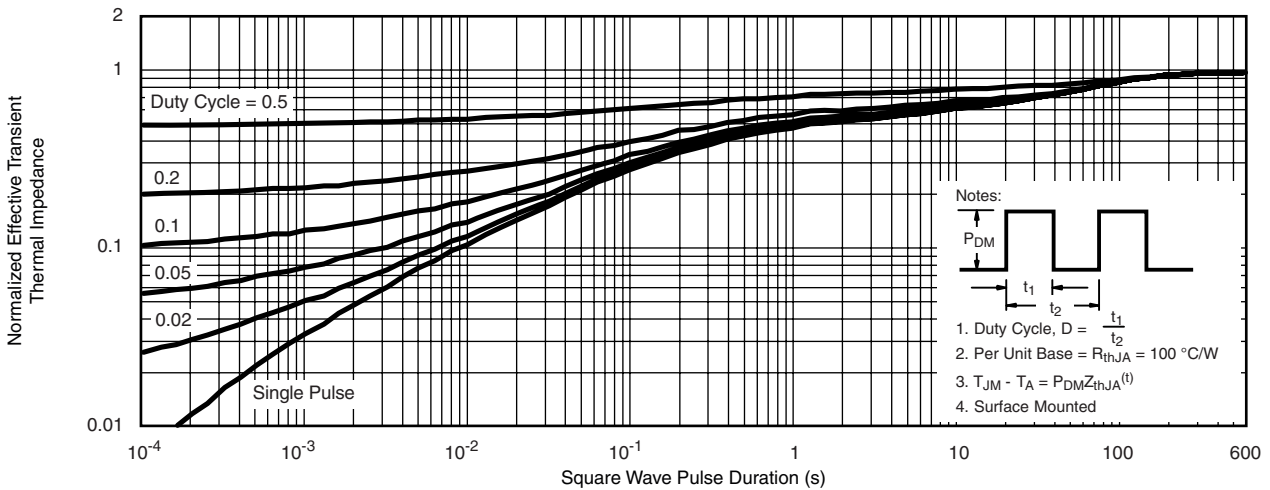
**On-Resistance vs. Gate-to-Source Voltage**



**Threshold Voltage**



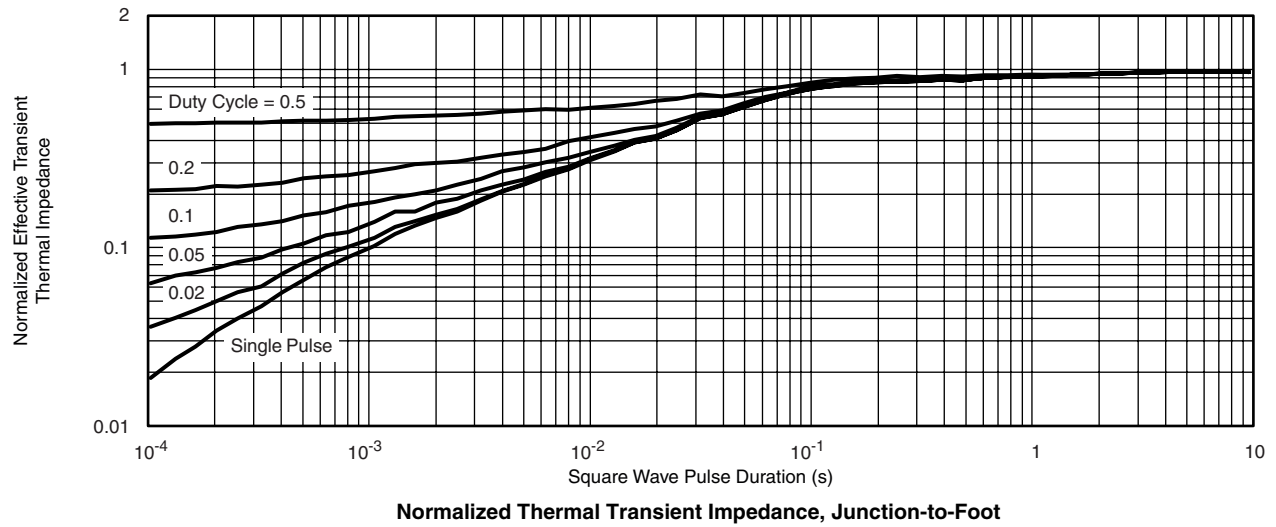
**Single Pulse Power, Junction-to-Ambient**



**Normalized Thermal Transient Impedance, Junction-to-Ambient**

- Notes:
- 1. Duty Cycle,  $D = \frac{t_1}{t_2}$
  - 2. Per Unit Base =  $R_{thJA} = 100 \text{ } ^\circ\text{C/W}$
  - 3.  $T_{JM} - T_A = P_{DM}Z_{thJA}^{(t)}$
  - 4. Surface Mounted

**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



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