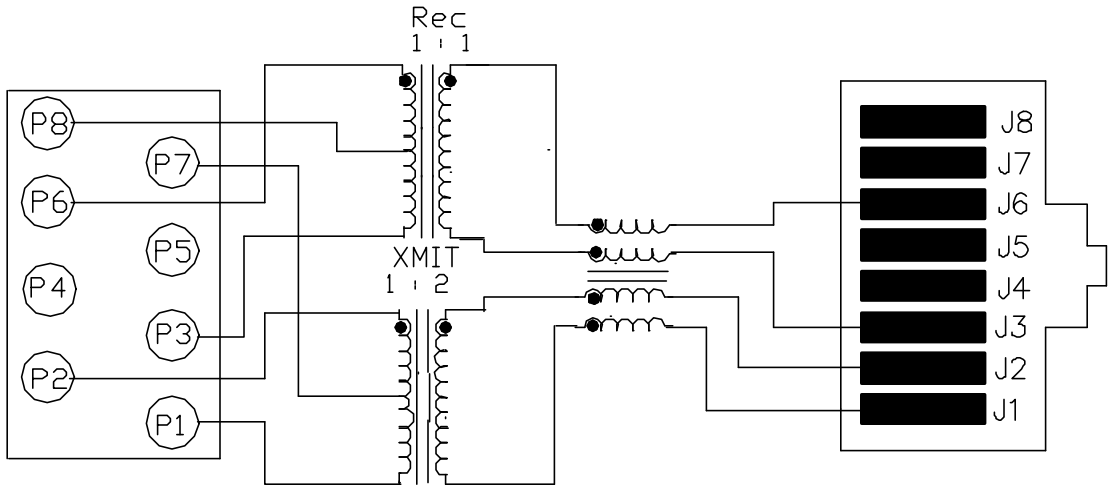

ELECTRICAL SPECIFICATIONS:

1.0 TURNS RATIO (P6-P8-P3) : (J6-J3)	: 1CT : 1 ± 3%
(P2-P7-P1) : (J2-J1)	: 1CT : 2 ± 3%
2.0 INDUCTANCE (P6-P3)=(J6-J3)	: 98uH MIN. @ 0.01V , 10KHz
(P2-P1)	: 18uH MIN. @ 0.01V, 10KHz
3.0 LEAKAGE INDUCTANCE P6-P3 (WITH J6 AND J3 SHORT)	: 0.3uH MAX. @ 1MHz
P2-P1 (WITH J2 AND J1 SHORT)	: 0.08uH MAX. @ 1MHz
4.0 INTERWINDING CAPACITANCE (P6,P3) TO (J6,J3)	: 8pf MAX @ 1MHz
(P2,P1) TO (J2,J1)	: 6pf MAX. @ 1MHz
5.0 DC RESISTANCE (J6-J3)=(J2-J1)	: 0.7 ohms Max.
(P6-P8)=(P8-P3)	: 0.3 ohms Max.
(P2-P7)=(P7-P1)	: 0.3 ohms Max.
6.0 DIELECTRIC WITHSTAND (P6,P3) TO (J6,J3)	: 1500VAC
(P2,P1) TO (J2,J1)	: 1500VAC

NOTES

1.0 PINS WITHOUT ELECTRICAL CONNECTION ARE OMITTED.

2.0 Ref. MG06

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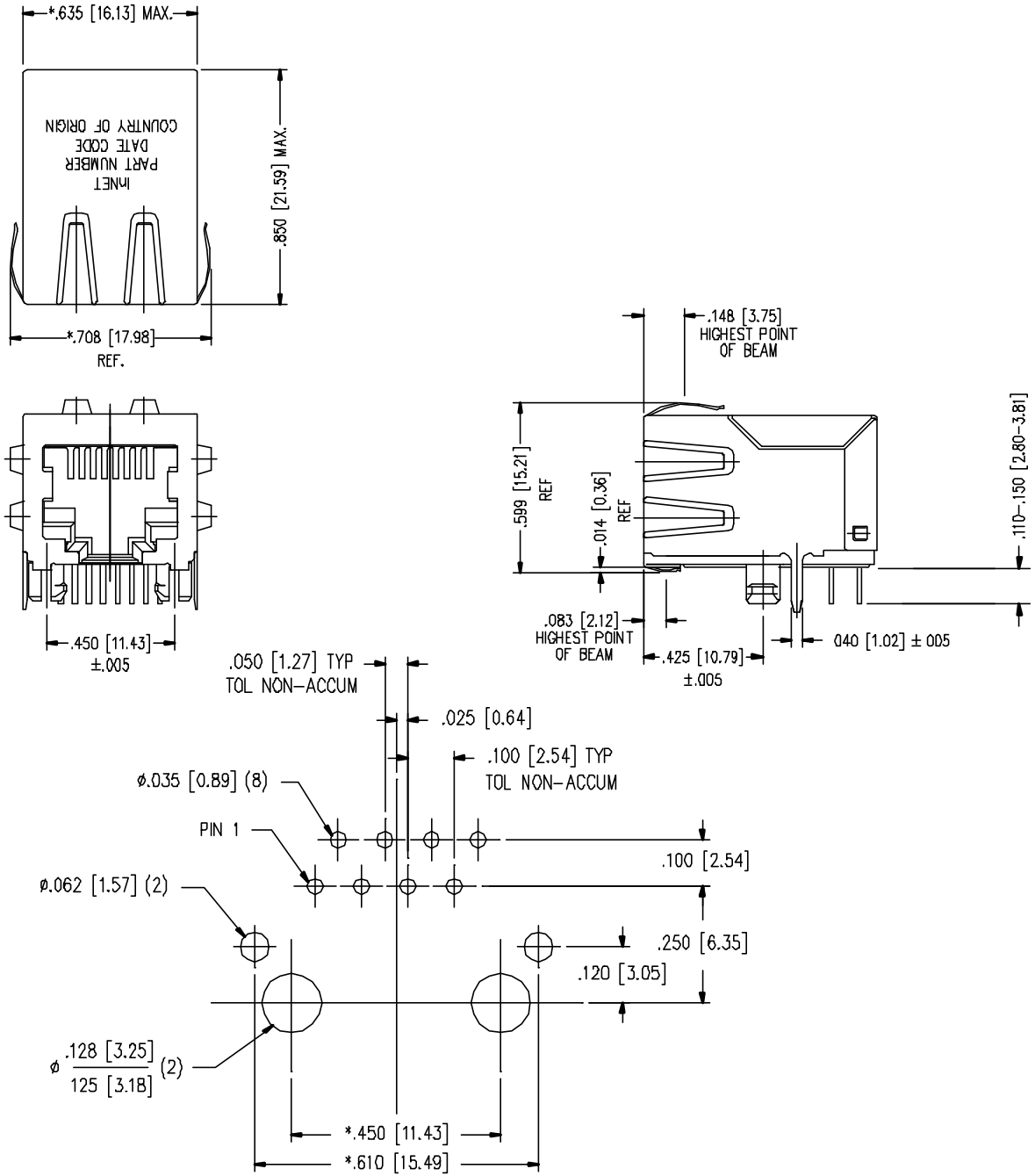

P.C.B. RECOMMENDED HOLE LAYOUT
SEEN FROM COMPONENT SIDE
TOLERANCE ±.003 [0.08] UNLESS OTHERWISE SPECIFIED

NOTES:
- TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS
- DIMENSIONS SHOWN WITH "*" TO BE CENTRAL ABOUT CENTER LINE
- DIMENSIONS SHOWN ARE SUBJECT TO CHANGE WITHOUT NOTICE.
- PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED. SEE ELECTRICAL DRAWING FOR OMITTED PINS.

AVAILABLE WITH:
- STANDARD 50 MICRO-INCH SELECTIVE GOLD PLATING (AVAILABLE WITH 30)

CT720034X1/24-0012

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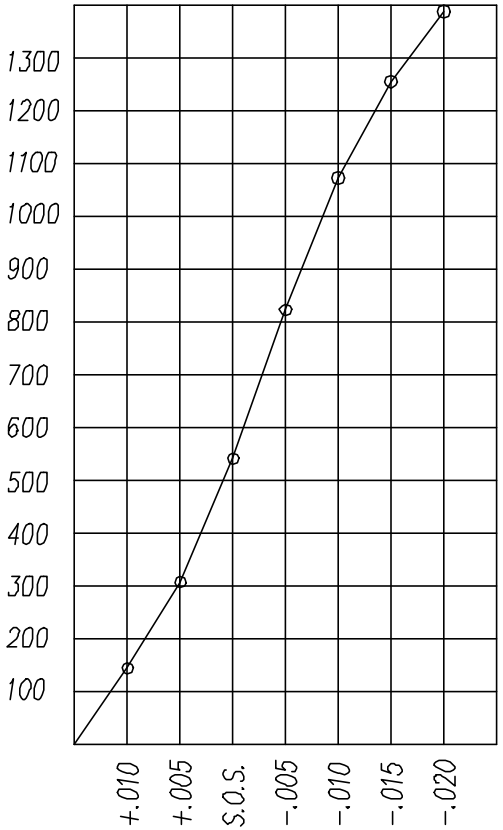
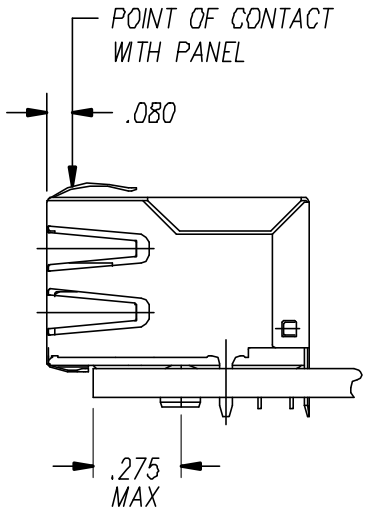
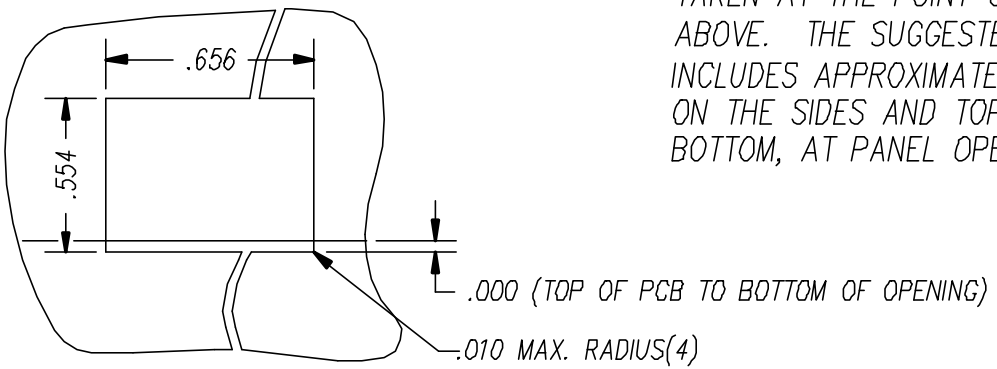


PANEL GROUNDING BEAM DEFLECTION
S.O.S. = SUGGESTED OPENING SIZE



THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY. THESE VARIABLES CAN BE ADJUSTED IN EITHER DIRECTION BUT MAY CARRY SOME CONSEQUENCES IN THE FORM OF LOWER MATING FORCES OR TIGHTER ASSEMBLY TOLERANCES. FORCE VALUES ON THE GRAPH ARE GENERAL AVERAGES TAKEN AT THE POINT OF CONTACT SHOWN ABOVE. THE SUGGESTED PANEL OPENING INCLUDES APPROXIMATELY .020 CLEARANCE ON THE SIDES AND TOP AND .013 ON THE BOTTOM, AT PANEL OPENING.



SUGGESTED PANEL OPENING

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