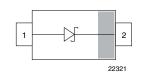
BAS70-02V



Vishay Semiconductors

Small Signal Schottky Diode





LINKS TO ADDITIONAL RESOURCES



MECHANICAL DATA

Case: SOD-523

Weight: approx. 1.4 mg

Molding compound flammability rating: UL 94 V-0 **Terminals:** high temperature soldering guaranteed:

260 °C/10 s at terminals

Packaging codes / options:

08/8K per 7" reel (8 mm tape)

FEATURES

This diode features very low turn-on voltage

This device is protected by a PN junction guard ring against excessive voltage, such as



RoHS

COMPLIANT

HALOGEN

FREE

GREEN

(5-2008)

• AEC-Q101 qualified available

electrostatic discharges

- Space saving SOD-523 package
- Base P/N-G3 RoHS-compliant, commercial grade
- Base P/N-HG3 RoHS-compliant, AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

PARTS TABLE						
PART	ORDERING CODE	AEC-Q101 QUALIFIED	CIRCUIT CONFIGURATION	TYPE MARKING	REMARKS	
BAS70-02V	BAS70-02V-G3-08	no	Single	:X	Tape and reel	
	BAS70-02V-HG3-08	yes			Tape and Teel	

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL VALUE		UNIT	
Repetitive peak reverse voltage		V _{RRM}	70	V	
Forward continuous current		I _F	100	mA	
Surge forward current	t_p = 10 ms square wave, T_j = 25 °C prior to surge	I _{FSM}	600	mA	
Power dissipation	on FR-4 board with recommended soldering footprint	P _{tot}	150	mW	

THERMAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL VALUE		UNIT		
Thermal resistance junction to ambient air	on FR-4 board according to JEDEC [®] 51-3 with recommended soldering footprint	R _{thJA} 680		K/W		
Thermal resistance junction to lead		R _{thJL}	480	K/W		
Junction temperature		Tj	125	°C		
Operating temperature range		T _{op}	-55 to +125	°C		
Storage temperature range		T _{stg}	-65 to +150	°C		

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Reserve breakdown voltage	I _R = 10 μA (pulsed)	V _(BR)	70			V
Leakage current	$V_{R} = 50 \text{ V}, \text{ t}_{p} < 300 \mu\text{s}$	I _R		20	100	nA
Forward voltage	t _p < 300 μs, I _F = 1.0 mA	VF			410	mV
Forward voltage	t _p < 300 μs, I _F = 15 mA	V _F			1000	mV
Diode capacitance	V _R = 0 V, f = 1 MHz	CD		1.5	2	pF
Reserve recovery time	$I_{\rm F}$ = 10 mA, $I_{\rm R}$ = 10 mA, $i_{\rm R}$ = 1 mA, $R_{\rm L}$ = 100 Ω	t _{rr}			5	ns

Rev. 1.6, 20-Jan-2022

Document Number: 85652

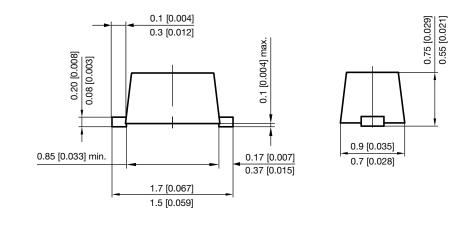
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Vishay Semiconductors

PACKAGE DIMENSIONS in millimeters [inches]: SOD-523

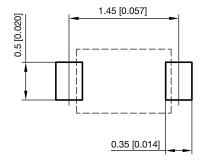


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Document no.: S8-V-3880.02-003 (4) Created - Date: 04. April 2017 Rev. 4 - Date: 03. Aug. 2020

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