TLP179D

Unit: mm

7.0+0.

0.6±0.3

11-5H1

TOSHIBA PHOTOCOUPLER PHOTO RELAY

TLP179D

Measurement Instruments Logic IC Testers / Memory Testers Board Testers / Scanners

The TOSHIBA TLP179D Mini-flat photorelay is a small-outline photorelay, suitable for surface-mount assembly. The TLP179D consists of an infrared-emitting diode optically coupled to a photo-MOS FET and housed in a 4-pin package.

Its characteristics include low OFF-state current and low output pin capacitance, enabling it to be used in high-frequency measurement instruments.

Features

• 4 pin SOP (2.54SOP4)

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- 1-Form-A
- Peak OFF-State Voltage : 200 V (min)
- Trigger LED Current
- ON-State Current
- ON-State Resistance $: 50 \Omega \text{ (max)}$
- Output Capacitance
- Isolation Voltage
- UL-recognized
- cUL-recognized

4) \therefore 2.1 mm high, 2.54 mm pitch

: 3 mA (max)

: 50 mA (max)

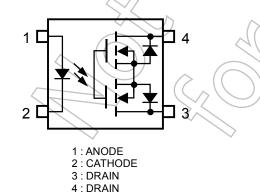
: 20 pF (max)

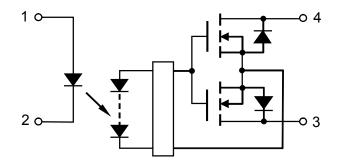
: 1500 Vrms (min)

: UL 1577, File No.E67349 : CSA Component Acceptance Service No.5A File No.E67349

Pin Configuration (top view)

Schematic





0.4±0.1

2.54±0.25

JEDEC

JEITA

TOSHIBA

Weight: 0.1 g (typ.)

Start of commercial production 2008-11

Absolute Maximum Ratings (Ta = 25°C)

	Characteristics	Symbol	Rating	Unit	
	Forward Current	lF	50	mA	
ED	Forward Current Derating (Ta $\ge 25^{\circ}$ C)	ΔIF/°C	-0.5	mA/°C	
	Reverse Voltage	VR	5	V	
Щ	Diode power dissipation	PD	50	mW	
	Diode power dissipation derating (Ta >25°C)	$\Delta P_D /°C$	-0.5	mW/°C	
	Junction Temperature	Tj	125	°C	\sum
	OFF-State Output Terminal Voltage	Voff	200	$\left(\sqrt{\sqrt{2}} \right)$	
DETECTOR	ON-State Current	ION	50	mA	
	ON-State Current Derating (Ta \ge 25°C)	Δlon/°C	-0.5	mA/°C	
	Output power dissipation	Po	125	mW	
	Output power dissipation derating (Ta \ge 25°C)	ΔP _O /°C	-1.25	∕mW / °C	
	Junction Temperature	Tj	125	°C	
Stora	ge Temperature Range	T _{stg}	-55 to 125	ر گ	(\bigcirc)
Oper	ating Temperature Range	Topr	-40 to 85	°C 🔇	X ()
Lead	Soldering Temperature (10 s)	Tsol	260	°C	\searrow
Isolat	ion Voltage (AC, 60 s, R.H. \leq 60 %) (Note1)	BVs	1500	Vrms))

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note1: Device considered a two-terminal device : LED side pins shorted together, and DETECTOR side pins shorted together.

Recommended Operating Conditions

Characteristics	Symbol	Min	√ Тур.	Max	Unit
Supply Voltage		()	-	160	V
Forward Current	F	5	7.5	15	mA
ON-State Current	ION	> _	_	50	mA
Operating Temperature	Topr	-20	_	60	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Individual Electrical Characteristics (Ta = 25°C)

	Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward Voltage	VF	I _F = 10 mA	1.0	1.15	1.3	V
LED	Reverse Current	I _R	$V_R = 5 V$			10	μA
	Capacitance between terminals	CT	V _F = 0 V, f = 1 MHz		30		pF
DETECTOR	OFF-State Current	IOFF	VOFF = 160 V	Ι		1	nA
DETE	Capacitance between terminals	COFF	V = 0 V, f = 1 MHz	Ι	15	20	pF

Coupled Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Trigger LED Current	I _{FT}	I _{ON} = 50 mA	-	1	3	mA
Return LED Current	IFC	IOFF = 100 μA	0.1			mA
ON-State Resistance	Ron	I _{ON} = 50 mA, I _F = 5 mA	Ń	40	50	Ω

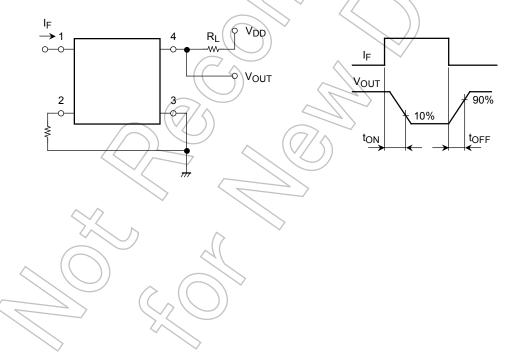
Isolation Characteristics (Ta = 25°C)

vitching Characteristics (Ta = 25°C)						
Isolation Voltage	BVS	AC, 60 s	1500	4		Vrms
Isolation Resistance	Rs	Vs = 500 V, R.H. ≦ 60 %	5×10 ¹⁰	10 ¹⁴	1	Ω
Capacitance Input to Output	Cs	Vs = 0 V, f = 1 MHz		0.8		pF
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit

Switching Characteristics (Ta = 25°C)

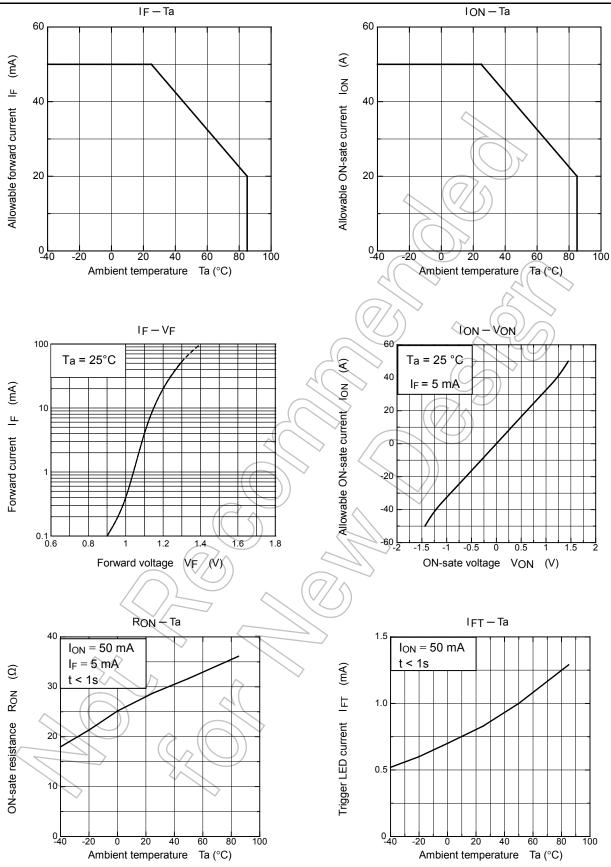
ibol	Test Condition	Min	Тур.	Max	Unit
N R _L = 2	() Ω 00:	Note2)	0.03	0.5	m 0
F VDD =	10 V, I _F	(75)	0.07	0.2	ms
	N $R_L = 2$	$\frac{N}{V_{DC}} = \frac{10}{200} \frac{\Omega}{\Omega} $	$\frac{N}{V_{DD}} = \frac{10}{2} \sqrt{1} r_{T} = 5 \text{ mA} $ (Note2)	$\frac{N}{N} = \frac{10 \text{ V}}{10 \text{ V}} = 5 \text{ mA} $ (Note2)	N $R_L = 200 \Omega$ (Note2) 0.03 0.5

Note2 : Switching Time Test Circuit



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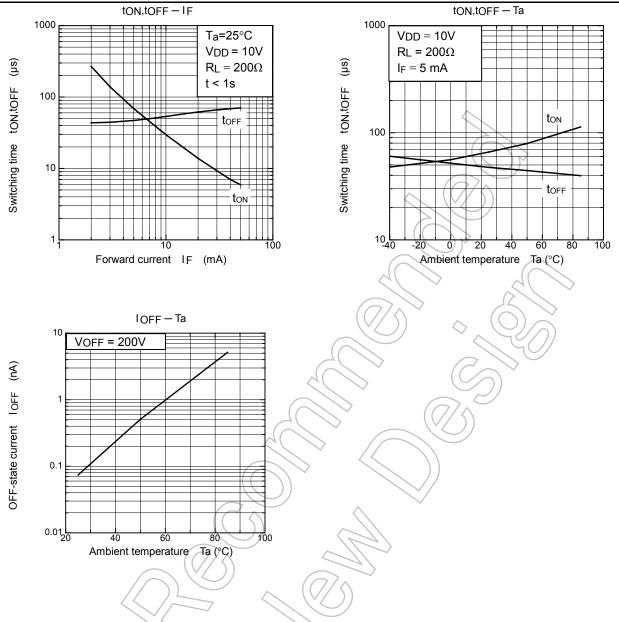
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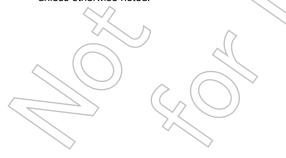
NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

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