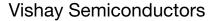
VS-U5FX60FA60





FRED Pt[®] Gen 5 Hyperfast Rectifier Diode, 600 V, 60 A



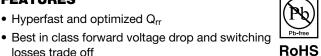
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PRIMARY CHARACTERISTICS								
V _R	600 V							
V _F (typical) at 30 A, per diode	1.6 V							
t _{rr} (typical) at 30 A, per diode	57 ns							
$I_{F(DC)}$ per module at $T_C = 128 \text{ °C}$	60 A							
Туре	Modules - diode, FRED Pt®							
Package	SOT-227							
Circuit configuration	Two separate diodes, parallel pin-out							

FEATURES

losses trade off

Hyperfast and optimized Q_{rr}



COMPLIANT

- · Optimized for high speed operation
- 175 °C maximum operating junction temperature
- · Electrically isolated base plate
- Large creepage distance between terminal
- · Simplified mechanical designs, rapid assembly
- Designed and gualified for industrial level
- UL approved file E78996
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

Featuring a unique combination of low conduction and switching losses, the VS-U5FX60FA60 is the right choice for high frequency converters, both soft switched / resonant. The semiconductor in the SOT-227 package is isolated from the copper base plate, allowing for common heatsinks and compact assemblies to be built.

These modules are specifically designed to improve efficiency of PFC and output rectification stages of EV / HEV battery charging stations, booster stage of solar inverters, and UPS applications, these devices are perfectly matched to operate with MOSFETs or high speed IGBTs.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS			
Cathode to anode voltage	V _R		600	V			
Continuous forward current per diode	I _F	T _C = 128 °C	30	۸			
Single pulse forward current per diode	I _{FSM}	T _J = 25 °C	290	A			
Maximum power dissipation per module	PD	T _C = 128 °C	99	W			
RMS isolation voltage	VISOL	Any terminal to case, t = 1 min	2500	V			
Operating junction and storage temperature range	TJ, T _{Stg}		-55 to +175	°C			

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Cathode to anode breakdown voltage	V _{BR}	I _R = 100 μA	600	-	-		
Forward voltage	V _{FM}	I _F = 30 A	-	1.6	2.1	V	
		I _F = 30 A, T _J = 150 °C	-	1.26	-		
		V _R = 600 V	-	0.1	30		
Reverse leakage current	I _{RM}	$T_{\rm J} = 125 \ ^{\circ}{\rm C}, \ V_{\rm R} = 600 \ {\rm V}$	-	14	-	μA	
		$T_{\rm J} = 150 \ ^{\circ}{\rm C}, \ V_{\rm R} = 600 \ {\rm V}$	-	53	-		

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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST	CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Boyeres resources time	+	$T_J = 25 \ ^\circ C$		-	57	-	ns A	
Reverse recovery time	t _{rr}	T _J = 125 °C		-	62	-		
Doold recovery ourrent		T _J = 25 °C	I _F = 30 A, di _F /dt = 1000 A/μs, V _R = 400 V	-	12	-		
Peak recovery current	I _{RRM}	T _J = 125 °C	$V_{\rm R} = 400 \text{ V}$	-	25	-		
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	0.3	-	μC	
neverse recovery charge		T _J = 125 °C		-	0.9	-	μΟ	
Junction capacitance	CT	V _R = 600 V, f = 1 MHz		-	29	-	pF	

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Thermal resistance junction to case, per diode			-	-	0.95		
Thermal resistance junction to case, per module	H _{thJC}	R _{thJC}	-	-	0.475	°C/W	
Thermal resistance case to heatsink, per module	R _{thCS}	Flat, greased surface	-	0.05	-		
Weight			-	30	-	g	
		Torque per diode	-	-	1.1 (9.7)	Nm (lbf.in)	
Mounting torque		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)	
Case style				SO	Г-227	•	



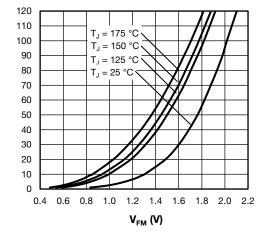


Fig. 1 - Typical Forward Voltage Drop Characteristics

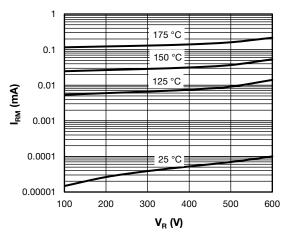


Fig. 2 - Typical Values of Reverse Current



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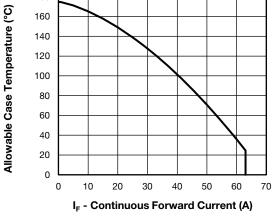


Fig. 3 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Diode)

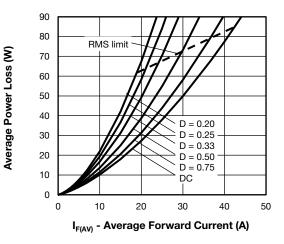


Fig. 4 - Average Power Loss vs. Average Forward Current

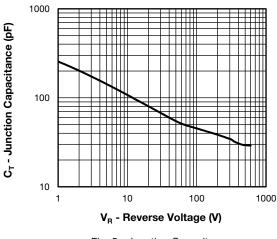


Fig. 5 - Junction Capacitance

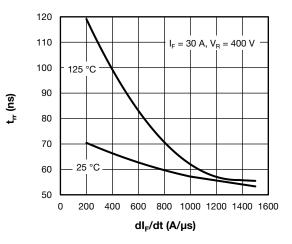
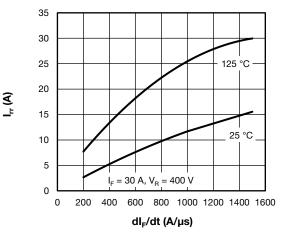


Fig. 6 - Diode Reverse Recovery Time vs. dl_Fdt





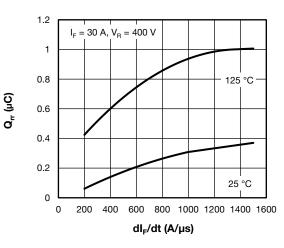


Fig. 8 - Diode Reverse Recovery Charge vs. dl_Fdt

Revision: 19-May-2022

3

Document Number: 96938

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10

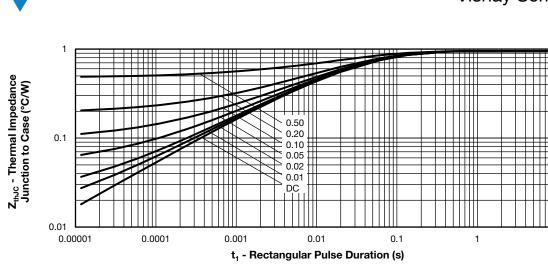


Fig. 9 - Maximum Thermal Impedance Junction to Case

ORDERING INFORMATION TABLE

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ISHAY

Device code	VS-	U5F	х	60	F	Α	60	
		(2)	(3)	(4)	(5)	(6)	(7)	
	1 2 3	- U5F	= Gen	niconduo 5 FRED ast FREI	Pt [®] far	nily		
	4	- Cur	rent rati	ing per r	nodule	(60 = 60) A)	
	믬			0	,	•		des, paralle
	6		•		•		lard ins	ulated base
	7	- Vol	tage rat	ing (60 =	= 600 V)			

CIRCUIT CONFIGURATION							
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING					
Two separate diodes, parallel pin-out	F	Lead Assignment					

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95423				
Packaging information	www.vishay.com/doc?95425				

 Revision: 19-May-2022
 4
 Document Number: 96938

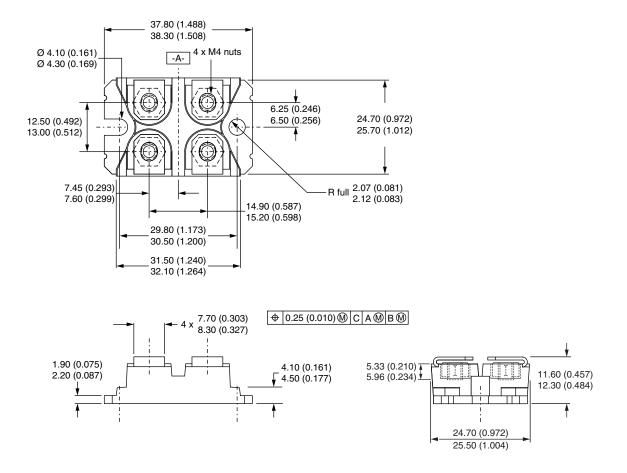
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SOT-227 Generation 2

DIMENSIONS in millimeters (inches)



Note

• Controlling dimension: millimeter



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