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November 2013

FDP036N10A

N-Channel PowerTrench[®] MOSFET 100 V, 214 A, 3.6 m Ω

Features

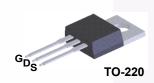
- $R_{DS(on)}$ = 3.2 m Ω (Typ.) @ V_{GS} = 10 V, I_D = 75 A
- · Fast Switching Speed
- Low Gate Charge, Q_G = 89 nC (Typ.)
- High Performance Trench Technology for Extremely Low $R_{DS(\text{on})}$
- · High Power and Current Handling Capability
- RoHS Compliant

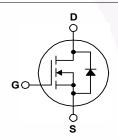
Description

This N-Channel MOSFET is produced using Fairchild Semiconductor's PowerTrench® process that has been tailored to minimize the on-state resistance while maintaining superior switching performance.

Applications

- · Synchronous Rectification for ATX / Server / Telecom PSU
- · Battery Protection Circuit
- · Motor Drives and Uninterruptible Power Supplies
- Micro Solar Inverter





MOSFET Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol		Parameter		Unit
V _{DSS}	Drain to Source Voltage		100	V
V _{GSS}	Gate to Source Voltage		±20	V
		- Continuous (T _C = 25°C, Silicon Limited)	214*	
I _D	Drain Current	- Continuous (T _C = 100°C, Silicon Limited)	151*	Α
		- Continuous (T _C = 25°C, Package Limited)	120	
I _{DM}	Drain Current	- Pulsed (Note 1)	856	Α
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		658	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	6.0	V/ns
D	Dower Dissination	$(T_C = 25^{\circ}C)$	333	W
P_{D}	Power Dissipation	- Derate Above 25°C	2.22	W/°C
T _J , T _{STG}	Operating and Storage Tem	-55 to +175	°C	
T _L	Maximum Lead Temperatur	re for Soldering, 1/8" from Case for 5 Seconds	300	°C

^{*}Calculated continuous current based on maximum allowable junction temperature. Package limitation current is 120A.

Thermal Characteristics

Symbol	Parameter	FDP036N10A	Unit
$R_{ heta JC}$	Thermal Resistance, Junction to Case, Max.	0.45	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	C/VV

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FDP036N10A	FDP036N10A	TO-220	Tube	N/A	N/A	50 units

Electrical Characteristics $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Off Charac	cteristics					
BV _{DSS}	Drain to Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V, T_C = 25^{\circ} C$	100	-	-	V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C	-	0.03	-	V/°C
I _{DSS} Zero Gate Voltage Drain Current	V _{DS} = 80 V, V _{GS} = 0 V	-	-	1	uА	
	Zero Gate voltage Drain Current	$V_{DS} = 80 \text{ V}, T_{C} = 150^{\circ}\text{C}$	-	-	500	μΑ
I _{GSS}	Gate to Body Leakage Current	V _{GS} = ±20 V, V _{DS} = 0 V	-	-	±100	nA

On Characteristics

V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 250 \mu\text{A}$	2.0	3.0	4.0	V
R _{DS(on)}	Static Drain to Source On Resistance	V _{GS} = 10 V, I _D = 75 A	-	3.2	3.6	mΩ
9 _{FS}	Forward Transconductance	V _{DS} = 10 V, I _D = 75 A	-	167	-	S

Dynamic Characteristics

C _{iss}	Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz		5485	7295	pF
Coss	Output Capacitance			2430	3230	pF
C _{rss}	Reverse Transfer Capacitance			210	315	pF
Q _{g(tot)}	Total Gate Charge at 10V		-	89	116	nC
Q_{gs}	Gate to Source Gate Charge	V _{DS} = 80 V, I _D = 75 A, V _{GS} = 10 V		24	-	nC
Q _{gs2}	Gate Charge Threshold to Plateau			8	-	nC
Q_{gd}	Gate to Drain "Miller" Charge	(Note 4	-	25	-	nC
ESR	Equivalent Series Resistance (G-S)	f = 1 MHz	-	1.2	-	Ω

Switching Characteristics

t _{d(on)}	Turn-On Delay Time		-	22	54	ns
t _r	Turn-On Rise Time	$V_{DD} = 50 \text{ V}, I_{D} = 75 \text{ A},$	-	54	118	ns
t _{d(off)}	Turn-Off Delay Time	V_{GS} = 10 V, R_{G} = 4.7 Ω	-/	37	84	ns
t _f	Turn-Off Fall Time	(Note 4)	-	11	32	ns

Drain-Source Diode Characteristics

I _S	Maximum Continuous Drain to Source Diod	Maximum Continuous Drain to Source Diode Forward Current			214	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current			-	856	Α
V_{SD}	Drain to Source Diode Forward Voltage V _{GS} = 0 V, I _{SD} = 75 A		-	-	1.25	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _{SD} = 75 A,	-	72	93.6	ns
Q _{rr}	Reverse Recovery Charge $dI_F/dt = 100 \text{ A/}\mu\text{s}$		-	129	-	nC

Notes:

- 1. Repetitive rating: pulse-width limited by maximum junction temperature.
- 2. Starting T_J = 25°C, L = 1 mH, I_{AS} = 36.3 A.
- 3. I_{SD} \leq 75 A, di/dt \leq 200 A/µs, V_DD \leq BV_DSS, starting T_J = 25°C.
- 4. Essentially independent of operating temperature typical characteristics.

Typical Performance Characteristics

Figure 1. On-Region Characteristics

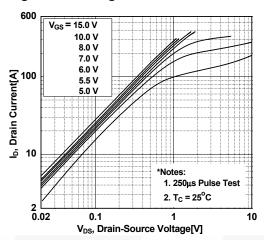


Figure 3. On-Resistance Variation vs.
Drain Current and Gate Voltage

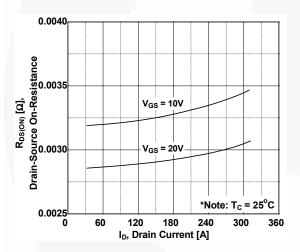


Figure 5. Capacitance Characteristics

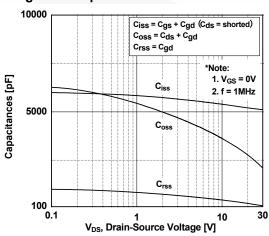


Figure 2. Transfer Characteristics

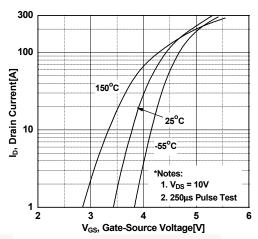


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

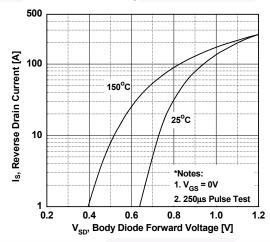
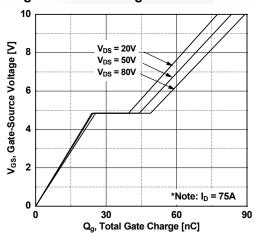


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics

Figure 7. Breakdown Voltage Variation vs. Temperature

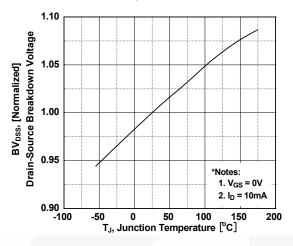


Figure 9. Maximum Safe Operating Area

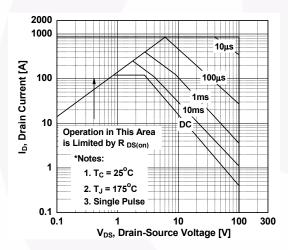


Figure 8. On-Resistance Variation vs. Temperature

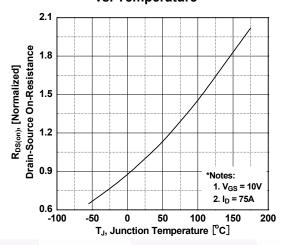


Figure 10. Maximum Drain Current vs. Case Temperature

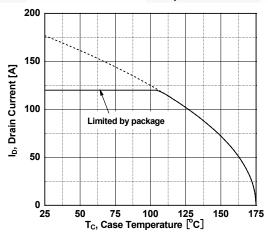
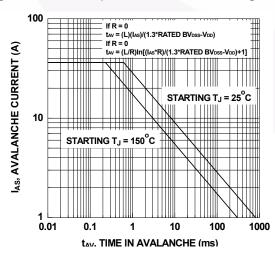
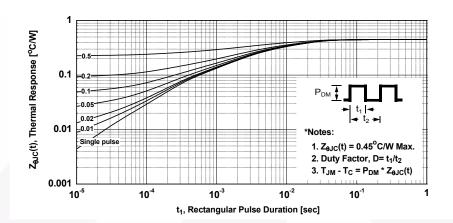


Figure 11. Unclamped Inductive Switching Capability



Typical Performance Characteristics

Figure 12. Transient Thermal Response Curve



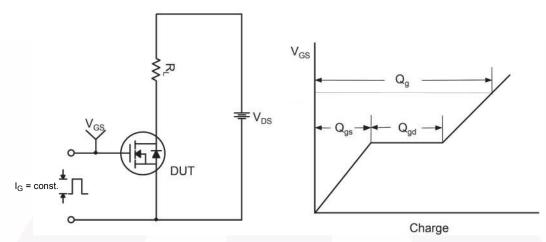


Figure 13. Gate Charge Test Circuit & Waveform

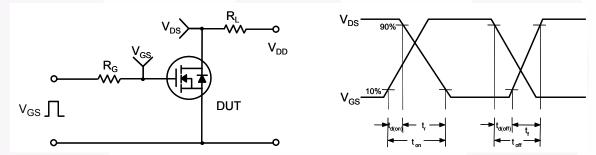


Figure 14. Resistive Switching Test Circuit & Waveforms

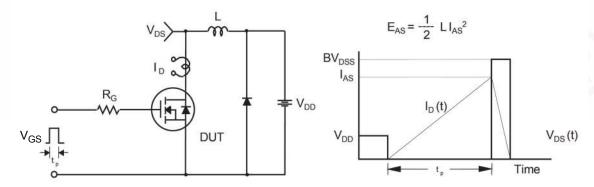


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms

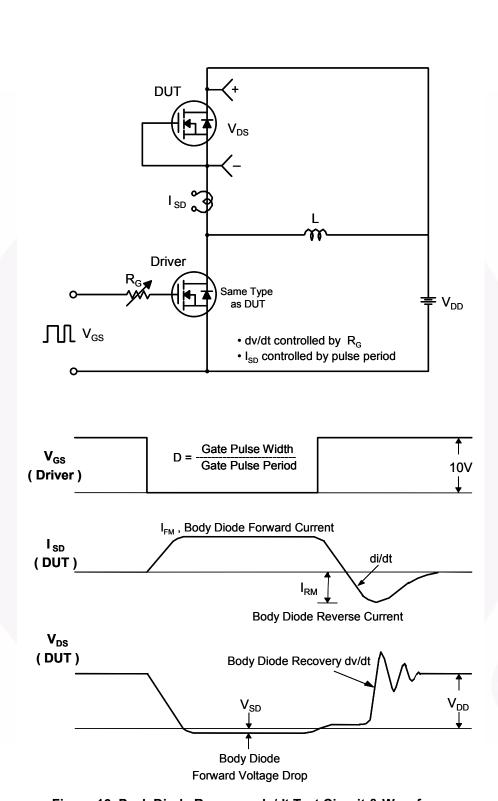


Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms

Mechanical Dimensions

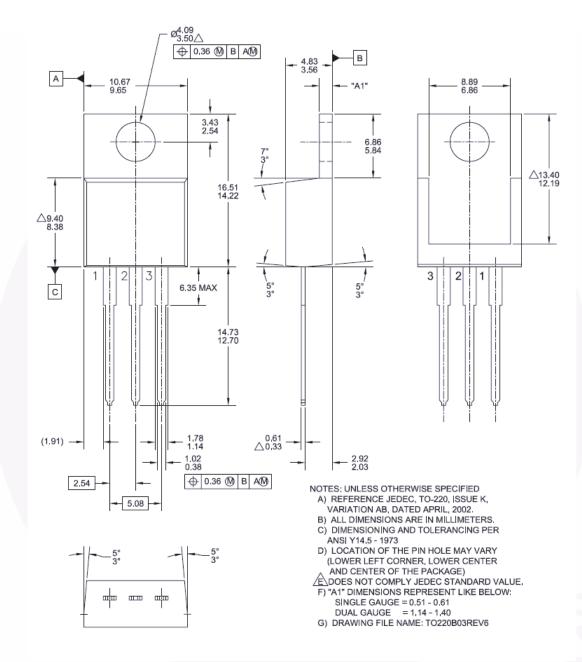


Figure 17. TO-220, Molded, 3-Lead, Jedec Variation AB

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