Notice for TAIYO YUDEN Products

[For High Quality and/or Reliability Equipment (Automotive Electronic Equipment / Industrial Equipment)]

Please read this notice before using the TAIYO YUDEN products.

!\ REMINDERS

Product information in this catalog is as of October 2018. All of the contents specified herein are subject to change without notice due to technical improvements, etc. Therefore, please check for the latest information carefully before practical application or use of our products.

Please note that TAIYO YUDEN shall not be in any way responsible for any damages and defects in products or equipment incorporating our products, which are caused under the conditions other than those specified in this catalog or individual product specification sheets.

- Please contact TAIYO YUDEN for further details of product specifications as the individual product specification sheets are available.
- Please conduct validation and verification of our products in actual condition of mounting and operating environment before using our products.
- The products listed in this catalog are intended for use in general electronic equipment (e.g., AV equipment, OA equipment, home electric appliances, office equipment, information and communication equipment), medical equipment classified as Class I or II by IMDRF, industrial equipment, and automotive interior applications, etc. Please be sure to contact TAIYO YUDEN for further information before using the products for any equipment which may directly cause loss of human life or bodily injury (e.g., transportation equipment including, without limitation, automotive powertrain control system, train control system, and ship control system, traffic signal equipment, medical equipment classified as Class III by IMDRF).

Please do not incorporate our products into any equipment requiring high levels of safety and/or reliability (e.g., aerospace equipment, aviation equipment*, medical equipment classified as Class IV by IMDRF, nuclear control equipment, undersea equipment, military equipment).

*Note: There is a possibility that our products can be used only for aviation equipment that does not directly affect the safe operation of aircraft (e.g., in-flight entertainment, cabin light, electric seat, cooking equipment) if such use meets requirements specified separately by TAIYO YUDEN. Please be sure to contact TAIYO YUDEN for further information before using our products for such aviation equipment.

When our products are used even for high safety and/or reliability-required devices or circuits of general electronic equipment, it is strongly recommended to perform a thorough safety evaluation prior to use of our products and to install a protection circuit as necessary.

Please note that unless you obtain prior written consent of TAIYO YUDEN, TAIYO YUDEN shall not be in any way responsible for any damages incurred by you or third parties arising from use of the products listed in this catalog for any equipment requiring inquiry to TAIYO YUDEN or prohibited for use by TAIYO YUDEN as described above.

- Information contained in this catalog is intended to convey examples of typical performances and/or applications of our products and is not intended to make any warranty with respect to the intellectual property rights or any other related rights of TAIYO YUDEN or any third parties nor grant any license under such rights.
- Please note that the scope of warranty for our products is limited to the delivered our products themselves and TAIYO YUDEN shall not be in any way responsible for any damages resulting from a fault or defect in our products. Notwithstanding the foregoing, if there is a written agreement (e.g., supply and purchase agreement, quality assurance agreement) signed by TAIYO YUDEN and your company, TAIYO YUDEN will warrant our products in accordance with such agreement.
- The contents of this catalog are applicable to our products which are purchased from our sales offices or authorized distributors (hereinafter "TAIYO YUDEN's official sales channel"). Please note that the contents of this catalog are not applicable to our products purchased from any seller other than TAIYO YUDEN's official sales channel.
- Caution for Export
 Some of our products listed in this catalog may require specific procedures for export according to "U.S. Export Administration Regulations", "Foreign Exchange and Foreign Trade Control Law" of Japan, and other applicable regulations. Should you have any questions on this matter, please contact our sales staff.

Automotive Application Guide

We classify automotive electronic equipment into the following four application categories and set usable application categories for each of our products. When using our products for automotive electronic equipment, please be sure to check such application categories and use our products accordingly. Should you have any questions on this matter, please contact us.

Category	Automotive Electronic Equipment (Typical Example)
	Engine ECU (Electronically Controlled Fuel Injector)
	Cruise Control Unit
	• 4WS (4 Wheel Steering)
POWERTRAIN	Automatic Transmission
	Power Steering
	HEV/PHV/EV Core Control (Battery, Inverter, DC-DC)
	Automotive Locator (Car location information providing device), etc.
	ABS (Anti-Lock Brake System)
SAFETY	• ESC (Electronic Stability Control)
JAILII	• Airbag
	ADAS (Equipment that directly controls running, turning and stopping), etc.
	• Wiper
	Automatic Door
	Power Window
	Keyless Entry System
BODY & CHASSIS	• Electric Door Mirror
	Interior Lighting
	• LED Headlight
	• TPMS (Tire Pressure Monitoring System)
	Anti-Theft Device (Immobilizer), etc.
	Car Infotainment System
INFOTAINMENT	• ITS/Telematics System
INFOTAINMENT	Instrument Cluster
	• ADAS (Sensor, Equipment that is not interlocked with safety equipment or powertrain), etc.

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MULTILAYER CERAMIC CAPACITORS





■PART NUMBER

J	М	K	3	1	6	Δ	В	J	1	0	6	М	L	Н	Т	Δ
1	2	3		4		5	(6	3)		7		8	9	10	11)	12

△=Blank space

1 Rated voltage

Code	Rated voltage[VDC]
Α	4
J	6.3
L	10
Е	16
Т	25
G	35
U	50
Н	100
Q	250
S	630

3End termination

Code	End termination
K	Plated
J	Soft Termination
S	Cu Internal Electrodes (For High Frequency)
F	High Reliability Application

Z Series Hairie	
Code	Series name
М	Multilayer ceramic capacitor
V	Multilayer ceramic capacitor for high frequency
W	LW reverse type multilayer capacitor

(4)Dimension(L×W)

4) Dimension (L × W)					
Туре	Dimensions (L×W)[mm]	EIA (inch)			
063	0.6 × 0.3	0201			
105	1.0 × 0.5	0402			
	0.52 × 1.0 ※	0204			
107	1.6 × 0.8	0603			
	0.8 × 1.6 🔆	0306			
010	2.0 × 1.25	0805			
212	1.25 × 2.0 💥	0508			
316	3.2 × 1.6	1206			
325	3.2 × 2.5	1210			
432	4.5 × 3.2	1812			

Note: ※LW reverse type(□WK) only

5Dimension tolerance

Code	Туре	L[mm]	W[mm]	T[mm]
Δ	ALL	Standard	Standard	Standard
	063	0.6±0.05	0.3±0.05	0.3±0.05
	105	1.0±0.10	0.5±0.10	0.5±0.10
	107	1.6+0.15/-0.05	0.8+0.15/-0.05	0.8+0.15/-0.05
Α	212	2.0+0.15/-0.05	1.25+0.15/-0.05	0.85±0.10
	212	2.0+0.19/ -0.05	1.25 + 0.15/ - 0.05	1.25+0.15/-0.05
	316	3.2±0.20	1.6±0.20	1.6±0.20
	325	3.2±0.30	2.5±0.30	2.5±0.30
	105	1.0+0.15/-0.05	0.5+0.15/-0.05	0.5+0.15/-0.05
	107	1.6+0.20/-0	0.8+0.20/-0	0.8+0.20/-0
В	212	2.0+0.20/-0	1.25+0.20/-0	0.85±0.10
		2.0+0.20/ -0	1.25 + 0.20/ - 0	1.25+0.20/-0
	316	3.2±0.30	1.6±0.30	1.6±0.30
	105	1.0+0.20/-0	0.5+0.20/-0	0.5+0.20/-0
С	107	1.6+0.25/-0	0.8+0.25/-0	0.8+0.25/-0
	212	2.0+0.25/-0	1.25+0.25/-0	1.25+0.25/-0
	212	2.0±0.15	1.25±0.15	0.85±0.15
K	316	3.2±0.20	1.6±0.20	1.15±0.20
r.	310	3.2 ± 0.20	1.0 ± 0.20	1.6±0.20
	325	3.2±0.50	2.5±0.30	2.5±0.30

Note: cf. STANDARD EXTERNAL DIMENSIONS

Δ= Blank space

6Temperature characteristics code

■ High dielectric type

Code	Applicable		Applicable standard		Temperature range[°C]	Ref. Temp.[°C]	Capacitance change	Capacitance tolerance	Tolerance code							
				0.5	1.450/	±10%	K									
BJ	EIA	X5R	-55~+ 85	25	±15%	±20%	М									
C6	C6 EIA X6S -55~+105 25 ±22%	±10%	K													
		703	703	703	703	703	703	703	×03	703	703	703	33.4 103	25	±2270	±20%
В7	B7 EIA X7R	-55~+125	25	±15%	±10%	K										
	LIA	X/IX	33.4 1 123	23	±1370	±20%	М									
C7	EIA	X7S	-55 ~ +125	25	±22%	±10%	K									
	LIA	7/3	33.4 1 123	23	- 2270	±20%	М									
D7 EIA	LIV A.	X7T	-55 ~ +125	25	+22%/-33%	±10%	K									
	ĭ	A/1	35.4 1 123	25	1 22 70/ 33 70	±20%	М									

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■Temperature compensating type

Code		cable dard	Temperature range [°C]	Ref. Temp.[°C]	Capacitance change	Capacitance tolerance	Tolerance code
						±0.1pF	В
	CG JIS CG	CG		20	20 0±30ppm/°C	±0.25pF	С
00						±0.5pF	D
CG		EIA COG	-55~+125			±1pF	F
EI	EIA		C0G		25		±2%
						±5%	J

7Nominal capacitance

Code (example)	Nominal capacitance
0R5	0.5pF
010	1pF
100	10pF
101	100pF
102	1,000pF
103	0.01 <i>μ</i> F
104	0.1 μ F
105	1.0 <i>μ</i> F
106	10 μ F
107	100 μ F

Note: R=Decimal point

8 Capacitance tolerance

Code	Capacitance tolerance
Α	±0.05pF
В	±0.1pF
С	±0.25pF
D	±0.5pF
G	±2%
J	±5%
K	±10%
М	±20%

Thickness

Code	Thickness[mm]
Р	0.3
Т	0.3
V	0.5
С	0.7(107type or more)
Α	0.8
D	0.85(212type or more)
F	1.15
G	1.25
L	1.6
N	1.9
М	2.5

10Special code

Code	Special code
Н	MLCC for Industrial and Automotive

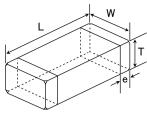
①Packaging

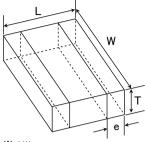
Code	Packaging
F	ϕ 178mm Taping (2mm pitch)
R	ϕ 178mm Embossed Taping (4mm pitch)
Т	ϕ 178mm Taping (4mm pitch)
	ϕ 178mm Taping (4mm pitch, 1000 pcs/reel)
Р	325 type (Thickness code M)

12Internal code

9	
Code	Internal code
Δ	Standard

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LW reverse type

T (=:->		Dime	nsion [mm] (inch)			
Type(EIA)	L	W	Т	*1	е	
□MK063(0201)	0.6±0.03 (0.024±0.001)	0.3±0.03 (0.012±0.001)	0.3±0.03 (0.012±0.001)	Т	0.15±0.05 (0.006±0.002)	
□MK105(0402)	1.0±0.05	0.5±0.05	0.5±0.05		0.25±0.10	
□MF105(0402)	(0.039 ± 0.002)	(0.020 ± 0.002)	(0.020 ± 0.002)	V	(0.010 ± 0.004)	
	0.52±0.05	1.0±0.05	0.3±0.05		0.18±0.08	
□WK105(0204)※	(0.020 ± 0.002)	(0.039 ± 0.002)	(0.012 ± 0.002)	Р	(0.007 ± 0.003)	
□MK107(0603)	1.6±0.10	0.8±0.10	0.8±0.10		0.35±0.25	
□MF107(0603)	(0.063 ± 0.004)	(0.031 ± 0.004)	(0.031 ± 0.004)	Α	(0.014 ± 0.010)	
ΠM H07(0602)	1.6±0.10	0.8±0.10	0.8±0.10	Α	0.35+0.3/-0.25	
□MJ107(0603)	(0.063 ± 0.004)	(0.031 ± 0.004)	(0.031 ± 0.004)	А	(0.014 + 0.012 / -0.010)	
□VS107(0603)	1.6±0.10	0.8±0.10	0.7±0.10	С	0.35±0.25	
	(0.063 ± 0.004)	(0.031 ± 0.004)	(0.028 ± 0.004)	U	(0.014±0.010)	
□WK107(0306)※	0.8 ± 0.10	1.6±0.10	0.5 ± 0.05	V	0.25 ± 0.15	
	(0.031 ± 0.004)	(0.063 ± 0.004)	(0.020 ± 0.002)	•	(0.010 ± 0.006)	
			0.85±0.10	D		
□MK212(0805)	2.0±0.10	1.25±0.10	(0.033 ± 0.004)		0.5±0.25	
□MF212(0805)	(0.079 ± 0.004)	(0.049 ± 0.004)	1.25±0.10	G	(0.020 ± 0.010)	
			(0.049 ± 0.004)			
			0.85±0.10	D		
□MJ212(0805)	2.0±0.10	1.25±0.10	(0.033±0.004)		0.5 + 0.35 / -0.25	
	(0.079 ± 0.004)	(0.049 ± 0.004)	1.25 ± 0.10	G	(0.020 + 0.014 / -0.010)	
	2.0±0.10	1.25±0.10	(0.049±0.004) 0.85±0.10		0.5±0.25	
□VS212(0805)	(0.079 ± 0.004)	(0.049 ± 0.004)	(0.033 ± 0.004)	D	(0.020±0.010)	
	1.25±0.15	2.0±0.15	0.85±0.10		0.3±0.2	
□WK212(0508)※	(0.049 ± 0.006)	(0.079±0.006)	(0.03 ± 0.004)	D	(0.012 ± 0.008)	
	((1.15±0.10		(
□MK316(1206)	3.2±0.15	1.6±0.15	(0.045 ± 0.004)	F	0.5+0.35/-0.25	
□MF316(1206)	(0.126±0.006)	(0.063±0.006)	1.6±0.20		(0.020 + 0.014 / -0.010)	
			(0.063 ± 0.008)	L		
			1.15±0.10			
	3.2±0.15	1.6±0.15	(0.045 ± 0.004)	F	0.6 + 0.4 / -0.3	
□MJ316(1206)	(0.126 ± 0.006)	(0.063 ± 0.006)	1.6±0.20		(0.024 + 0.016 / -0.012)	
			(0.063 ± 0.008)	L		
			1.15±0.10	_		
			(0.045 ± 0.004)	F		
□MK325(1210)	3.2 ± 0.30	2.5 ± 0.20	1.9±0.20	Ν	0.6 ± 0.3	
□MF325(1210)	(0.126 ± 0.012)	(0.098 ± 0.008)	(0.075 ± 0.008)	IN	(0.024 ± 0.012)	
			2.5 ± 0.20	М		
			(0.098 ± 0.008)			
			1.9±0.20	N		
□MJ325(1210)	3.2±0.30	2.5±0.20	(0.075±0.008)		0.6 + 0.4 / -0.3	
	(0.126 ± 0.012)	(0.098 ± 0.008)	2.5±0.20	М	(0.024 + 0.016 / -0.012)	
	45.10.40	001000	(0.098±0.008)		00100	
□MK432(1812)	4.5±0.40 (0.177±0.016)	3.2±0.30 (0.126±0.012)	2.5±0.20 (0.098±0.008)	М	0.9±0.6 (0.035±0.024)	
	(0.177 ± 0.010)	(U.12U U.U12)	(0.030 ± 0.000)	1	(U.UJJ <u>+</u> U.UZ4)	

Note: X. LW reverse type, *1.Thickness code

(0.177±0.016)

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T	ΓΙΛ (: l-)	Dime	nsion	Standard qu	uantity[pcs]
Туре	EIA (inch)	[mm]	Code	Paper tape	Embossed tape
063	0201	0.3	Т	15000	_
105	0402	0.5	V	10000	
105	0204 ※	0.30	Р	10000	_
		0.7	С	4000	
		0.8	Α	4000	_
	0603	0.8	٨	3000	
107	0003	0.8	Α	(Soft Termination)	_
		0.8	Α		3000
		0.0	A		(Soft Termination)
	0306 💥	0.50	V	_	4000
		0.85	D	4000	_
	0805	1.25	G	_	3000
212	0803	1.25	G		2000
		1.20	u	_	(Soft Termination)
	0508 ※	0.85	D	4000	_
316	1206	1.15	F	_	3000
310	1200	1.6	L,	_	2000
		1.15	F		2000
325	1210	1.9	N		2000
		2.5	М	_	500(T), 1000(P)
432	1812	2.5	М	_	500

Note : ※LW Reverse type(□WK)

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Medium-High Voltage Multilayer Ceramic Capacitors

●107TYPE (Dimension:1.6 × 0.8mm JIS:1608 EIA:0603)

[Temperature Characteristic B7 : X7R($-55 \sim +125 ^{\circ}$ C), C7 : X7S($-55 \sim +125 ^{\circ}$ C)] 0.8mm thickness(A)

Part number 1	Part number 2	Rated voltage [V]	Temperature characteristics		Capacitance	Capacitance	$ an\delta$	HTLT	Thickness*3 [mm]	Note
Part number 1	Part number 2				[F]	tolerance [%]	[%]	Rated voltage x %	Thickness [mm]	Note
HMK107 B7102[]AHT				X7R	1000 p	±10, ±20	3.5	200	0.8 ± 0.10	*1, *2
HMK107 B7152[]AHT				X7R	1500 p	±10, ±20	3.5	200	0.8±0.10	*1, *2
HMK107 B7222[AHT				X7R	2200 p	±10, ±20	3.5	200	0.8±0.10	*1, *2
HMK107 B7332∏AHT				X7R	3300 p	±10, ±20	3.5	200	0.8±0.10	*1, *2
HMK107 B7472[]AHT				X7R	4700 p	±10, ±20	3.5	200	0.8±0.10	*1, *2
HMK107 B7682∏AHT				X7R	6800 p	±10, ±20	3.5	200	0.8±0.10	*1, *2
HMK107 B7103∏AHT		100		X7R	0.01 μ	±10, ±20	3.5	200	0.8±0.10	*1, *2
HMK107 B7153∏AHT				X7R	0.015 μ	±10, ±20	3.5	200	0.8±0.10	*1, *2
HMK107 B7223∏AHT				X7R	0.022 μ	±10, ±20	3.5	200	0.8±0.10	*1, *2
HMK107 B7333[]AHT				X7R	0.033 μ	±10, ±20	3.5	200	0.8 ± 0.10	*1, *2
HMK107 B7473[]AHT				X7R	0.047 μ	±10, ±20	3.5	200	0.8±0.10	*1, *2
HMK107 B7104[]AHT				X7R	0.1 μ	±10, ±20	3.5	200	0.8±0.10	*1, *2
HMK107 C7224□AHTE				X7S	0.22 μ	±10, ±20	3.5	150	0.8±0.10	*1, *2

212TYPE (Dimension:2.0 × 1.25mm JIS:2012 EIA:0805)

[Temperature Characteristic B7 : X7R(-55~+125°C), C7 : X7S(-55~+125°C)] 1.25mm thickness(G)

Part number 1	Part number 2	Rated voltage	Temperat	ure	Capacitance	Capacitance	tan δ	HTLT	*3 5 3	Note
Part number 1	Part number 2	[V]	characteristics		[F]	tolerance [%]	[%]	Rated voltage x %	Thickness*3 [mm]	Note
HMK212 B7472[]GHT				X7R	4700 p	±10, ±20	2.5	200	1.25±0.10	*1, *2
HMK212 B7682[]GHT				X7R	6800 p	±10, ±20	2.5	200	1.25±0.10	*1, *2
HMK212 B7103[]GHT				X7R	0.01 μ	±10, ±20	3.5	200	1.25±0.10	*1, *2
HMK212 B7153[]GHT				X7R	0.015 μ	±10, ±20	3.5	200	1.25 ± 0.10	*1, *2
HMK212 B7223[]GHT				X7R	0.022 μ	±10, ±20	3.5	200	1.25±0.10	*1, *2
HMK212 B7333 GHT		100		X7R	0.033 μ	±10, ±20	3.5	200	1.25±0.10	*1, *2
HMK212 B7473[]GHT		100		X7R	0.047 μ	±10, ±20	3.5	200	1.25±0.10	*1, *2
HMK212 B7683∏GHT				X7R	0.068 μ	±10, ±20	3.5	200	1.25±0.10	*1, *2
HMK212 B7104[]GHT				X7R	0.1 μ	±10, ±20	3.5	200	1.25±0.10	*1, *2
HMK212 B7224[]GHT				X7R	0.22 μ	±10, ±20	3.5	200	1.25±0.10	*1, *2
HMK212 C7474[]GHTE				X7S	0.47 μ	±10, ±20	3.5	150	1.25±0.10	*1, *2
HMK212BC7105[]GHTE				X7S	1 μ	±10, ±20	3.5	150	1.25+0.20/-0	*1, *2
QMK212 B7472 GHT				X7R	4700 p	±10, ±20	2.5	150	1.25±0.10	*1, *2
QMK212 B7682 GHT				X7R	6800 p	±10, ±20	2.5	150	1.25±0.10	*1, *2
QMK212 B7103[]GHT		250		X7R	0.01 μ	±10, ±20	2.5	150	1.25±0.10	*1, *2
QMK212 B7153[GHT				X7R	0.015 μ	±10, ±20	2.5	150	1.25±0.10	*1, *2
QMK212 B7223 GHT				X7R	0.022 μ	±10, ±20	2.5	150	1.25±0.10	*1, *2

[Temperature Characteristic B7 : $X7R(-55 \sim +125 ^{\circ}C)$] 0.85mm thickness(D)

Part number 1	Part number 2	Rated voltage [V]	Temperature characteristics		Capacitance	Capacitance tolerance [%]	tan δ [%]	HTLT	Thickness*3 [mm]	Note
					[F]			Rated voltage x %	Inickness [mm]	14006
HMK212 B7102[]DHT				X7R	1000 p	±10, ±20	2.5	200	0.85 ± 0.10	*1, *2
HMK212 B7152[]DHT		100		X7R	1500 p	±10, ±20	2.5	200	0.85 ± 0.10	*1, *2
HMK212 B7222 DHT		100		X7R	2200 p	±10, ±20	2.5	200	0.85 ± 0.10	*1, *2
HMK212 B7332[]DHT				X7R	3300 p	±10, ±20	2.5	200	0.85 ± 0.10	*1, *2
QMK212 B7102 DHT				X7R	1000 p	±10, ±20	2.5	150	0.85 ± 0.10	*1, *2
QMK212 B7152 DHT		250		X7R	1500 p	±10, ±20	2.5	150	0.85 ± 0.10	*1, *2
QMK212 B7222 DHT		230		X7R	2200 p	±10, ±20	2.5	150	0.85 ± 0.10	*1, *2
QMK212 B7332 DHT				X7R	3300 р	±10, ±20	2.5	150	0.85 ± 0.10	*1, *2

316TYPE (Dimension:3.2 × 1.6mm JIS:3216 EIA:1206)

[Temperature Characteristic B7 : X7R($-55 \sim +125^{\circ}$ C), C7 : X7S($-55 \sim +125^{\circ}$ C)] 1.6mm thickness(L)

Part number 1	Part number 2	Rated voltage [V]	Temperature	Capacitance	Capacitance	$ an\delta$	HTLT	Thickness*3 [mm]	Note
Part number 1	Part number 2		characteristics	[F]	tolerance [%]	[%]	Rated voltage x %	Thickness [mm]	Note
HMK316 B7473[]LHT			X7R	0.047 μ	±10, ±20	3.5	200	1.6±0.20	*1, *2
HMK316 B7683[]LHT			X7R	0.068 μ	±10, ±20	3.5	200	1.6±0.20	*1, *2
HMK316 B7104[]LHT			X7R	0.1 μ	±10, ±20	3.5	200	1.6±0.20	*1, *2
HMK316 B7154□LHT			X7R	0.15 μ	±10, ±20	3.5	200	1.6±0.20	*1, *2
HMK316 B7224□LHT		100	X7R	0.22 μ	±10, ±20	3.5	200	1.6±0.20	*1, *2
HMK316 B7334□LHT			X7R	0.33 μ	±10, ±20	3.5	200	1.6±0.20	*1, *2
HMK316 B7474□LHT			X7R	0.47 μ	±10, ±20	3.5	200	1.6±0.20	*1, *2
HMK316 B7105□LHT			X7R	1 μ	±10, ±20	3.5	200	1.6±0.20	*1, *2
HMK316AC7225∏LHTE			X7S	2.2 μ	±10, ±20	3.5	150	1.6±0.20	*1, *2
QMK316 B7223 LHT			X7R	0.022 μ	±10, ±20	2.5	150	1.6±0.20	*1, *2
QMK316 B7333 LHT			X7R	0.033 μ	±10, ±20	2.5	150	1.6±0.20	*1, *2
QMK316 B7473[LHT		250	X7R	0.047 μ	±10, ±20	2.5	150	1.6±0.20	*1, *2
QMK316 B7683[LHT			X7R	0.068 μ	±10, ±20	2.5	150	1.6±0.20	*1, *2
QMK316 B7104[LHT			X7R	0.1 μ	±10, ±20	2.5	150	1.6±0.20	*1, *2
SMK316 B7153[]LHT		630	X7R	0.015 μ	±10, ±20	2.5	120	1.6±0.20	*1, *2
SMK316 B7223 LHT		030	X7R	0.022 μ	±10, ±20	2.5	120	1.6±0.20	*1, *2

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[Temperature Characteristic B7 : X7R(-55~+125°C)] 1.15mm thickness(F)

Part number 1	Part number 2	Rated voltage [V]	Temperature characteristics		Capacitance	Capacitance tolerance [%]	tan δ [%]	HTLT	Thickness*3 [mm]	Note	
	Part number 2				[F]			Rated voltage x %	Thickness [mm]	Note	
SMK316	B7102[FHT				X7R	1000 p	±10, ±20	2.5	120	1.15±0.10	*1, *2
SMK316	B7152[]FHT				X7R	1500 p	±10, ±20	2.5	120	1.15±0.10	*1, *2
SMK316	B7222[]FHT				X7R	2200 p	±10, ±20	2.5	120	1.15±0.10	*1, *2
SMK316	B7332∏FHT		630		X7R	3300 p	±10, ±20	2.5	120	1.15±0.10	*1, *2
SMK316	B7472[]FHT		Ī		X7R	4700 p	±10, ±20	2.5	120	1.15±0.10	*1, *2
SMK316	B7682[FHT				X7R	6800 p	±10, ±20	2.5	120	1.15±0.10	*1, *2
SMK316	B7103[FHT		Ī		X7R	0.01 μ	±10, ±20	2.5	120	1.15±0.10	*1, *2

325TYPE (Dimension:3.2 × 2.5mm JIS:3225 EIA:1210)

[Temperature Characteristic B7 : X7R($-55 \sim +125 ^{\circ}$ C), C7 : X7S($-55 \sim +125 ^{\circ}$ C)] 2.5mm thickness(M)

Part number 1	Part number 2	Rated voltage [V]	erature eristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Note
HMK325 B7225□MHP		100	X7R	2.2 μ	±10, ±20	3.5	200	2.5 ± 0.20	*1, *2
HMK325 C7475∏MHPE		100	X7S	4.7 μ	±10, ±20	3.5	150	2.5±0.20	*1, *2

Part number 1	Part number 2	Rated voltage	Temperature characteristics		Capacitance	Capacitance tolerance [%]	tan δ [%]	HTLT	Thickness*3 [mm]	Note
Part number 1	Part number 2	[V]			[F]			Rated voltage x %	Inickness [mm]	Note
HMK325 B7224[]NHT				X7R	0.22 μ	±10, ±20	3.5	200	1.9±0.20	*1, *2
HMK325 B7474[]NHT		100		X7R	0.47 μ	±10, ±20	3.5	200	1.9±0.20	*1, *2
HMK325 B7684[]NHT		100		X7R	0.68 μ	±10, ±20	3.5	200	1.9±0.20	*1, *2
HMK325 B7105[]NHT				X7R	1 μ	±10, ±20	3.5	200	1.9±0.20	*1, *2
QMK325 B7473[NHT				X7R	0.047 μ	±10, ±20	2.5	150	1.9±0.20	*1, *2
QMK325 B7104 NHT		250		X7R	0.1 μ	±10, ±20	2.5	150	1.9±0.20	*1, *2
QMK325 B7154 NHT		230		X7R	0.15 μ	±10, ±20	2.5	150	1.9±0.20	*1, *2
QMK325 B7224 NHT				X7R	0.22 μ	±10, ±20	2.5	150	1.9±0.20	*1, *2
SMK325 B7223[NHT				X7R	0.022 μ	±10, ±20	2.5	120	1.9±0.20	*1, *2
SMK325 B7333[NHT		630		X7R	0.033 μ	±10, ±20	2.5	120	1.9±0.20	*1, *2
SMK325 B7473[NHT			Ť	X7R	0.047 μ	±10, ±20	2.5	120	1.9±0.20	*1, *2

[Temperature Characteristic B7 : $X7R(-55 \sim +125^{\circ}C)$] 1.15mm thickness(F)

Part number 1	Part number 2	Rated voltage [V]	Tempe characte		Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Note
HMK325 B7104[]FHT		100		X7R	0.1 μ	±10, ±20	3.5	200	1.15±0.10	*1, *2

432TYPE (Dimension:4.5 × 3.2mm JIS:4532 EIA:1812)

[Temperature Characteristic B7 : X7R($-55\sim+125^{\circ}$ C)] 2.5mm thickness(M)

Part number 1 Part number 2		Rated voltage	Temperatu	re	Capacitance	Capacitance	$ an\delta$	HTLT	Thickness*3 [mm]	Note
		[V]	characteristics		[F]	[F] tolerance [%]		Rated voltage x %	Thickness [mm]	Note
HMK432 B7474□MHT			X	7R	0.47 μ	±10, ±20	3.5	200	2.5±0.20	*1, *2
HMK432 B7105 MHT		100	X	7R	1 μ	±10, ±20	3.5	200	2.5±0.20	*1, *2
HMK432 B7155□MHT		100	X	7R	1.5 μ	±10, ±20	3.5	200	2.5±0.20	*1, *2
HMK432 B7225 MHT			X	7R	2.2 μ	±10, ±20	3.5	200	2.5±0.20	*1, *2
QMK432 B7104[]MHT			X	7R	0.1 μ	±10, ±20	2.5	150	2.5±0.20	*1, *2
QMK432 B7224[]MHT		250	Х	7R	0.22 μ	±10, ±20	2.5	150	2.5±0.20	*1, *2
QMK432 B7334[]MHT		230	X	7R	0.33 μ	±10, ±20	2.5	150	2.5±0.20	*1, *2
QMK432 B7474[]MHT			X	7R	0.47 μ	±10, ±20	2.5	150	2.5±0.20	*1, *2
SMK432 B7473 MHT			X	7R	0.047 μ	±10, ±20	2.5	120	2.5±0.20	*1, *2
SMK432 B7683 MHT		630	X	7R	0.068 μ	±10, ±20	2.5	120	2.5±0.20	*1, *2
SMK432 B7104 MHT			X	7R	0.1 μ	±10, ±20	2.5	120	2.5±0.20	*1, *2

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Multilayer Ceramic Capacitors

■PACKAGING

1)Minimum Quantity

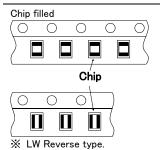
Taped package				
Type(EIA)	Thick			quantity [pcs]
	mm	code	Paper tape	Embossed tape
□MK021(008004)	0.125	K	_	50000
□VS021(008004)				
□MK042(01005)	0.2	C, D		40000
□VS042(01005)	0.2	С		
□MK063(0201)	0.3	P,T	15000	_
□WK105(0204) ※	0.3	Р	10000	_
	0.13	Н	_	20000
□MK105(0402)	0.18	E	_	15000
☐MF105(0402)	0.2	С	20000	_
□WF105(0402)	0.3	Р	15000	_
	0.5	V	10000	_
□VK105(0402)	0.5	W	10000	_
□MK107(0603)	0.45	K	4000	_
□WK107(0306) ※	0.5	V	_	4000
□MF107(0603)	0.8	Α	4000	_
□VS107(0603)	0.7	С	4000	_
□MJ107(0603)	0.8	Α	3000	3000
□MK212(0805)	0.45	K	4000	
□WK212(0508) ※	0.85	D	4000	_
□MF212(0805)	1.25	G	_	3000
□VS212(0805)	0.85	D	4000	_
	0.85	D	4000	_
□MJ212(0805)	1.25	G	_	2000
	0.85	D	4000	_
☐MK316(1206)	1.15	F	_	3000
□MF316(1206)	1.6	L	_	2000
	1.15	F	_	3000
□MJ316(1206)	1.6	L	_	2000
	0.85	D		
□MK325(1210) □MF325(1210)	1.15	F		
	1.9	N	┥ -	2000
	2.0max.	Y	7	
	2.5	М	_	1000
	1.9	N	_	2000
□MJ325(1210)	2.5	M	_	500(T), 1000(P)
□MK432(1812)	2.5	M	_	500

Note:

K LW Reverse type.

**No bottom tape for pressed carrier tape Card board carrier tape Top tape Base tape Sprocket hole Chip cavity Base tape Chip cavity

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3 Representative taping dimensions

 (0.079 ± 0.002)

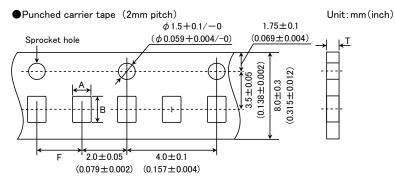
Paper Tape (8mm wide) Pressed carrier tape (2mm pitch) Unit: mm(inch) Sprocket hole ϕ 1.5+0.1/-0 ϕ 1.75±0.1 ϕ 1.75±

Type(EIA)	Chip	Cavity	Insertion Pitch	Tape Thickness	
Type(EIA)	Α	В	F	Т	T1
☐MK063(0201)	0.37	0.67		0.45max.	0.42max.
□WK105(0204) ※			2.0±0.05	0.45max.	0.42max.
□MK105(0402) (*1 C)	0.65	1.15	2.0 ± 0.05	0.4max.	0.3max.
□MK105(0402) (*1 P)				0.45max.	0.42max.

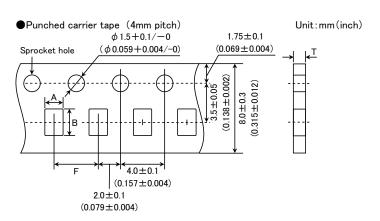
Note *1 Thickness, C:0.2mm ,P:0.3mm. * LW Reverse type.

 (0.157 ± 0.004)

Unit:mm



Type(EIA)	Chip	Cavity	Insertion Pitch	Tape Thickness
Type(EIA)	A B		F	Т
☐MK105 (0402)				
☐MF105 (0402)	0.65	1.15	2.0±0.05	0.8max.
□VK105 (0402)				
				Unit:mm

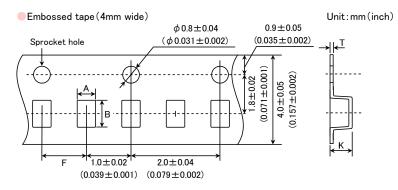


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Type(EIA)	Chip (Cavity	Insertion Pitch	Tape Thickness	
Type(EIA)	Α	В	F	Т	
☐MK107(0603)					
□WK107(0306) ※	1.0	1.8		1.1max.	
☐MF107(0603)			40+01		
☐MK212(0805)	1.65	0.4	4.0±0.1		
□WK212(0508) ※	1.65	2.4		1.1max.	
☐MK316(1206)	2.0	3.6			

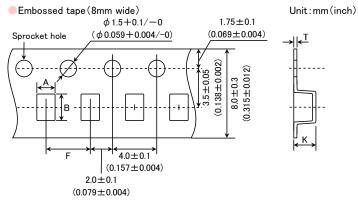
Note: Taping size might be different depending on the size of the product. X LW Reverse type.

Unit:mm



Type(EIA)	Chip (Cavity	Insertion Pitch	Tape Thickness	
Type(EIA)	Α	В	F	K	Т
☐MK021(008004)	0.135	0.27			0.25max.
□VS021(008004)	0.135		1.0±0.02	0.5max.	
☐MK042(01005)	0.23	0.40	1.0 ± 0.02		
□VS042(01005)	0.23	0.43			

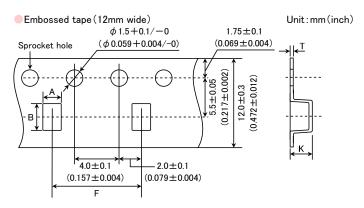
Unit:mm



Type(EIA)	Chip	Cavity	Insertion Pitch	Tape Thickness		
Type(EIA)	Α	В	F	K	Т	
☐MK105(0402)	0.6	1.1	2.0±0.1	0.6max	0.2±0.1	
□WK107(0306) ※	1.0	1.8		1.3max.	0.25±0.1	
☐MK212(0805) ☐MF212(0805)	1.65	2.4	4.0±0.1			
☐MK316(1206) ☐MF316(1206)	2.0	3.6		3.4max.	0.6max.	
☐MK325(1210) ☐MF325(1210)	2.8	3.6				

Note: ※ LW Reverse type. Unit:mm

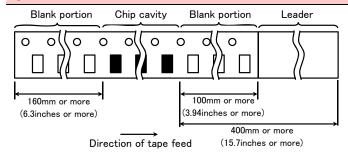
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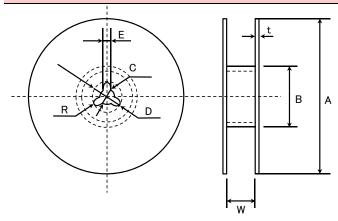
Type(EIA)	Chip (Cavity	Insertion Pitch	Tape Th	nickness
Type(EIA)	Α	В	F	K	Т
☐MK325(1210)	3.1	4.0	8.0±0.1	4.0max.	0.6max.
☐MK432(1812)	3.7	4.9	8.0±0.1	4.0max.	0.6max.

Unit:mm

4 Trailer and Leader



⑤Reel size



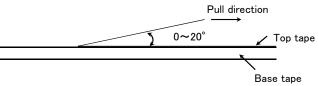
Α	В	С	D	E	R
ϕ 178 ± 2.0	<i>ф</i> 50min.	ϕ 13.0 \pm 0.2	ϕ 21.0 ± 0.8	2.0±0.5	1.0

	T	W
4mm wide tape	1.5max.	5±1.0
8mm wide tape	2.5max.	10±1.5
12mm wide tape	2.5max.	14±1.5

Unit:mm

6Top Tape Strength

The top tape requires a peel-off force of 0.1 to 0.7N in the direction of the arrow as illustrated below.



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Medium-High Voltage Multilayer Ceramic Capacitor

■RELIABILITY DATA

1. Operating Tempe	rature Range						
	Temperature Compensating(High Frequency type) CG(C0G) : -55 to +125°C						
Specified Value	High permittivity						
	X7R, X7S : −55 to +125°C						
	X5 : −55 to +85°C						
	B : −25 to +85°C						
2. Storage Tempera	atura Panga						
z. Storage Tempera							
	Temperature Compensating(High Frequency type)						
	CG(COG) : −55 to +125°C						
Specified Value	High permittivity						
opcomod value	X7R, X7S : −55 to +125°C						
	X5R : −55 to +85°C						
	B : −25 to +85°C						
3. Rated Voltage							
Specified Value	100VDC(HMK,HMJ), 250VDC(QMK,QMJ,QVS), 630VDC(SMK,SMJ)						
4. Withstanding Vol	tage (Between terminals)						
Specified Value	No breakdown or damage						
Test Methods and	Applied voltage : Rated voltage × 2.5 (HMK,HMJ), Rated voltage × 2 (QMK,QMJ,QVS), Rated voltage × 1.2 (SMK,SMJ)						
Remarks	Duration : 1 to 5sec.						
- Comarks	Carge/discharge current : 50mA max.						
5. Insulation Resis	ance						
	Temperature Compensating(High Frequency type)						
	1000010						

5. Insulation Resista	ance	
Specified Value	Temperature Compensating $10000M\Omega$ min	(High Frequency type)
	High permittivity $100M \Omega \mu F$ or $10G \Omega$ whichever	ver is smaller.
Test Methods and Remarks	Applied voltage Duration Charge / discharge current	: Rated voltage(HMK,HMJ, QMK,QMJ,QVS), 500V(SMK,SMJ) : 60±5sec. : 50mA may

6. Capacitance (To	olerance)		
Specified Value	Temperature Compensating(High Frequency type) $\pm 0.1 pF (C < 5pF) \ \pm 0.25pF (C < 10pF) \ \pm 0.5pF (5pF \le C < 10pF) \ \pm 2\% (C = 10pF) \ \pm 5\% (C \ge 10pF)$		
	High permittivity		
	±10%, ±20%		
	Temperature Compensating(High Frequency type)		
	Measuring frequency	: 1MHz±10%	
	Measuring voltage	: 0.5 to 5Vrms	
Test Methods and	Bias application	: None	
Remarks	High permittivity		
	Measuring frequency	: 1kHz±10%	
	Measuring voltage	: 1±0.2Vrms	
	Bias application	: None	

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7. Q or Dissipation	Factor		
	Temperature Compensa	ing(High Frequency type)	
	C<30pF: Q≧800+20C		
	C≧30pF: Q≧1400	C:Normal Capacitance(/pF)	
Specified Value			
	High permittivity		
	3.5%max(HMK,HMJ)		
	2.5%max(QMK,QMJ, SMK,SMJ)		
	Temperature Compensa	ing(High Frequency type)	
	Measuring frequency	: 1MHz±10%	
	Measuring voltage	: 0.5 to 5Vrms	
Test Methods and	Bas application	: None	
Remarks			
Nomano	High permittivity		
	Measuring frequency	: 1kHz±10%	
	Measuring voltage	: 1±0.2Vrms	
	Bas application	: None	

following equation. $ \frac{(C_{85}-C_{25})}{C_{25}\times\Delta\Gamma} \times 10^6\times \text{[ppm/°C]} $ High permittivity		
Specified Value $ \begin{array}{lll} \text{Specified Value} & \text{High permittivity} \\ \text{B} & : \pm 10\%(-25 \text{ to} + 85^{\circ}\text{C}) \\ \text{X5R} & : \pm 15\%(-55 \text{ to} + 85^{\circ}\text{C}) \\ \text{X7R} & : \pm 15\%(-55 \text{ to} + 125^{\circ}\text{C}) \\ \text{X7S} & : \pm 22\%(-55 \text{ to} + 125^{\circ}\text{C}) \\ \text{Capacitance at 25^{\circ}C} & \text{shall be measured in thermal equilibrium, and the temperature characteristic shall be calculated from the following equation.} \\ & \frac{(C_{85} - C_{25})}{C_{25} \times \Delta T} \times 10^{6} \times [\text{ppm/°C}] \\ \text{High permittivity} \\ \text{Capacitance value at each step shall be measured in thermal equilibrium, and the temperature characteristic shall be calculated from the following equation.} \\ & \text{Test Methods and Remarks} \\ & \text{Test Minimum operating tempeature} \\ & \text{2} & 20^{\circ}\text{C} & 25^{\circ}\text{C} \\ & \text{3} & \text{Maximum operating temperature} \\ & \frac{(C - C_2)}{3} \times 100(\%) \end{array} $	8. Temperature Cha	aracteristic of Capacitance
Specified Value B : ±10%(-25 to +85°C) X5R : ±15%(-55 to +85°C) X7R : ±15%(-55 to +125°C) X7S : ±22%(-55 to +125°C) X7S : ±22%(-55 to +125°C) Temperature Compensating(High Frequency type) Capacitance at 25°C and 85°C shall be measured in thermal equilibrium, and the temperature characteristic shall be calculated from the following equation. \[\begin{array}{c} \(\Cmathcal{C}_{85} \cdot \Cmathcal{C}_{25} \) \times \(\Tau\) \(\Cmathcal{C}_{85} \cdot \Tau\) \times \(\Cmathcal{C}_{95} \cdot \Tau\) \(\Cmathc		
Capacitance at 25°C and 85°C shall be measured in thermal equilibrium, and the temperature characteristic shall be calculated from the following equation. $ \frac{(C_{85}-C_{25})}{C_{25}\times\Delta\Gamma} \times 10^{6}\times[\text{ppm/°C}] $ High permittivity Capacitance value at each step shall be measured in thermal equilibrium, and the temperature characteristic shall be calculated from the following equation. $ \frac{\text{Step}}{R} = \frac{R}{R} \times $	Specified Value	B : ±10%(-25 to +85°C) X5R : ±15%(-55 to +85°C) X7R : ±15%(-55 to +125°C)
C : Capacitance value in Step 1 or Step 3 C2 : Capacitance value in Step 2		Capacitance at 25°C and 85°C shall be measured in thermal equilibrium, and the temperature characteristic shall be calculated from the following equation. $\frac{(C_{85}-C_{25})}{C_{25}\times\Delta \text{T}} \times 10^{6}\times[\text{ppm}/^{\circ}\text{C}]$ High permittivity Capacitance value at each step shall be measured in thermal equilibrium, and the temperature characteristic shall be calculated from the following equation. $\frac{\text{Step} B}{1} \frac{\text{Minimum operating tempeature}}{2} \frac{20^{\circ}\text{C}}{2} \frac{25^{\circ}\text{C}}{3} \frac{3}{1} \frac{\text{Maximum operating temperature}}{1} \frac{(C-C_{2})}{C_{2}} \times 100(\%)$ C : Capacitance value in Step 1 or Step 3

9. Deflection	
Specified Value	Temperature Compensating(High Frequency type) Appearance : No abnormality Capacitance change : ±5% or ±0.5pF, whichever is larger.
	High permittivity Appearance : No abnormality Capacitance change : Within±10%
Test Methods and Remarks	Warp : 1mm (Soft Termination type:3mm) Duration : 10sec. Test board : Glass epoxy-resin substrate Thicknss : 1.6mm Board Warp Warp (Unit: mm)
	Capacitance measurement shall be conducted with the board bent.

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10. Adhesive Strength of Terminal Electrodes Specified Value No terminal separation or its indication. Temperature Compensating(High Frequency type) Applied force : 2N Hooked jig Duration : 10±5sec. Board Test Methods and Remarks High permittivity Applied force : 5N Hooked jig Duration : 30±5sec. Board

11. Solderability			
Specified Value	At least 95% of terminal elect	rode is covered by new solder	
		Eutectic solder	Lead-free solder
Test Methods and	Solder type	H60A or H63A	Sn-3.0Ag-0.5Cu
Remarks	Solder temperature	230±5°C	245±3°C
	Duration	4±1	sec.

	Temperature Compensating(High Frequency type)				
	Appearance : No abnormality				
	Capacitance change : C%≤10pF :±0.25pF C%>10pF :±2.5%				
	Withstanding voltage	(between terminals): No abnormality			
Specified Value High permittivity					
	Appearance	: No abnormality			
	Capacitance change	: Within±15%(HMK,HMJ), ±10%(QMK,QMJ, SMK,SMJ)			
	Dissipation factor	: Inital value			
	Insulation resistance	: Initial value			
	Withstanding voltage	(between terminals): No abnormality			
	Preconditioning	: Thermal treatment(at 150°C for 1hr) Note1 (Only High permittivity)			
Test Methods and	Solder temperature	: 270±5°C			
Remarks	Duration	: 3±0.5sec.			
Remarks	Preheating conditions	: 80 to 100°C, 2 to 5 min. 150 to 200°C, 2 to 5min.			
	Recovery	: 24 ± 2 hrs under the stadard condition Note 3			

13. Temperature C	ycle (Thermal Sho	sk)			
	Temperature Cor	npensating(High Frequency type)			
	Appearance	: No abnormality			
	Capacitance cha	ge : C‰≦10pF:±0.25% C‰>10pF:±2	: C※≦10pF :±0.25% C※>10pF :±2.5%		
	Insulation resista	nce : Initial value	: Initial value		
	Withstanding volt	age (between terminals): No abnormality			
Specified Value	High permittivity				
	Appearance	: No abnormality	: No abnormality		
	Capacitance cha	ge : Within±15%(HMK,HMJ), ±7.5%(QMK	: Within±15%(HMK,HMJ), ±7.5%(QMK,QMJ, SMK,SMJ)		
	Dissipation factor	: Initial value	: Initial value		
	Insulation resistance : Initial value				
	Withstanding volt	/ithstanding voltage (between terminals): No abnormality			
	Preconditioning:	Thermal treatment (at 150°C for 1hr) Note1			
	Conditions for 1	cycle			
	Step	temperature(°C)	Time (min.)		
Test Methods and	1	Minimum operating temperature	30±3min.		
Remarks	2	Normal temperature	2 to 3min.		
	3	Maximum operating temperature	30±3min.		
	4	Normal temperature	2 to 3min.		
	Number of cycles	: 5 times			
	Recovery: 24±2	hrs under the standard condition Note3			

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14. Humidity (Stea	14. Humidity (Steady state)				
	Temperature Compensating(High Frequency type) Appearance: No abnormality				
	Capacitance change	: C※≦10pF :±0.5pF C※>10pF :±5% ※Normal capacitance			
	Insulation resistance	: 1000M Ωmin			
Specified Value High permittivity					
	Appearance	: No abnormality			
	Capacitance change	: Within \pm 15%			
	Dissipation factor	: 7%max(HMK,HMJ), 5%max(QMK,QMJ, SMK,SMJ).			
	Insulation resistance	: 25M $\Omega\mu\!\!\!/\!E$ or 1000M Ω whichever is smaller.			
	Preconditioning	: Thermal treatment(at 150°C for 1hr) Note1 (Only High permittivity)			
Test Methods and	Temperature	: 40±2°C			
Remarks	Humidity	: 90 to 95%RH			
Nemarks	Duration	:500 + 24/-0 hrs			
	Recovery	: 24 ± 2 hrs under the standard condition Note 3			

15. Humidity Loadii	ng				
	Temperature Compensating(High Frequency type)				
	Appearance	: No abnormality			
	Capacitance change	: C $\frac{5}{2.0}$ pF : ± 0.4 pF 2.0pF < C $\frac{5}{10}$ pF : ± 0.75 pF C $\frac{5}{10}$ >10pF : ± 7.5 %			
		: ※Normal capacitance			
	Insulation resistance	: 500M Ωmin			
Specified Value					
	High permittivity				
	Appearance	: No abnormality			
	Capacitance change	: Within±15%			
	Dissipation factor	: 7%max(HMK,HMJ), 5%max(QMK,QMJ, SMK,SMJ).			
	Insulation resistance	: 10M $\Omega\mu\!\!\!/F$ or 500M Ω whichever is smaller.			
	According to JIS 5102 claus	se 9.9.			
	Preconditioning	: Voltage treatment Note2 (Only High permittivity)			
	Temperature	: 40±2°C			
Test Methods and	Humidity	: 90 to 95%RH			
Remarks	Applied voltage	: Rated voltage			
	Charge/discharge current	: 50mA max.			
	Duration	: 500 + 24/-0 hrs			
	Recovery	: 24±2hrs under the standard condition Note3			

16. High Temperatu	re Loading			
	Temperature Compensating(High Frequency type)			
	Appearance	: No abnormality		
	Capacitance change	: C‰≦10pF :±0.3pF C‰>10pF :±3%		
	Insulation resistance	:1000M Ωmin		
Specified Value	High permittivity			
	Appearance	: No abnormality		
	Capacitance change	: Within±15%		
	Dissipation factor	: 7%max(HMK,HMJ), 5%max(QMK,QMJ, SMK,SMJ).		
	Insulation resistance	: 50M $\Omega\mu\!\!\!\!/F$ or 1000M Ω whichever is smaller.		
	According to JIS 5102 claus	se 9.10.		
	Preconditioning	: Voltage treatment Note2 (Only High permittivity)		
Test Methods and	Temperature	: Maximum operating temperature		
Remarks	Applied voltage	: Rated voltage \times 2(HMK,HMJ,QVS) Rated voltage \times 1.5(QMK,QMJ) Rated voltage \times 1.2(SMK,SMJ)		
Remarks	Charge/discharge current	: 50mA max.		
	Duration	: 1000 + 24/-0 hrs		
	Recovery	: 24±2hrs under the standard condition Note3		

Note1 Thermal treatment : Initial value shall be measured after test sample is heat-treated at $150 \pm 0/-10^{\circ}\text{C}$ for an hour and kept at room temperature

for 24±2hours.

Note2 Voltage treatment : Initial value shall be measured after test sample is voltage-treated for an hour at both the temperature and voltage specified in

the test conditions, and kept at room temperature for 24 \pm 2hours.

Note3 Standard condition : Temperature: 5 to 35°C, Relative humidity: 45 to 85 % RH, Air pressure: 86 to 106kPa

When there are questions concerning measurement results, in order to provide correlation data, the test shall be conducted

under the following condition.

Temperature: $20\pm2^{\circ}$ C, Relative humidity: 60 to 70 % RH, Air pressure: 86 to 106kPa Unless otherwise specified, all the tests are conducted under the "standard condition".

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Precautions on the use of Multilayer Ceramic Capacitors

■PRECAUTIONS

1. Circuit Design

- ◆ Verification of operating environment, electrical rating and performance
 - A malfunction of equipment in fields such as medical, aerospace, nuclear control, etc. may cause serious harm to human life or have severe social ramifications.

Therefore, any capacitors to be used in such equipment may require higher safety and reliability, and shall be clearly differentiated from them used in general purpose applications.

Precautions

- ◆Operating Voltage (Verification of Rated voltage)
 - 1. The operating voltage for capacitors must always be their rated voltage or less.
 - If an AC voltage is loaded on a DC voltage, the sum of the two peak voltages shall be the rated voltage or less.
 - For a circuit where an AC or a pulse voltage may be used, the sum of their peak voltages shall also be the rated voltage or less.
 - 2. Even if an applied voltage is the rated voltage or less reliability of capacitors may be deteriorated in case that either a high frequency AC voltage or a pulse voltage having rapid rise time is used in a circuit.

2. PCB Design

Precautions

- ◆Pattern configurations (Design of Land-patterns)
- 1. When capacitors are mounted on PCBs, the amount of solder used (size of fillet) can directly affect the capacitor performance. Therefore, the following items must be carefully considered in the design of land patterns:
 - (1) Excessive solder applied can cause mechanical stresses which lead to chip breaking or cracking. Therefore, please consider appropriate land-patterns for proper amount of solder.
 - (2) When more than one component are jointly soldered onto the same land, each component's soldering point shall be separated by solder-resist.
- ◆Pattern configurations (Capacitor layout on PCBs)

After capacitors are mounted on boards, they can be subjected to mechanical stresses in subsequent manufacturing processes (PCB cutting, board inspection, mounting of additional parts, assembly into the chassis, wave soldering of the boards, etc.). For this reason, land pattern configurations and positions of capacitors shall be carefully considered to minimize stresses.

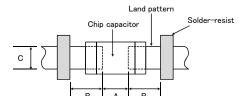
◆Pattern configurations (Design of Land-patterns)

The following diagrams and tables show some examples of recommended land patterns to prevent excessive solder amounts.

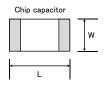
- (1) Recommended land dimensions for typical chip capacitors
- Multilayer Ceramic Capacitors : Recommended land dimensions (unit: mm)

Wave-soldering

Ту	ре	107	212	316	325
Size	L	1.6	2.0	3.2	3.2
Size	W	0.8	1.25	1.6	2.5
A	١	0.8 to 1.0	1.0 to 1.4	1.8 to 2.5	1.8 to 2.5
Е	3	0.5 to 0.8	0.8 to 1.5	0.8 to 1.7	0.8 to 1.7
С		0.6 to 0.8	0.9 to 1.2	1.2 to 1.6	1.8 to 2.5



Land patterns for PCBs



Technical considerations

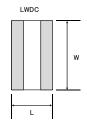
Reflow-soldering

	Notion Soldering								
Туре		042	063	105	107	212	316	325	432
Size	L	0.4	0.6	1.0	1.6	2.0	3.2	3.2	4.5
	W	0.2	0.3	0.5	0.8	1.25	1.6	2.5	3.2
	Α	0.15 to 0.25	0.20 to 0.30	0.45 to 0.55	0.8 to 1.0	0.8 to 1.2	1.8 to 2.5	1.8 to 2.5	2.5 to 3.5
	В	0.15 to 0.20	0.20 to 0.30	0.40 to 0.50	0.6 to 0.8	0.8 to 1.2	1.0 to 1.5	1.0 to 1.5	1.5 to 1.8
С		0.15 to 0.30	0.25 to 0.40	0.45 to 0.55	0.6 to 0.8	0.9 to 1.6	1.2 to 2.0	1.8 to 3.2	2.3 to 3.5

Note: Recommended land size might be different according to the allowance of the size of the product.

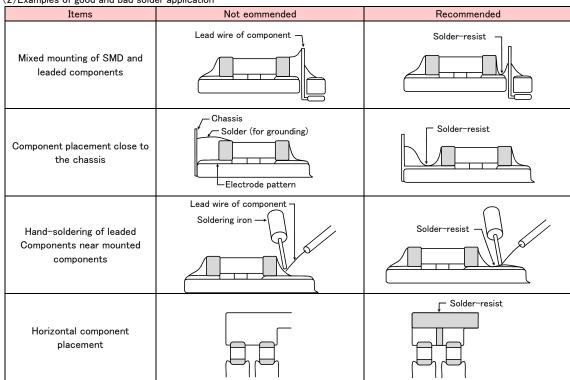
● LWDC: Recommended land dimensions for reflow-soldering

(unit: mm)					
Type		105	107	212	
Size	L	0.52	0.8	1.25	
Size	W	1.0	1.6	2.0	
Α		0.18 to 0.22	0.25 to 0.3	0.5 to 0.7	
В		0.2 to 0.25	0.3 to 0.4	0.4 to 0.5	
С		0.9 to 1.1	1.5 to 1.7	1.9 to 2.1	



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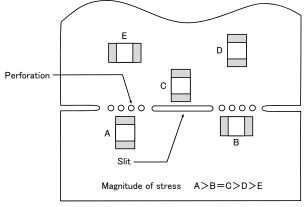
(2) Examples of good and bad solder application



- ◆Pattern configurations (Capacitor layout on PCBs)
 - 1-1. The following is examples of good and bad capacitor layouts; capacitors shall be located to minimize any possible mechanical stresses from board warp or deflection.

Items	Not recommended	Recommended
Deflection of board		Place the product at a right angle to the direction of the anticipated mechanical stress.

1-2. The amount of mechanical stresses given will vary depending on capacitor layout. Please refer to diagram below.



1-3. When PCB is split, the amount of mechanical stress on the capacitors can vary according to the method used. The following methods are listed in order from least stressful to most stressful: push-back, slit, V-grooving, and perforation. Thus, please consider the PCB, split methods as well as chip location.

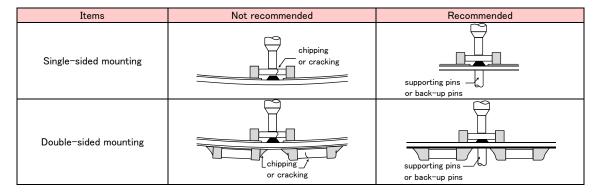
3. Mounting

- ◆Adjustment of mounting machine
 - 1. When capacitors are mounted on PCB, excessive impact load shall not be imposed on them.
 - 2. Maintenance and inspection of mounting machines shall be conducted periodically.
- ◆Selection of Adhesives
 - 1. When chips are attached on PCBs with adhesives prior to soldering, it may cause capacitor characteristics degradation unless the following factors are appropriately checked: size of land patterns, type of adhesive, amount applied, hardening temperature and hardening period. Therefore, please contact us for further information.

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◆Adjustment of mounting machine

- 1. When the bottom dead center of a pick-up nozzle is too low, excessive force is imposed on capacitors and causes damages. To avoid this, the following points shall be considerable.
 - (1) The bottom dead center of the pick-up nozzle shall be adjusted to the surface level of PCB without the board deflection.
 - (2) The pressure of nozzle shall be adjusted between 1 and 3 N static loads.
 - (3) To reduce the amount of deflection of the board caused by impact of the pick-up nozzle, supporting pins or back-up pins shall be used on the other side of the PCB. The following diagrams show some typical examples of good and bad pick-up nozzle placement:



Technical considerations

2. As the alignment pin is worn out, adjustment of the nozzle height can cause chipping or cracking of capacitors because of mechanical impact on the capacitors.

To avoid this, the monitoring of the width between the alignment pins in the stopped position, maintenance, check and replacement of the pin shall be conducted periodically.

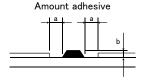
◆Selection of Adhesives

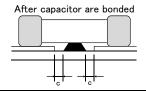
Some adhesives may cause IR deterioration. The different shrinkage percentage of between the adhesive and the capacitors may result in stresses on the capacitors and lead to cracking. Moreover, too little or too much adhesive applied to the board may adversely affect components. Therefore, the following precautions shall be noted in the application of adhesives.

- (1) Required adhesive characteristics
 - a. The adhesive shall be strong enough to hold parts on the board during the mounting & solder process.
 - b. The adhesive shall have sufficient strength at high temperatures.
 - c. The adhesive shall have good coating and thickness consistency.
 - d. The adhesive shall be used during its prescribed shelf life.
 - e. The adhesive shall harden rapidly.
 - f. The adhesive shall have corrosion resistance.
 - g. The adhesive shall have excellent insulation characteristics.
 - h. The adhesive shall have no emission of toxic gasses and no effect on the human body.
- $\begin{tabular}{ll} (2) The recommended amount of adhesives is as follows; \\ \end{tabular}$

[Recommended condition]

Figure	212/316 case sizes as examples
а	0.3mm min
b	100 to 120 μm
С	Adhesives shall not contact land





4. Soldering

Precautions

Technical

considerations

◆Selection of Flux

Since flux may have a significant effect on the performance of capacitors, it is necessary to verify the following conditions prior to use;

- (1) Flux used shall be less than or equal to 0.1 wt% (in Cl equivalent) of halogenated content. Flux having a strong acidity content shall not be applied.
- (2) When shall capacitors are soldered on boards, the amount of flux applied shall be controlled at the optimum level.
- (3) When water-soluble flux is used, special care shall be taken to properly clean the boards.

◆ Soldering

Temperature, time, amount of solder, etc. shall be set in accordance with their recommended conditions.

Sn-Zn solder paste can adversely affect MLCC reliability.

Please contact us prior to usage of Sn-Zn solder.

◆Selection of Flux

1-1. When too much halogenated substance (Chlorine, etc.) content is used to activate flux, or highly acidic flux is used, it may lead to corrosion of terminal electrodes or degradation of insulation resistance on the surfaces of the capacitors.

- 1-2. Flux is used to increase solderability in wave soldering. However if too much flux is applied, a large amount of flux gas may be emitted and may adversely affect the solderability. To minimize the amount of flux applied, it is recommended to use a flux-bubbling system.
- 1-3. Since the residue of water-soluble flux is easily dissolved in moisture in the air, the residues on the surfaces of capacitors in high humidity conditions may cause a degradation of insulation resistance and reliability of the capacitors. Therefore, the cleaning methods and the capability of the machines used shall also be considered carefully when water-soluble flux is used.

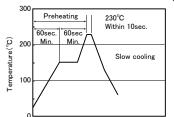
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◆Soldering

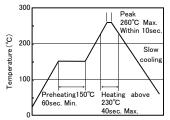
- · Ceramic chip capacitors are susceptible to thermal shock when exposed to rapid or concentrated heating or rapid cooling.
- · Therefore, the soldering must be conducted with great care so as to prevent malfunction of the components due to excessive thermal shock
- Preheating: Capacitors shall be preheated sufficiently, and the temperature difference between the capacitors and solder shall be within 100 to 130°C.
- · Cooling: The temperature difference between the capacitors and cleaning process shall not be greater than 100°C.

[Reflow soldering]

[Recommended conditions for eutectic soldering]

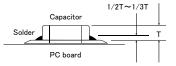


[Recommended condition for Pb-free soldering]



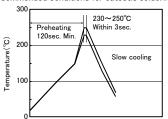
Caution

- 1The ideal condition is to have solder mass(fillet)controlled to 1/2 to 1/3 of the thickness of a capacitor.
- ②Because excessive dwell times can adversely affect solderability, soldering duration shall be kept as close to recommended times as possible.
- 3 Allowable number of reflow soldering: 2 times max.

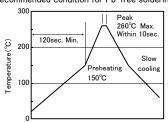


[Wave soldering]

[Recommended conditions for eutectic soldering]



[Recommended condition for Pb-free soldering]

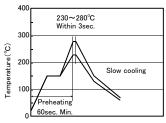


Caution

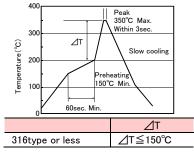
- ①Wave soldering must not be applied to capacitors designated as for reflow soldering only.
- ②Allowable number of wave soldering: 1 times max.

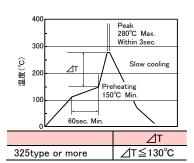
[Hand soldering]

[Recommended conditions for eutectic soldering]



[Recommended condition for Pb-free soldering]





Caution

- ①Use a 50W soldering iron with a maximum tip diameter of 1.0 mm.
- 2The soldering iron shall not directly touch capacitors.
- 3 Allowable number of hand soldering: 1 times max.

5. Cleaning Cleaning conditions 1. When PCBs are cleaned after capacitors mounting, please select the appropriate cleaning solution in accordance with the intended use Precautions of the cleaning. (e.g. to remove soldering flux or other materials from the production process.) 2. Cleaning condition shall be determined after it is verified by using actual cleaning machine that the cleaning process does not affect capacitor's characteristics. 1. The use of inappropriate cleaning solutions can cause foreign substances such as flux residue to adhere to capacitors or deteriorate their outer coating, resulting in a degradation of the capacitor's electrical properties (especially insulation resistance). 2. Inappropriate cleaning conditions (insufficient or excessive cleaning) may adversely affect the performance of the capacitors. In the case of ultrasonic cleaning, too much power output can cause excessive vibration of PCBs which may lead to the Technical cracking of capacitors or the soldered portion, or decrease the terminal electrodes' strength. Therefore, the following conditions shall considerations be carefully checked; Ultrasonic output: 20 W/Q or less Ultrasonic frequency: 40 kHz or less Ultrasonic washing period: 5 min. or less

6. Resin coating and mold

Precautions

1. With some type of resins, decomposition gas or chemical reaction vapor may remain inside the resin during the while left under normal storage conditions resulting in the deterioration of the capacitor's performance.

2. When a resin's hardening temperature is higher than capacitor's operating temperature, the stresses generated by the excessive heat may lead to damage or destruction of capacitors.

The use of such resins, molding materials etc. is not recommended.

7. Handling

♦Splitting of PCB

1. When PCBs are split after components mounting, care shall be taken so as not to give any stresses of deflection or twisting to the board.

2. Board separation shall not be done manually, but by using the appropriate devices.

Precautions

◆Mechanical considerations

Be careful not to subject capacitors to excessive mechanical shocks.

- (1) If ceramic capacitors are dropped onto a floor or a hard surface, they shall not be used.
- (2) Please be careful that the mounted components do not come in contact with or bump against other boards or components.

8. Storage conditions

◆Storage

- 1. To maintain the solderability of terminal electrodes and to keep packaging materials in good condition, care must be taken to control temperature and humidity in the storage area. Humidity should especially be kept as low as possible.
 - Recommended conditions

Precautions

Ambient temperature : Below 30°C
Humidity : Below 70% RH

The ambient temperature must be kept below 40° C. Even under ideal storage conditions, solderability of capacitor is deteriorated as time passes, so capacitors shall be used within 6 months from the time of delivery.

- •Ceramic chip capacitors shall be kept where no chlorine or sulfur exists in the air.
- 2. The capacitance values of high dielectric constant capacitors will gradually decrease with the passage of time, so care shall be taken to design circuits. Even if capacitance value decreases as time passes, it will get back to the initial value by a heat treatment at 150°C for 1hour.

Technical considerations

If capacitors are stored in a high temperature and humidity environment, it might rapidly cause poor solderability due to terminal oxidation and quality loss of taping/packaging materials. For this reason, capacitors shall be used within 6 months from the time of delivery. If exceeding the above period, please check solderability before using the capacitors.

**RCR-2335B(Safety Application Guide for fixed ceramic capacitors for use in electronic equipment) is published by JEITA.

Please check the guide regarding precautions for deflection test, soldering by spot heat, and so on.

This catalog contains the typical specification only due to the limitation of space. When you consider the purchase of our products, please check our specification. For details of each product (characteristics graph, reliability information, precautions for use, and so on), see our Web site (http://www.ty-top.com/).