



60V 175°C N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _C = +25°C (Note 9)
60V	$3.8m\Omega$ @ $V_{GS} = 10V$	100A

Description

This MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$), yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Engine Management Systems
- Body Control Electronics
- DC-DC Converters
- Motor Control

Features

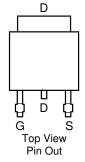
- Rated to +175°C Ideal For High Ambient Temperature Environments
- 100% Unclamped Inductive Switching Ensures More Reliable and Robust End Application
- Low R_{DS(ON)} Minimizes Power Losses
- Low Q_G Minimizes Switching Losses
- Lead-Free Finish; RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- An Automotive-Compliant Part is Available Under Separate Datasheet (<u>DMTH6004SK3Q</u>)

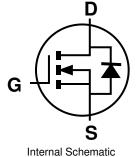
Mechanical Data

- Case: TO252 (DPAK)
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (§3)
- Weight: 0.33 grams (Approximate)









Ordering Information (Note 4)

Part Number	Case	Packaging
DMTH6004SK3-13	TO252 (DPAK)	2,500/Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



=Manufacturer's Marking T6004S = Product Type Marking Code YYWW = Date Code Marking YY = Last Digit of Year (ex: 15 = 2015) WW = Week Code (01 to 53)



Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage	V_{DSS}	60	V		
Gate-Source Voltage	V_{GSS}	±20	V		
Continuous Drain Current (Note 6)	T _C = +25°C (Note 9)	Ι _D	100	А	
	$T_{C} = +100^{\circ}C$		75		
Maximum Body Diode Forward Current (Note 6)	T _C = +25°C	Is	100	Α	
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)	I _{DM}	150	Α		
Avalanche Current, L = 0.2mH	I _{AS}	45	Α		
Avalanche Energy, L = 0.2mH	E _{AS}	200	mJ		

Thermal Characteristics

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)	T _A = +25°C	P_{D}	3.9	W
Thermal Resistance, Junction to Ambient (Note 5)		$R_{\theta JA}$	38	°C/W
Total Power Dissipation (Note 6)	T _C = +25°C	P _D	180	W
Thermal Resistance, Junction to Case (Note 6)		R ₀ JC	0.8	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +175	°C

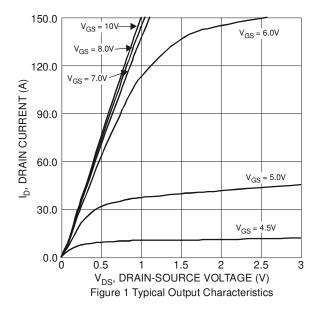
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

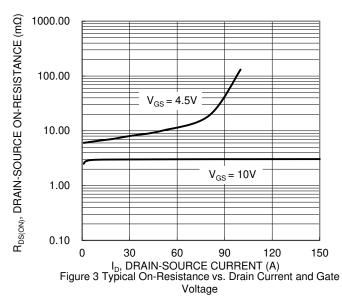
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	_	V	$V_{GS} = 0V$, $I_D = 1mA$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 48V, V_{GS} = 0V$	
Gate-Source Leakage	IGSS	_	_	±100	nA	$V_{GS} = \pm 20V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	2	_	4	٧	$V_{DS} = V_{GS}, \ I_D = 250 \mu A$	
Static Drain-Source On-Resistance	R _{DS} (ON)		3	3.8	mΩ	$V_{GS} = 10V, I_D = 90A$	
Diode Forward Voltage	V_{SD}		0.9	1.2	V	$V_{GS} = 0V, I_S = 20A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{ISS}	_	4,556	_		$V_{DS} = 30V, V_{GS} = 0V, f = 1MHz$	
Output Capacitance	Coss	_	1,383	_	pF		
Reverse Transfer Capacitance	C _{RSS}	_	105.2	_			
Gate Resistance	R _G	_	0.66	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Q_{G}	_	95.4	_		V _{DS} = 30V, I _D = 90A, V _{GS} = 10V	
Gate-Source Charge	Q _{GS}	_	21.6	_	nC		
Gate-Drain Charge	Q_{GD}	_	20.4	_			
Turn-On Delay Time	t _{D(ON)}	_	13.2	_		$V_{DD} = 30V, V_{GS} = 10V,$ $I_{D} = 90A, R_{G} = 3.5\Omega$	
Turn-On Rise Time	t _R	_	11.7	_	20		
Turn-Off Delay Time	t _{D(OFF)}	_	31	_	ns		
Turn-Off Fall Time	t _F	_	12	_			
Body Diode Reverse Recovery Time	t _{RR}	_	50.5	_	ns	F0A di/dt 100A/us	
Body Diode Reverse Recovery Charge	Q _{RR}	_	80.8	_	nC	$I_F = 50A$, di/dt = 100A/ μ s	

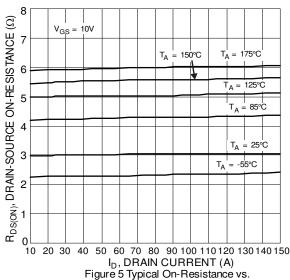
5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout. Notes:

Device infloating off 17-4 substitute 1 to board, 200 copper, with finite square oc.
 Thermal resistance from junction to soldering point (on the exposed drain pad).
 Short duration pulse test used to minimize self-heating effect
 Guaranteed by design. Not subject to production testing
 Package limited.

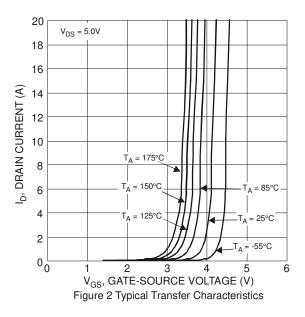


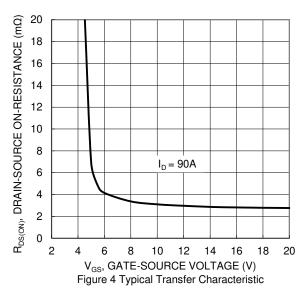






Drain Current and Temperature





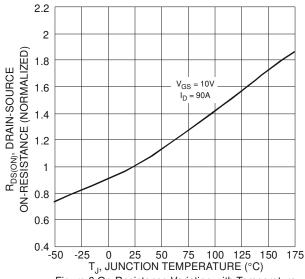
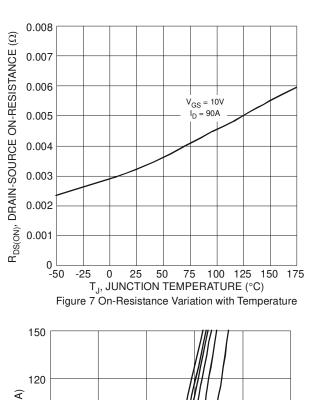
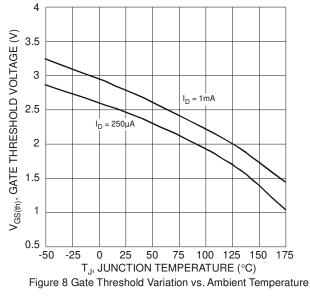
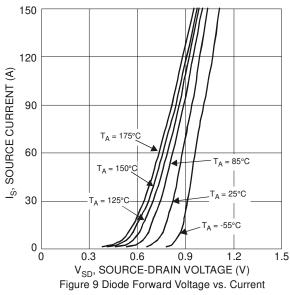


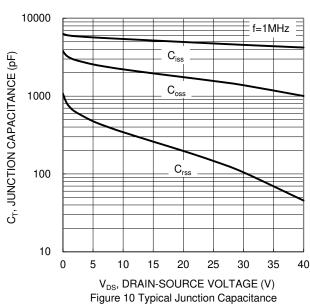
Figure 6 On-Resistance Variation with Temperature

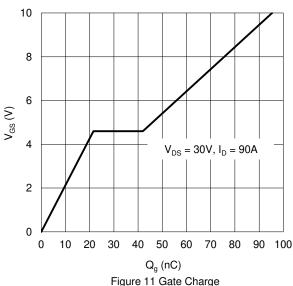












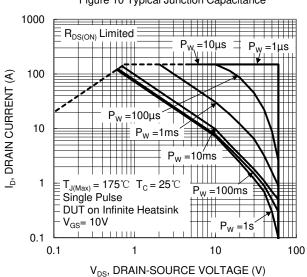


Figure 12 SOA, Safe Operation Area



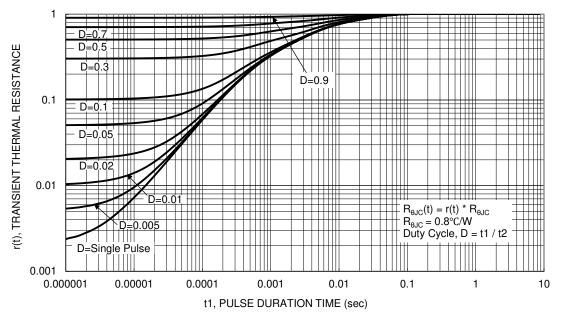


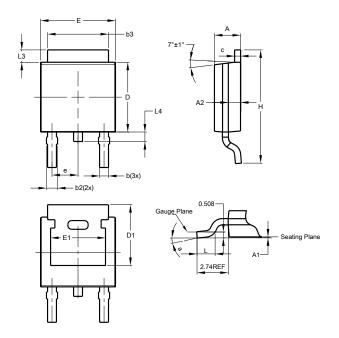
Figure 13 Transient Thermal Resistance



Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

TO252 (DPAK)

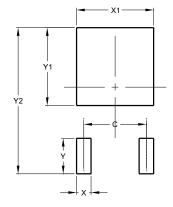


TO252 (DPAK)					
Dim	Min	Max	Тур		
Α	2.19	2.39	2.29		
A 1	0.00	0.13	0.08		
A2	0.97	1.17	1.07		
b	0.64	0.88	0.783		
b2	0.76	1.14	0.95		
b3	5.21	5.46	5.33		
С	0.45	0.58	0.531		
D	6.00	6.20	6.10		
D1	5.21	-	-		
е	-	-	2.286		
Е	6.45	6.70	6.58		
E1	4.32	-	-		
Н	9.40	10.41	9.91		
L	1.40	1.78	1.59		
L3	0.88	1.27	1.08		
L4	0.64	1.02	0.83		
а	0°	10°	-		
All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

TO252 (DPAK)



Dimensions	Value (in mm)		
С	4.572		
X	1.060		
X1	5.632		
Υ	2.600		
Y1	5.700		
V2	10 700		



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