

ELECTRICAL SPECIFICATIONS:

1.0 TURNS RATIO: (P8-P6-P7) : (J1-J2) (P2-P3-P1) : (J3-J6) : 1CT : 1± 3% : 1CT : 1 ± 3% NOTES

1.0 PINS WITHOUT ELECTRICAL CONNECTION ARE OMITTED.

2.0 INDUCTANCE: (P7-P8) (P1-P2)

: 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias : 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias

3.0 LEAKAGE INDUCTANCE: P8-P7 (WITH J1 AND J2 SHORT) : 0.3 MAX. @ 1MHz P2-P1 (WITH J3 AND J6 SHORT) : 0.3 MAX. @ 1MHz

4.0 INTERWINDING CAPACITANCE: (P8,P6,P7) TO (J1,J2) : 30pf MAX @ 1MHz (P3,P2,P1) TO (J6,J3) : 30pf MAX. @ 1MHZ

5.0 DC RESISTANCE: (J1-J2) (P1-P2)

: 1.2 ohms Max. : 1.2 ohms Max.



Bel Stewart Connector 11118 Susquehanna Trail, South

Glen Rock, Pa 17327-9199 717.234.7512

http://www.stewartconnector.com

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~SI-60168-F

RECEIVE

6.0 RETURN LOSS: 1MHz TO 30MHz : 18dB MIN.

60MHz TO 80MHz : 12dB MIN.

7.0 DIELECTRIC WITHSTAND: (J3, J6) TO (P1, P2) (J1, J2) TO (P8,P7) : 1500 VAC

: 1500 VAC

8.0 INSERTION LOSS: RS=RL=100 ohms 100KHz TO 100MHz : 1.1 dB TYP

9.0 RISE TIME: RS=100 OHMS AND RL = 100 OHMS

OUTPUT VOLTAGE = 1 V peak: 3.0 nS MAX : 3.0 nS MAX PULSE WIDTH= 112nS

10.0 CROSS TALK: 1MHz TO 100MHz : 35 dB TYP

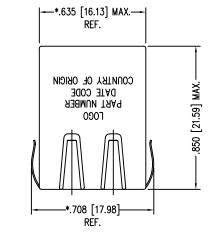
11.0 COMMON TO COMMON MODE ATTENUATION: 1MHz TO 100MHz : 35dB TYP

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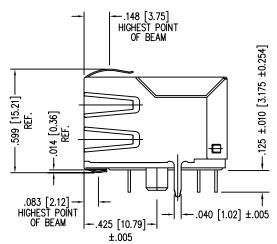
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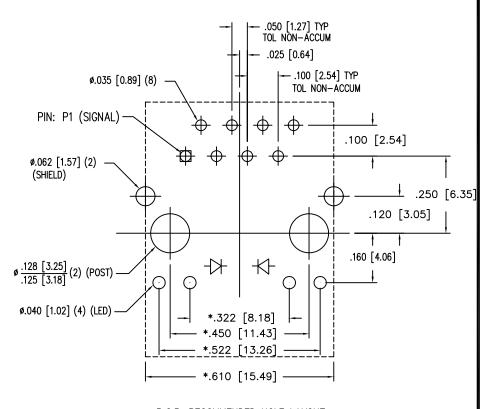


PIN: J1

−.450 [11.43]--

±.005





P.C.B. RECOMMENDED HOLE LAYOUT SEEN FROM COMPONENT SIDE TOLERANCE ±.003 [0.08] UNLESS OTHERWISE SPECIFIED

NOTES:

LED

GREEN

- TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS
- DIMENSIONS SHOWN WITH "*" TO BE CENTRAL ABOUT CENTER LINE
- DIMENSIONS SHOWN ARE SUBJECT TO CHANGE WITHOUT NOTICE.

YELLOW

- PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED. SEE ELECTRICAL DRAWING FOR OMITTED PINS.
- STANDARD 50 MICRO-INCH SELECTIVE GOLD PLATING.

LED SPECIFICATIONS

COLOR: YELLOW **GREEN** FORWARD VOLTAGE(20mA): 2.5v (MAX) 2.5v (MAX) FORWARD VOLTAGE(20mA): 2.1v (TYP) 2.2v (TYP) POWER DISSIPATION: 105mW 105mW

WAVE LENGTH: 590nm

LUMINOUS INTENSITY (10mA): 2-8 MCD



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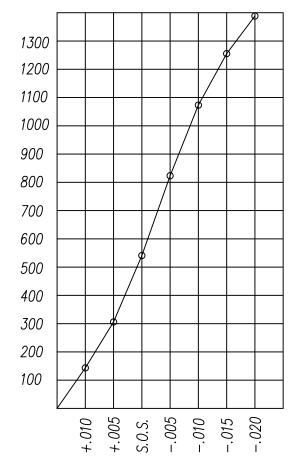
OF 4

565nm

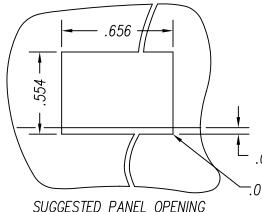
8-32 MCD

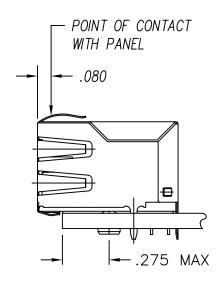
05

DRAWING NO. SI-60168-F



PANEL GROUNDING BEAM DEFLECTION S.O.S. = SUGGESTED OPENING SIZE





THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY. THESE VARIABLES CAN BE ADJUSTED IN EITHER DIRECTION BUT MAY CARRY SOME CONSEQUENCES IN THE FORM OF LOWER MATING FORCES OR TIGHTER ASSEMBLY TOLERANCES. FORCE VALUES ON THE GRAPH ARE GENERAL AVERAGES TAKEN AT THE POINT OF CONTACT SHOWN ABOVE. THE SUGGESTED PANEL OPENING INCLUDES APPROXIMATELY .020 CLEARANCE ON THE SIDES AND TOP AND .013 ON THE BOTTOM, AT PANEL OPENING.

.000 (TOP OF PCB TO BOTTOM OF OPENING)

-.010 MAX. RADIUS(4)

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