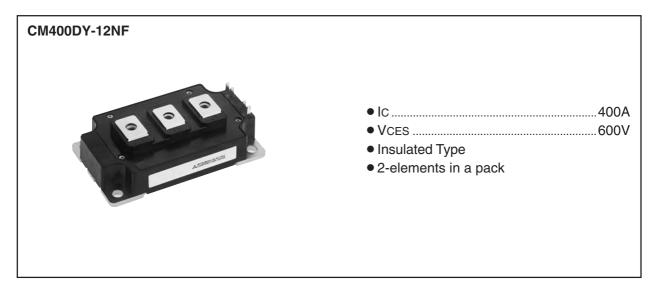
**MITSUBISHI IGBT MODULES** 

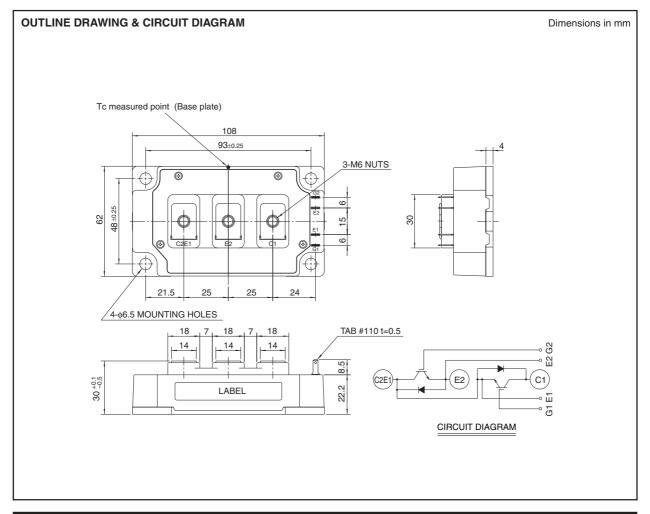
# **CM400DY-12NF**

HIGH POWER SWITCHING USE



#### **APPLICATION**

General purpose inverters & Servo controls, etc







# **CM400DY-12NF**

#### **HIGH POWER SWITCHING USE**

#### MAXIMUM RATINGS (Tj = 25°C, unless otherwise specified)

Symbol	Parameter	Conditions		Ratings	Unit
VCES	Collector-emitter voltage	G-E Short		600	V
VGES	Gate-emitter voltage	C-E Short		±20	V
Ic	Collector current	DC, $Tc' = 92^{\circ}C^{*3}$		400	Α
Ісм	Collector current	Pulse (I	Note 2)	800	Α
IE (Note 1)	Emitter current			400	Α
IEM (Note 1)	Emiller current	Pulse (I	Note 2)	800	Α
PC (Note 3)	Maximum collector dissipation	Tc = 25°C		1130	W
Tj	Junction temperature			-40 ~ +150	°C
Tstg	Storage temperature			<b>−</b> 40 ~ +125	°C
Viso	Isolation voltage	Terminals to base plate, f = 60Hz, AC 1 minute		2500	Vrms
_	To you a patro postly	Main terminals M6 screw		3.5 ~ 4.5	N•m
_	Torque strength	Mounting M6 screw		3.5 ~ 4.5	N•m
_	Weight	Typical value		400	g

#### ELECTRICAL CHARACTERISTICS (Tj = 25°C, unless otherwise specified)

0	Parameter	Test conditions		Limits			1.1
Symbol	Parameter			Min.	Тур.	Max.	Unit
ICES	Collector cutoff current	VCE = VCES, VGE = 0V			_	1	mA
VGE(th)	Gate-emitter threshold voltage	IC = 40mA, VCE = 10V		5	6	7.5	V
IGES	Gate leakage current	±VGE = VGES, VCE = 0V		_	_	0.5	μΑ
VCE(sat)	Collector-emitter saturation voltage	IC = 400A, VGE = 15V	Tj = 25°C		1.7	2.2	V
			Tj = 125°C		1.7	_	
Cies	Input capacitance	VCE = 10V VGE = 0V			_	60	nF
Coes	Output capacitance				_	7.3	nF
Cres	Reverse transfer capacitance			_	_	2.4	nF
QG	Total gate charge	VCC = 300V, IC = 400A, VGE = 15V		_	1600	_	nC
td(on)	Turn-on delay time	VCC = 300V, IC = 400A $VGE = \pm 15V$ $RG = 3.1\Omega, Inductive load IE = 400A$			_	300	ns
tr	Turn-on rise time			_	_	200	ns
td(off)	Turn-off delay time				_	450	ns
tf	Turn-off fall time			-	_	300	ns
trr (Note 1)	Reverse recovery time			_	_	250	ns
Qrr (Note 1)	Reverse recovery charge			_	6.8	_	μC
VEC(Note 1)	Emitter-collector voltage	IE = 400A, VGE = 0V			_	2.6	V
Rth(j-c)Q	Thermal resistance*1	IGBT part (1/2 module)		_	_	0.11	K/W
Rth(j-c)R	Thermal resistance	FWDi part (1/2 module)		_	_	0.19	K/W
Rth(c-f)	Contact thermal resistance	Case to heat sink, Thermal compound Applied*2 (1/2 module)		_	0.04		K/W
Rth(j-c')Q	Thermal resistance	Case temperature measured point is just under the chips			_	0.066*3	K/W
RG	External gate resistance			1.6	_	16	Ω



Feb. 2009

<sup>\*1 :</sup> Case temperature (Tc) measured point is shown in page OUTLINE DRAWING. \*2 : Typical value is measured by using thermally conductive grease of  $\lambda$  = 0.9[W/(m • K)].

<sup>\*3 :</sup> Case temperature (Tc') measured point is just under the chips.

If you use this value, Rth(f-a) should be measured just under the chips.

Note 1. IE, VEC, trr & Qrr represent characteristics of the anti-parallel, emitter-collector free-wheel diode (FWDi).

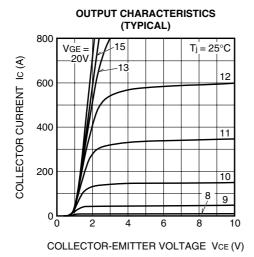
2. Pulse width and repetition rate should be such that the device junction temperature (Tj) does not exceed T<sub>jmax</sub> rating.

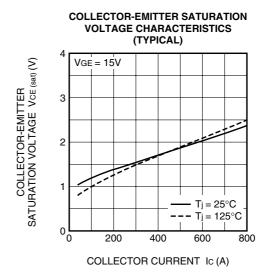
3. Junction temperature (Tj) should not increase beyond 150°C.

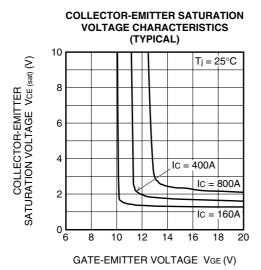
### **CM400DY-12NF**

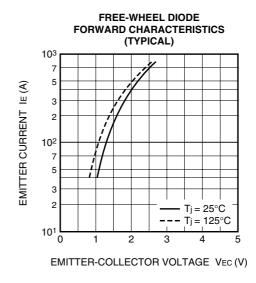
#### HIGH POWER SWITCHING USE

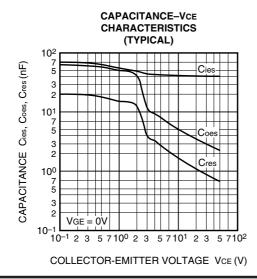
#### **PERFORMANCE CURVES**

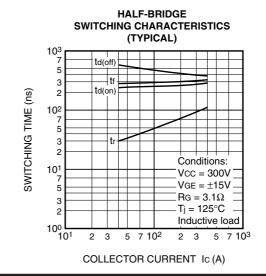












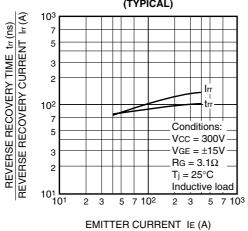


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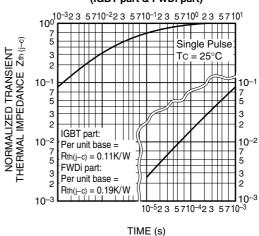
## **CM400DY-12NF**

#### **HIGH POWER SWITCHING USE**

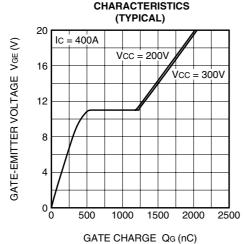
# REVERSE RECOVERY CHARACTERISTICS OF FREE-WHEEL DIODE (TYPICAL)



# TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (IGBT part & FWDi part)



# GATE CHARGE CHARACTERISTICS





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