

Evaluation Board for the 800 mA, 3 MHz Buck Regulator

FEATURES

- Full-featured evaluation board for the [ADP2147](#)**
- Standalone capability**
- Simple device measurements, including line and load regulation, demonstrable with**
 - A single voltage supply (added indent)**
 - A voltmeter**
 - A current meter**
 - Load resistors**
- Easy access to external components**

GENERAL DESCRIPTION

The ADP2147 is a high efficiency, low quiescent current, synchronous, step-down, dc-to-dc converter.

The buck regulator automatically switches operating modes, depending on the load current level. At higher output loads, the buck regulator operates in PWM mode. When the load current falls below a predefined threshold, the regulators operate in power save mode (PSM), improving the light-load efficiency. The output voltage can be changed between two different settings under the control of a VSEL pin.

The 3 MHz switching frequency minimizes the size of external components. Full details on the ADP2147 are provided in the ADP2147 data sheet available from Analog Devices, Inc., which should be consulted in conjunction with this user guide.

DIGITAL PICTURE OF THE EVALUATION BOARD

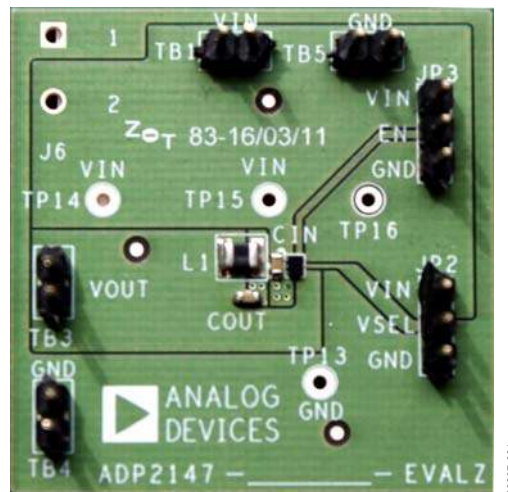


Figure 1.

TABLE OF CONTENTS

Features	1	Powering Up the Evaluation Board	3
General Description	1	Measuring Evaluation Board Performance	3
Digital Picture of the Evaluation Board.....	1	Evaluation Board Schematic and Artwork.....	4
Revision History	2	Ordering Information.....	5
Using the Evaluation Board.....	3	Bill of Materials.....	5

REVISION HISTORY

5/11—Revision 0: Initial Version

USING THE EVALUATION BOARD

POWERING UP THE EVALUATION BOARD

The ADP2147 evaluation board is supplied fully assembled and tested. Before applying power to the evaluation board, follow the procedures in this section.

Jumper JP3 (ENABLE)

Jumper JP3 (EN) enables/disables the ADP2147. Connect a jumper between Pin 2 of JP3 and Pin 3 (VIN) of JP3, to enable the ADP2147. Connecting Pin 2 of JP3 to Pin 1 of JP3 disables the ADP2147 and brings the current to <1 μ A.

Input Power Source

If the input power source includes a current meter, use that meter to monitor the input current. Connect the positive terminal of the power source to TB1 (VIN) on the evaluation board, and the negative terminal of the power source to TB5 (GND IN) of the evaluation board.

If the power source does not include a current meter, connect a current meter in series with the input source voltage. Connect the positive (+) lead of the power source to the ammeter positive (+) connection, the negative (-) lead of the power source to TB5 (GND IN) on the evaluation board, and the negative (-) lead of the ammeter to TB1 (VIN) on the evaluation board.

Output Load

Connect an electronic load or resistor to set the load current. If the load includes an ammeter, or if the current is not measured, connect the load directly to the evaluation board, with the positive (+) load connection to TB3 (VOUT) and the negative (-) load connection to TB4 (GND OUT).

If an ammeter is used, connect it in series with the load. Connect the positive (+) ammeter terminal to the evaluation board at TB3 (VOUT), the negative (-) ammeter terminal to the positive (+) load terminal, and the negative (-) load terminal to the evaluation board at TB4 (GND OUT).

Input and Output Voltmeters

Measure the input and output voltages with voltmeters. Make sure that the voltmeters are connected to the appropriate evaluation board terminals and not to the load or power sources themselves. If the voltmeters are not connected directly to the evaluation board, the measured voltages are incorrect due to the voltage drop across the leads and/or connections between the evaluation board, the power source, and/or the load.

Connect the input voltage measuring voltmeter positive (+) terminal to the evaluation board at TB1 (VIN), and the negative (-) terminal to the evaluation board at TB5 (GND IN). Connect the output voltage measuring voltmeter positive (+) terminal to the evaluation board at TB3 (VOUT) and the negative (-) terminal to the evaluation board at TB4 (GND OUT).

Turning On the Evaluation Board

After the power source and load are connected to the ADP2147 evaluation board, the board can be powered for operation. To do this, perform the following steps:

1. Ensure that the power source voltage is >2.3 V but <5.5 V.
2. Ensure that JP3 (EN) is high and monitor the output voltage.

If the load is not already enabled, enable the load, check that it is drawing the proper current, and that the output voltage maintains voltage regulation.

Changing the Output Voltage of the Device

Each ADP2147 has a VSEL pin, which determines the output voltage of the buck regulator by either a high selection (the VSEL pin is set high by connecting Pin 2 of JP2 to Pin 3 of JP2) or by a low selection (the VSEL pin is set low by connecting Pin 2 of JP2 to Pin 1 of JP2).

MEASURING EVALUATION BOARD PERFORMANCE

Measuring Output Voltage Ripple

To observe the output voltage ripple, place an oscilloscope probe across the output capacitor (COUT) with the probe ground lead at the negative (-) capacitor terminal and the probe tip at the positive (+) capacitor terminal. Set the oscilloscope to ac, 20 mV/division, and 2 μ s/division time base.

Measuring the Switching Waveform

To observe the switching waveform with an oscilloscope, place the oscilloscope probe tip at the end of the inductor with the probe ground at GND OUT. Set the oscilloscope to dc, 2 V/division, and 2 μ s/division time base.

Measuring Load Regulation

The load regulation is tested by increasing the load at the output and looking at the change in output voltage. To minimize voltage drop, use short low resistance wires, especially for loads approaching maximum current.

Measuring Line Regulation

Vary the input voltage and examine the change in the output voltage.

Measuring Efficiency

Measure the efficiency, η , by comparing the input power with the output power.

$$\eta = \frac{V_{OUT} \times I_{OUT}}{V_{IN} \times I_{IN}}$$

Measure the input and output voltages as close as possible to the input and output capacitors to reduce the effect of IR drops.

Measuring Inductor Current

The inductor current can be measured by removing one end of the inductor from its pad and connecting a current loop in series. A current probe can be connected onto this wire.

EVALUATION BOARD SCHEMATIC AND LAYOUTS

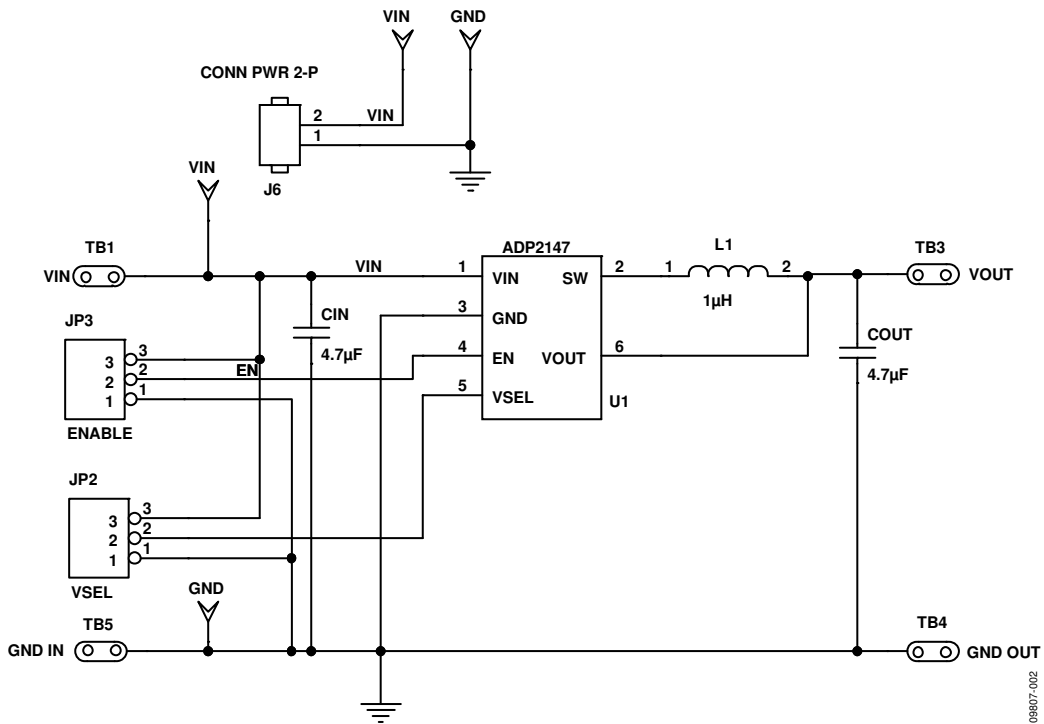


Figure 2. Evaluation Board Schematic

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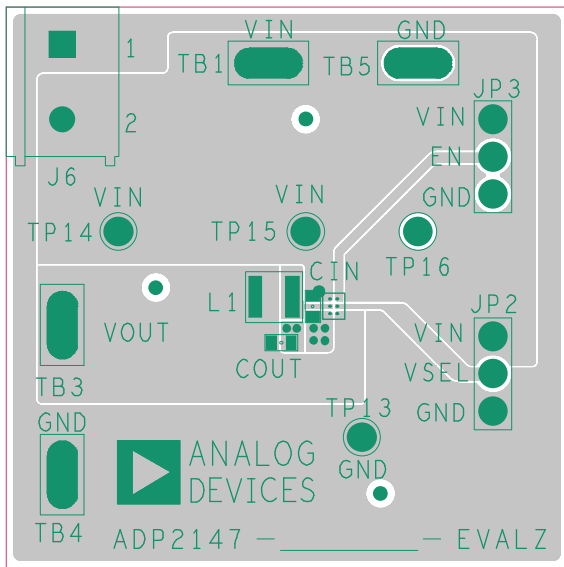


Figure 3. Evaluation Board, Top Layer

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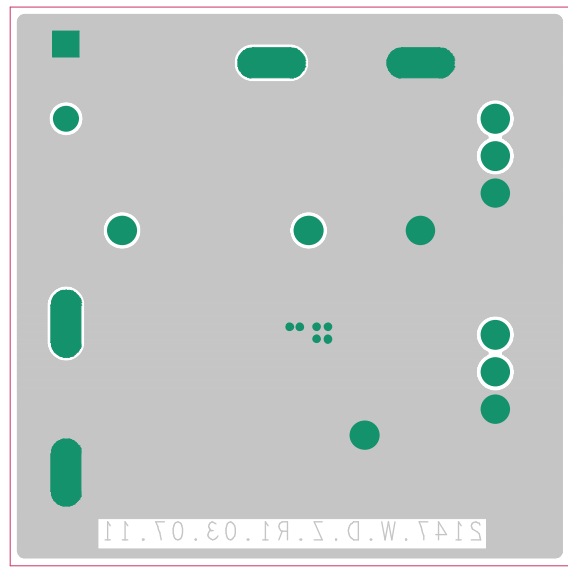


Figure 4. Evaluation Board, Bottom Layer

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ORDERING INFORMATION**BILL OF MATERIALS**

Table 1.

Quantity	Reference Designator	Description	Manufacturer	Part Number
1	U1	ADP2147 buck regulator	Analog Devices	ADP2147
1	CIN	Capacitor, MLCC, 4.7 μ F, 6.3 V, 0603, X5R	Murata Manufacturing Co., Ltd	GRM188R60J475
1	COU1	Capacitor, MLCC, 4.7 μ F, 6.3 V, 0603, X5R	Murata Manufacturing Co., Ltd	GRM188R60J475
1	L1	Inductor, 1 μ H, 1.4 A, 85 m Ω	Murata Manufacturing Co., Ltd	LQM2MPN1R0NG0B

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**ESD Caution**

ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

Legal Terms and Conditions

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