TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7PG34AFE

Dual NON-Inverter

Features

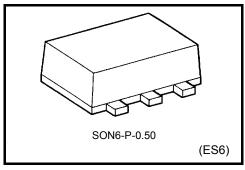
• High output current : ±8 mA (min) at V_{CC} = 3 V

• Super high speed operation : t_{pd} = 2.8 ns (typ.)

at $V_{CC} = 3.3 \text{ V}, 15 \text{pF}$

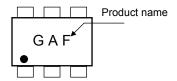
Operating voltage range : V_{CC} = 0.9 to 3.6 V

• 5.5-V tolerant inputs

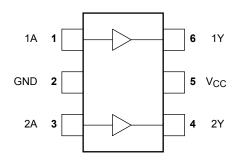


Weight: 0.003 g (typ.)

Marking



Pin Assignment (top view)



Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	-0.5 to 4.6	V
DC input voltage	V _{IN}	-0.5 to 7.0	V
DC output voltage	Vout	-0.5 to V _{CC} + 0.5	V
Input diode current	I _{IK}	-20	mA
Output diode current	lok	±20 (Note 1)	mA
DC output current	lout	±25	mA
DC V _{CC} /GND current	Icc	±100	mA
Power dissipation	PD	150	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: V_{OUT} < GND, V_{OUT} > V_{CC}

IEC Logic Symbol



Truth Table

А	Y
L	L
Н	Н

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	0.9 to 3.6	V
Input voltage	V _{IN}	0 to 5.5	V
Output voltage	V _{OUT}	0 to V _{CC}	V
Output Current	I _{OH} /I _{OL}	±8.0 (Note 2)	·
		±4.0 (Note 3)	
		±3.0 (Note 4)	mA
		±1.7 (Note 5)	IIIA
		±0.3 (Note 6)	
		±0.02 (Note 7)	
Operating temperature	T _{opr}	-40 to 85	°C
Input rise and fall time	dt/dv	0 to10 (Note 8)	ns/V

Note 2: $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$

Note 3: $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$

Note 4: $V_{CC} = 1.65 \text{ to } 1.95 \text{ V}$

Note 5: $V_{CC} = 1.4 \text{ to } 1.6 \text{ V}$

Note 6: $V_{CC} = 1.1 \text{ to } 1.3 \text{ V}$

Note 7: $V_{CC} = 0.9 \text{ V}$

Note 8: $V_{IN} = 0.8$ to 2.0 V, $V_{CC} = 3.0$ V

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Electrical Characteristics

DC Electrical Characteristics

Characteristics Symbol Test Condition				Ta = 25°C			Ta = -40 to 85°C			
		rest	Test Condition		Min	Тур.	Max	Min	Max	Unit
					V _{CC}	_	_	V _{CC}	_	-
				1.1 to 1.3	V _{CC} × 0.7	_	_	V _{CC} × 0.7	_	
High-level VIH input voltage	V _{IH}	_		1.4 to 1.6	V _{CC} × 0.65		_	V _{CC} × 0.65		V
			1.65 to 1.95	V _{CC} × 0.65		_	V _{CC} × 0.65			
				2.3 to 2.7	1.7	1	_	1.7		
				3.0 to 3.6	2.0		_	2.0		
				0.9	_	_	GND	_	GND	
Low-level V _{IL} input voltage			1.1 to 1.3		-	V _{CC} × 0.3	_	V _{CC} × 0.3		
					-	V _{CC} × 0.35	_	V _{CC} × 0.35	V	
					-	V _{CC} × 0.35	_	V _{CC} × 0.35		
				2.3 to 2.7			0.7		0.7	
				3.0 to 3.6	_	_	0.8		0.8	
			I _{OH} =-0.02 mA	0.9	0.75	_	_	0.75	_	
			$I_{OH} = -0.3 \text{ mA}$	1.1 to 1.3	V _{CC} × 0.75		_	V _{CC} × 0.75		
High-level VOH	V _{OH}	V _{IN} = V _{IH}	$I_{OH} = -1.7 \text{ mA}$	1.4 to 1.6	V _{CC} × 0.75		_	V _{CC} × 0.75		
			$I_{OH} = -3.0 \text{ mA}$	1.65 to 1.95	V _{CC} -0.45	_	_	V _{CC} -0.45		
			$I_{OH} = -4.0 \text{ mA}$	2.3 to 2.7	2.0	_	_	2.0		
			$I_{OH} = -8.0 \text{ mA}$	3.0 to 3.6	2.48		_	2.48		
			$I_{OL} = 0.02 \text{ mA}$	0.9	_	_	0.1	_	0.1	
Low-level V _{OL}		I _{OL} = 0.3 mA	1.1 to1.3	_	_	V _{CC} × 0.25	_	V _{CC} × 0.25		
	V _{OL}	OL VIN = VIL	I _{OL} = 1.7 mA	1.4 to 1.6	_	_	V _{CC} × 0.25	_	V _{CC} × 0.25	V
			I _{OL} = 3.0 mA	1.65 to 1.95	_	_	0.45	_	0.45	
			I _{OL} = 4.0 mA	2.3 to 2.7	_	_	0.4	_	0.4	
			I _{OL} = 8.0 mA	3.0 to 3.6	_	_	0.4	_	0.4	
Input leakage current	I _{IN}	V _{IN} = 0 to 5.5V		0 to 3.6	_	_	±0.1	_	±1.0	μА
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		3.6	_	_	1.0	_	10.0	μΑ

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AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition	_	Ta = 25°C		Ta = -40 to 85°C		Unit	
Criaracteristics Symbol		rest Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
		$\begin{array}{c} C_L = 10 \; pF, \\ R_L = 1 \; M\Omega \end{array}$	0.9	_	27.2	_	_	_	
			1.1 to 1.3	_	12.2	23.2	1.0	42.6	
			1.4 to 1.6	_	6.5	10.2	1.0	12.0	
			1.65 to 1.95	_	4.7	7.0	1.0	7.6	
			2.3 to 2.7	_	3.1	4.4	1.0	4.9	
	t _P LH t _P HL		3.0 to 3.6	_	2.4	3.5	1.0	4.1	
		$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	29.8	_	_	_	
			1.1 to 1.3	_	13.5	26.0	1.0	44.5	ns
Door and the delay for			1.4 to 1.6	_	7.2	11.4	1.0	13.6	
Propagation delay time			1.65 to 1.95	_	5.2	7.5	1.0	7.7	
			2.3 to 2.7	_	3.4	4.8	1.0	5.5	
			3.0 to 3.6	_	2.8	3.8	1.0	4.4	
		$C_L=30$ pF, $R_L=1$ M Ω	0.9	_	40.7	_	_	_	
			1.1 to 1.3	_	17.8	33.9	1.0	64.1	
			1.4 to 1.6	_	9.1	14.3	1.0	17.4	
			1.65 to 1.95	_	6.6	9.8	1.0	10.2	
			2.3 to 2.7	_	4.1	6.2	1.0	6.6	
			3.0 to 3.6	_	3.3	4.8	1.0	5.2	
Input capacitance	C _{IN}	_	3.6	_	3	_	_	_	pF
Power dissipation capacitance	C _{PD}	(Note 9)	0.9 to 3.6	_	6			_	pF

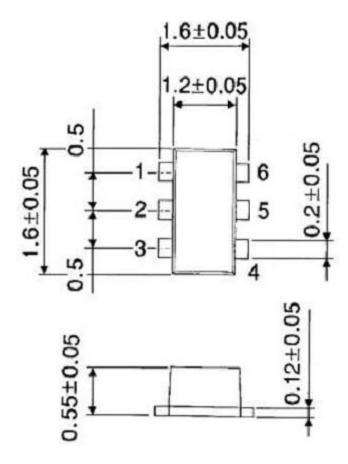
Note 9: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC \text{ (opr.)}} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$

Package Dimensions

SON6-P-0.50 Unit: mm



Weight: 0.003 g (typ.)

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