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April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

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# SWITCHING N-CHANNEL POWER MOS FET

#### **DESCRIPTION**

The 2SK3570 is N-channel MOS FET device that features a low on-state resistance and excellent switching characteristics, designed for low voltage high current applications such as DC/DC converter with synchronous rectifier.

#### **FEATURES**

- •4.5V drive available.
- •Low on-state resistance,

 $R_{DS(on)1} = 12 \text{ m}\Omega \text{ MAX.} \text{ (Vgs} = 10 \text{ V, ID} = 24 \text{ A)}$ 

•Low gate charge

 $Q_G = 23 \text{ nC TYP.}$  ( $V_{DD} = 16 \text{ V}$ ,  $V_{GS} = 10 \text{ V}$ ,  $I_D = 48 \text{ A}$ )

- •Built-in gate protection diode
- •Surface mount device available

#### **★ ORDERING INFORMATION**

PART NUMBER	PACKAGE
2SK3570	TO-220AB
2SK3570-S	TO-262
2SK3570-ZK	TO-263
2SK3570-Z	TO-220SMD Note

Note TO-220SMD package is produced only in Japan.

#### ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (Vgs = 0 V)	VDSS	20	V
Gate to Source Voltage (VDS = 0 V)	Vgss	±20	٧
Drain Current (DC) (Tc = 25°C)	I <sub>D(DC)</sub>	±48	Α
Drain Current (pulse) Note	ID(pulse)	±160	Α
Total Power Dissipation (T <sub>A</sub> = 25°C)	P <sub>T1</sub>	1.5	W
Total Power Dissipation (Tc = 25°C)	P <sub>T2</sub>	29	W
Channel Temperature	$T_ch$	150	°C
Storage Temperature	Tstg	-55 to +150	°C

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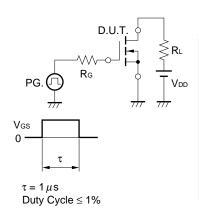
**Note** PW  $\leq$  10  $\mu$ s, Duty Cycle  $\leq$  1%

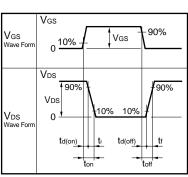


#### **ELECTRICAL CHARACTERISTICS (TA = 25°C)**

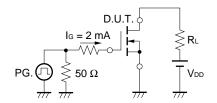
Characteristics	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Zero Gate Voltage Drain Current	Ipss	V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V			10	μΑ
Gate Leakage Current	Igss	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$			±10	μΑ
Gate Cut-off Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	1.5		2.5	٧
Forward Transfer Admittance	<b>y</b> fs	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 24 A	8.0			S
Drain to Source On-state Resistance	RDS(on)1	Vgs = 10 V, ID = 24 A		8.2	12	mΩ
	RDS(on)2	Vgs = 4.5 V, ID = 15 A		12.3	22	mΩ
Input Capacitance	Ciss	V <sub>DS</sub> = 10 V		930		pF
Output Capacitance	Coss	V <sub>G</sub> S = 0 V		360		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		250		pF
Turn-on Delay Time	td(on)	V <sub>DD</sub> = 10 V, I <sub>D</sub> = 24 A		13		ns
Rise Time	tr	Vgs = 10 V		20		ns
Turn-off Delay Time	td(off)	$R_G = 10 \Omega$		39		ns
Fall Time	t <sub>f</sub>			14		ns
Total Gate Charge	Qg	V <sub>DD</sub> = 16 V		23		nC
Gate to Source Charge	Qgs	V <sub>G</sub> S = 10 V		4		nC
Gate to Drain Charge	Q <sub>GD</sub>	ID = 48 A		7		nC
Body Diode Forward Voltage	V <sub>F(S-D)</sub>	IF = 48 A, VGS = 0 V		1.1		V
Reverse Recovery Time	trr	IF = 48 A, VGS = 0 V		33		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/μs		25		nC

#### **★** TEST CIRCUIT 1 SWITCHING TIME



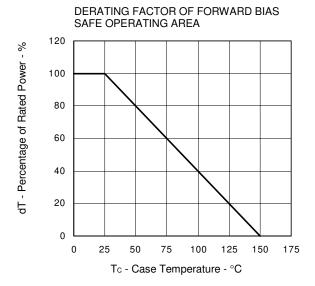


#### **TEST CIRCUIT 2 GATE CHARGE**

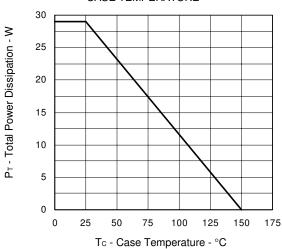




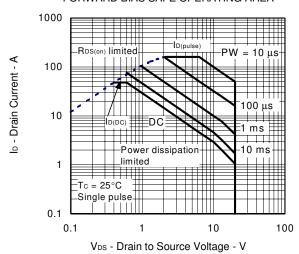
#### \* TYPICAL CHARACTERISTICS (TA = 25°C)



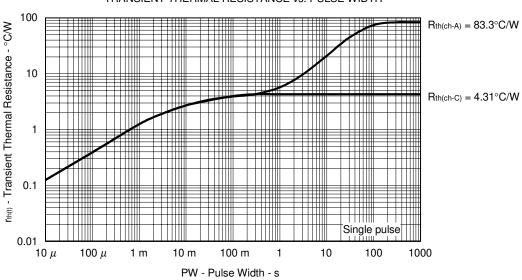
## TOTAL POWER DISSIPATION vs. CASE TEMPERATURE



#### FORWARD BIAS SAFE OPERATING AREA

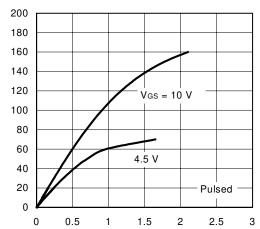


#### TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



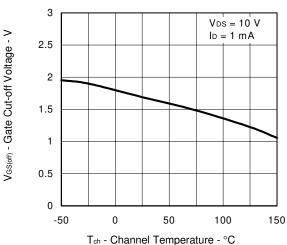
lo - Drain Current - A

### DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE

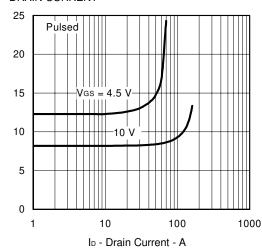


V<sub>DS</sub> - Drain to Source Voltage - V

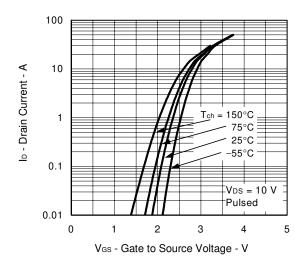
### GATE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE



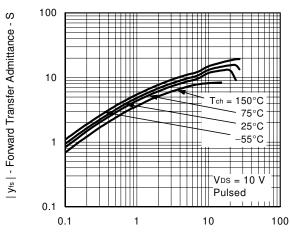
### DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



#### FORWARD TRANSFER CHARACTERISTICS

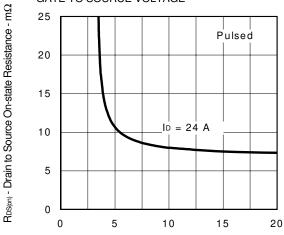


### FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



ID - Drain Current - A

## DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE

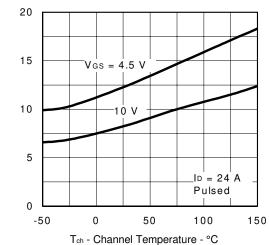


V<sub>GS</sub> - Gate to Source Voltage - V

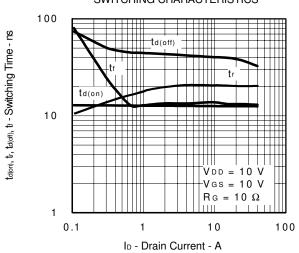
RDS(on) - Drain to Source On-state Resistance - m\Omega

R<sub>DS(m)</sub> - Drain to Source On-state Resistance - mΩ

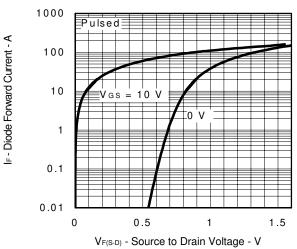




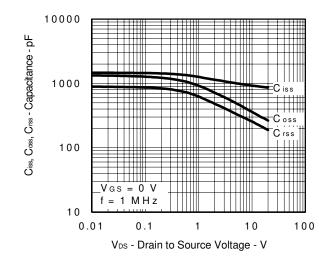
#### SWITCHING CHARACTERISTICS



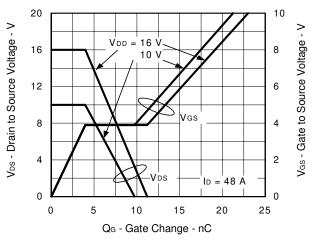
### SOURCE TO DRAIN DIODE FORWARD VOLTAGE



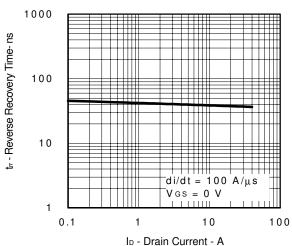
#### CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



#### DYNAMIC INPUT/OUTPUT CHARACTERISTICS



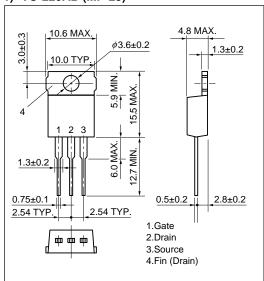
### REVERSE RECOVERY TIME vs. DRAIN CURRENT



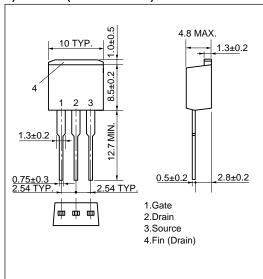


#### **★ PACKAGE DRAWINGS (Unit: mm)**

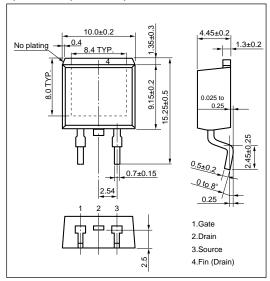
#### 1) TO-220AB (MP-25)



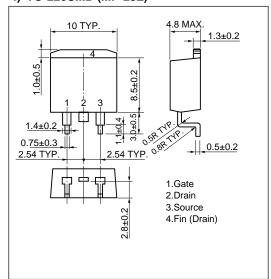
#### 2) TO-262 (MP-25 Fin Cut)



#### 3) TO-263 (MP-25ZK)

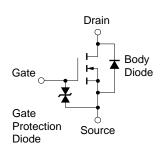


### 4) TO-220SMD (MP-25Z)



Note This package is produced only in Japan.

#### **EQUIVALENT CIRCUIT**



Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

[MEMO]



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