# FAIRCHILD

SEMICONDUCTOR®

# BD440/442

# Medium Power Linear and Switching Applications

Complement to BD439, BD441 respectively

# **PNP Epitaxial Silicon Transistor**



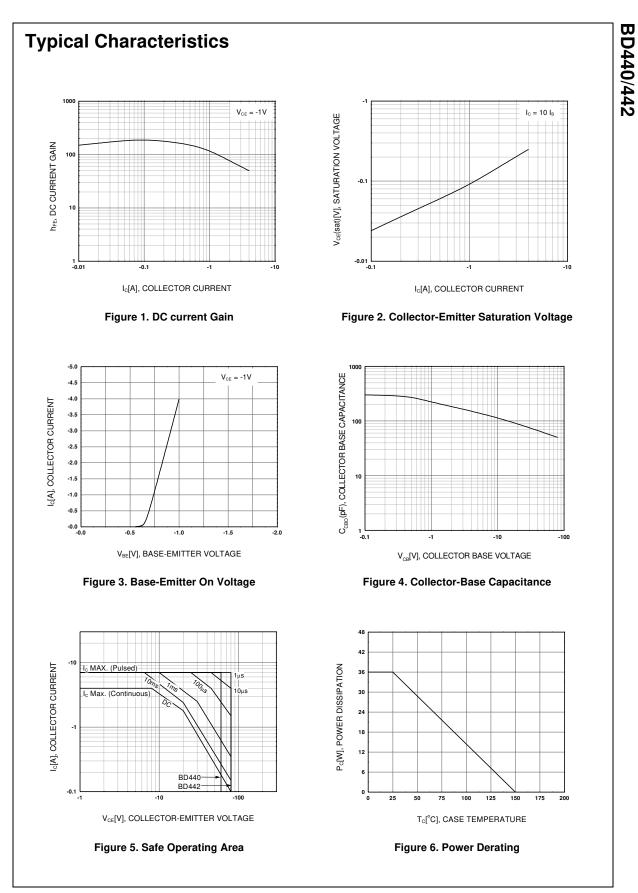
1. Emitter 2.Collector 3.Base

Absolute Maximum Ratings T<sub>C</sub>=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>CBO</sub>	Collector-Base Voltage		
	: BD440	- 60	V
	: BD442	- 80	V
V <sub>CES</sub>	Collector-Emitter Voltage		
010	: BD440	- 60	V
	: BD442	- 80	V
V <sub>CEO</sub>	Collector-Emitter Voltage		
	: BD440	- 60	V
	: BD442	- 80	V
V <sub>EBO</sub>	Emitter-Base Voltage	- 5	V
I <sub>C</sub>	Collector Current (DC)	- 4	А
I <sub>CP</sub>	*Collector Current (Pulse)	- 7	А
I <sub>B</sub>	Base Current	- 1	А
I <sub>B</sub> P <sub>C</sub>	Collector Dissipation (T <sub>C</sub> =25°C)	36	W
TJ	Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature	- 65 ~ 1 50	°C

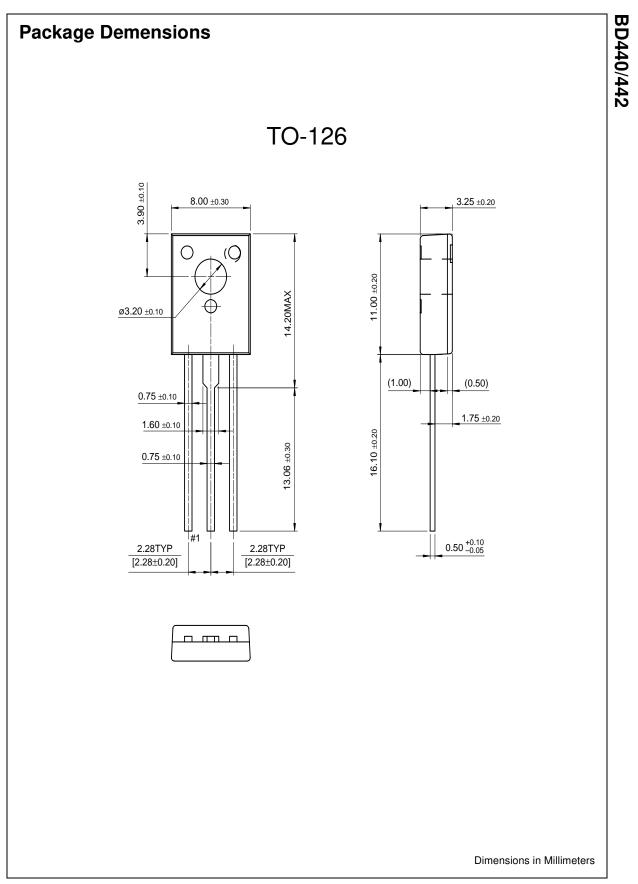
# Electrical Characteristics $T_{C}=25^{\circ}C$ unless otherwise noted

Symbol	Paramete	er	Test Condition	Min.	Тур.	Max.	Units
V <sub>CEO</sub> (sus)	Collector-Emitter Sustainin	ng Voltage					
		: BD440	I <sub>C</sub> = - 100mA, I <sub>B</sub> = 0	-60			V
		: BD442		-80			V
I <sub>CBO</sub>	Collector Cut-off Current	: BD440	$V_{CB} = -60V, I_{E} = 0$			- 100	μA
		: BD442	$V_{CB} = -80V, I_E = 0$			- 100	μA
I <sub>CES</sub>	Collector Cut-off Current	: BD440	$V_{CE} = -60V, V_{BE} = 0$			- 100	μA
		: BD442	$V_{CE} = -80V, V_{BE} = 0$			- 100	μA
I <sub>EBO</sub>	Emitter Cut-off Current		$V_{EB} = -5V, I_{C} = 0$			- 1	mA
h <sub>FE</sub>	* DC Current Gain	: BD440	V <sub>CE</sub> = - 5V, I <sub>C</sub> = - 10mA	20	140		
		: BD442		15	140		
		: BD440	V <sub>CE</sub> = - 1V, I <sub>C</sub> = - 500mA	40	140		
		: BD442		40	140		
		: BD440	$V_{CF} = -1V, I_{C} = -2A$	25			
		: BD442	02 0	15			
V <sub>CE</sub> (sat)	* Collector-Emitter Saturati	on Voltage	I <sub>C</sub> = - 2A, I <sub>B</sub> = - 0.2A			- 0.8	V
V <sub>BE</sub> (on)	* Base-Emitter ON Voltage	)	V <sub>CE</sub> = - 5V, I <sub>C</sub> = - 10mA		-0.58		V
			$V_{CE} = -1 V, I_{C} = -2A$			- 1.5	V
f <sub>T</sub>	Current Gain Bandwidth F	Product	$V_{CE} = -1V, I_{C} = -250mA$	3			MH:



©2001 Fairchild Semiconductor Corporation

Rev. A1, June 2001



#### TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACEx™	FAST <sup>®</sup>	OPTOPLANAR™	SuperSOT™-3
Bottomless™	FASTr™	PACMAN™	SuperSOT™-6
CoolFET™	FRFET™	POP™	SuperSOT™-8
CROSSVOLT™	GlobalOptoisolator™	PowerTrench <sup>®</sup>	SyncFET™
DenseTrench™	GTO™	QFET™	TinyLogic™
DOME™	HiSeC™	QS™	UHC™
EcoSPARK™	ISOPLANAR™	QT Optoelectronics™	UltraFET <sup>®</sup>
E <sup>2</sup> CMOS™	LittleFET™	Quiet Series™	VCX™
EnSigna™	MicroFET™	SLIENT SWITCHER <sup>®</sup>	
FACT™	MICROWIRE™	SMART START™	
FACT Quiet Series™	OPTOLOGIC™	Stealth™	

#### DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

#### LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

#### As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.

2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

#### **PRODUCT STATUS DEFINITIONS**

#### **Definition of Terms**

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

Fairchild Semiconductor			etric   Cross Reference
Find products         Products groups         Analog and Mixed         Signal         Discrete         Interface         Logic         Microcontrollers         Non-Volatile         Memory         Optoelectronics         Markets and         applications         New products	Home >> Find products >> BD440 PNP Epitaxial Silicon Transistor Contents Features   Applications   Product status/pricing/packaging Features • Complement to BD439, BD441 respectively	Datasheet <u>Download this</u> <u>datasheet</u> <u>PDF</u> <u>e-mail this datasheet</u> <u>[E-</u> This page <u>Print version</u>	Related Links         Request samples         Dotted line         How to order products         Dotted line         Product Change Notices         (PCNs)         Dotted line         Support         Dotted line         Distributor and field sales         representatives         Dotted line         Quality and reliability
Product selection and parametric search Cross-reference search	back to top • Applications		Dotted line Design tools

## Medium Power Linear and Switching

#### back to top

technical information

buy products

my Fairchild

company

technical support

Product status/pricing/packaging

Product	Product status	Pricing*	Package type	Leads	Packing method
BD440S	Full Production	\$0.233	<u>TO-126</u>	3	BULK

\* 1,000 piece Budgetary Pricing

## back to top

<u>Home</u> | <u>Find products</u> | <u>Technical information</u> | <u>Buy products</u> | <u>Support</u> | <u>Company</u> | <u>Contact us</u> | <u>Site index</u> | <u>Privacy policy</u>

© Copyright 2002 Fairchild Semiconductor

Fairchild Semiconductor			etric   <u>Cross Reference</u> 3C uct Folders and D Applica
find products	Home >> Find products >>		
Products groups	BD442 PNP Epitaxial Silicon Transistor		Related Links
Analog and Mixed Signal Discrete Interface Logic Microcontrollers Non-Volatile Memory Optoelectronics	Contents <u>Features</u>   <u>Applications</u>   <u>Product</u> <u>status/pricing/packaging</u> Features	Datasheet <u>Download this</u> <u>datasheet</u> PDF <u>e-mail this datasheet</u>	Request samplesDotted lineHow to order productsDotted lineProduct Change Notices(PCNs)Dotted lineSupportDotted lineDotted lineDotted line
Markets and         applications         New products         Product selection and         parametric search         Cross-reference	• Complement to BD439, BD441 respectively	This page • Print version	<ul> <li><u>Postroutor and rield sales</u></li> <li><u>Potted line</u></li> <li><u>Quality and reliability</u></li> <li><u>Dotted line</u></li> <li><u>Design tools</u></li> </ul>
search	Applications		

## Medium Power Linear and Switching

#### back to top

technical information

buy products

my Fairchild

company

technical support

Product status/pricing/packaging

Product	Product status	Pricing*	Package type	Leads	Packing method
BD442S	Full Production	\$0.233	<u>TO-126</u>	3	BULK
BD442STU	Full Production	\$0.233	<u>TO-126</u>	3	RAIL

\* 1,000 piece Budgetary Pricing

## back to top

<u>Home</u> | <u>Find products</u> | <u>Technical information</u> | <u>Buy products</u> | <u>Support</u> | <u>Company</u> | <u>Contact us</u> | <u>Site index</u> | <u>Privacy policy</u>

© Copyright 2002 Fairchild Semiconductor