

SURFACE MOUNT SILICON LOW V_F SCHOTTKY RECTIFIER



Central semiconductor Corp.

www.centralsemi.com

DESCRIPTION:

The CENTRAL SEMICONDUCTOR CTLSH4-200M364 is a high performance 4.0 Amp silicon Schottky rectifier designed for applications requiring high voltage capability and high levels of efficiency in a low profile package.

MARKING CODE: CTLSH420

APPLICATIONS:

- DC-DC converters
- Reverse polarity protection
- By-pass diode

FEATURES:

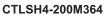
- Low forward voltage
- Low reverse leakage current
- Low profile package

MAXIMUM RATINGS: (T _A =25°C) Peak Repetitive Reverse Voltage	SYMBOL VRRM	200	UNITS V
DC Blocking Voltage	VR	200	V
RMS Reverse Voltage	V _{R(RMS)}	140	V
Average Forward Current (T _L =125°C)	ι _ο	4.0	А
Peak Forward Surge Current, tp=8.3ms	IFSM	100	А
Operating and Storage Junction Temperature	TJ, T _{stg}	-55 to +150	°C
Thermal Resistance (Note 1)	Θ_{JA}	80	°C/W
Thermal Resistance	Θ_{JL}	15	°C/W

ELECTRICAL CHARACTERISTICS: (T _A =25°C unless otherwise noted)							
SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS		
I _R	V _R =200V		0.15	1.0	μA		
I _R	V _R =200V, T _A =125°C		0.35	10	mA		
BVR	I _R =500μΑ	200			V		
VF	I _F =4.0A		0.76	0.84	V		
CJ	V _R =4.0V, f=1.0MHz		140		pF		

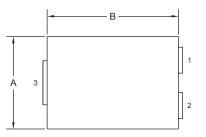
Notes: (1) FR-4 epoxy PCB with minimum copper pad area.

R1 (4-January 2013)

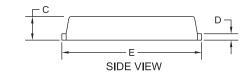


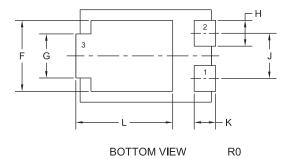


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TOP VIEW





LEAD CODE:

1) Anode

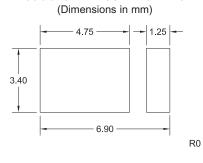
- 2) Anode
- 3) Cathode

MARKING CODE: CTLSH420

DIMENSIONS INCHES MILLIMETERS SYMBOL MIN MAX MIN MAX 0.167 0.172 4.25 4.35 A В 0.238 0.243 6.05 6.15 1.20 С 0.039 0.048 1.00 D 0.009 0.014 0.25 0.35 0.250 0.262 6.35 Е 6.65 F 0.128 0.136 3.25 3.45 G 0.076 0.085 1.95 2.15 0.044 0.052 1.10 1.30 н 0.083 J 2.10 0.035 0.044 0.90 1.10 Κ 0.171 0.183 4.35 4.65 TLM364 (REV:R0)

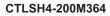
TLM364 CASE - MECHANICAL OUTLINE





R1 (4-January 2013)

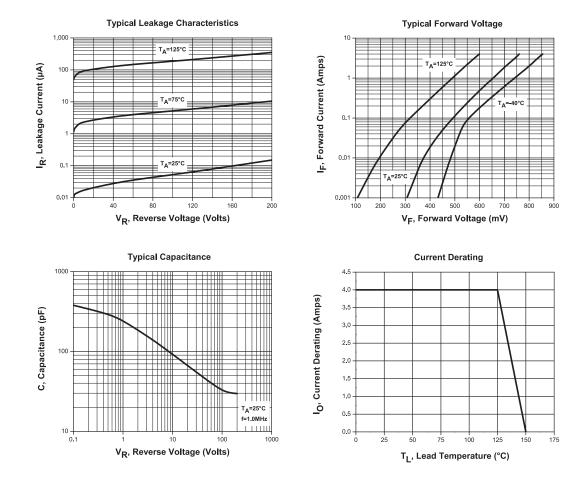
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TYPICAL ELECTRICAL CHARACTERISTICS

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PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- · Consolidated shipping options

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

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- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities

Special wafer diffusions

· Custom product packing

- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits

Custom bar coding for shipments

Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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