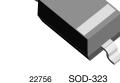
## VLIN1626-02G



**Vishay Semiconductors** 

## Low Capacitance, Single-Line ESD-Protection Diode in SOD-323

# 2



#### **MARKING** (example only)

20503



XYZ = type code (see table below) bar = pin 1

#### LINKS TO ADDITIONAL RESOURCES



#### **FEATURES**

- · For LIN-Bus applications
- Small SOD-323 package
- Working range: -16 V; +26.5 V
- Low leakage current  $I_R < 0.05 \ \mu A$
- Low load capacitance C<sub>D</sub> < 18 pF
- ESD-protection acc. IEC 61000-4-2 ± 30 kV contact discharge ± 30 kV air discharge
- ESD capability according to AEC-Q101: human body model: class H3B: > 8 kV
- e3 pins plated with tin (Sn)
- 1-line ESD-protection
- AEC-Q101 qualified available
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

ORDERING INFORMATION								
PART NUMBER (EXAMPLE)	ENVIRONMENTAL AND QUALITY CODE				PACKAG	ING CODE		
	AEC-Q101 QUALIFIED	RoHS-COMPLIANT + LEAD (Pb)-FREE TERMINATIONS		TIN PLATED	3K PER 7" REEL (8 mm TAPE)	10K PER 13" REEL (8 mm TAPE)	ORDERING CODE (EXAMPLE)	
	QUALIFIED	STANDARD	GREEN	FLATED	15K/BOX = MOQ	10K/BOX = MOQ		
VLIN1626-02G	-	E	-	3	-08	-	VLIN1626-02G-E3-08	
VLIN1626-02G	Н	E	-	3	-08	-	VLIN1626-02GHE3-08	
VLIN1626-02G	-	E	-	3	-	-18	VLIN1626-02G-E3-18	
VLIN1626-02G	Н	E	_	3	-	-18	VLIN1626-02GHE3-18	

PACKAGE DATA							
DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING			
VLIN1626-02G	SOD-323	6A1	4.30 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	Peak temperature max. 260 °C	

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT		
Peak pulse current	Pin 1 to pin 2; T <sub>A</sub> = 25 °C, acc. IEC 61000-4-5; $t_p$ = 8/20 µs; single shot		6	А		
	Pin 2 to pin 1; T <sub>A</sub> = 25 °C, acc. IEC 61000-4-5; $t_p$ = 8/20 µs; single shot	IPPM	4			
Peak pulse power	$T_A$ = 25 °C, acc. IEC 61000-4-5; $t_p$ = 8/20 µs; single shot	P <sub>PP</sub>	200	W		
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses, T <sub>A</sub> = 25 °C	V	± 30	kV		
	Air discharge acc. IEC 61000-4-2; 10 pulses, $T_A = 25 \text{ °C}$	V <sub>ESD</sub>	± 30			
Operating temperature	Junction temperature	TJ	-55 to +150	°C		
Storage temperature		T <sub>STG</sub>	-55 to +150	U		



RoHS

COMPLIANT

## VLIN1626-02G



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ELECTRICAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITIONS / REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines	
Reverse stand-off voltage	Pin 1 to pin 2; max. reverse working voltage	V	-	-	16	v	
	Pin 2 to pin 1; max. reverse working voltage	V <sub>RWM</sub>	-	-	26.5		
Reverse voltage	Pin 1 to pin 2; at I <sub>R</sub> = 0.05 μA	V	16	-	-	v	
	Pin 2 to pin 1; at I <sub>R</sub> = 0.05 μA	V <sub>R</sub>	26.5	-	-		
Reverse current	Pin 1 to pin 2; at V <sub>RWM</sub> = 16 V		-	-	0.05	μA	
	Pin 2 to pin 1; at $V_{RWM} = 26.5 V$	I <sub>R</sub>	-	-	0.05		
	Pin 1 to pin 2; at I <sub>R</sub> = 1 mA	V	17.1	18.7	20.3	v	
Reverse breakdown voltage	Pin 2 to pin 1; at I <sub>R</sub> = 1 mA	V <sub>BR</sub>	28	30	32		
Reverse clamping voltage	Pin 1 to pin 2; at $I_{PP} = 1 \text{ A}$ ; $t_p = 8/20 \ \mu s$		-	22	25	V	
	Pin 1 to pin 2; at $I_{PP} = 6 \text{ A}$ ; $t_p = 8/20 \mu\text{s}$		-	29	33		
	Pin 2 to pin 1; at I <sub>PP</sub> = 1 A; t <sub>p</sub> = 8/20 μs	V <sub>C</sub>	-	32	40		
	Pin 2 to pin 1; at I <sub>PP</sub> = 4 A; t <sub>p</sub> = 8/20 μs	1	-	39	50		
Capacitance	At $V_R = 0 V$ , f = 1 MHz	CD	-	15.5	18	pF	

#### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

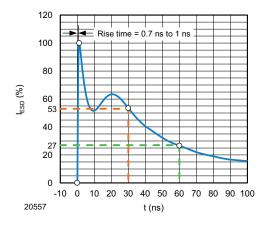


Fig. 1 - ESD Discharge Current Wave Form acc. IEC 61000-4-2 (330  $\Omega$  / 150 pF)

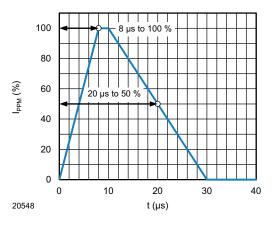


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5

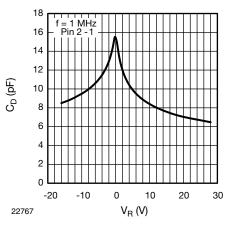
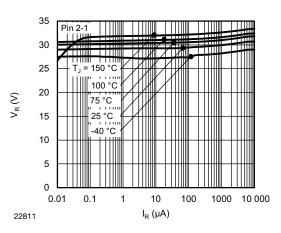


Fig. 3 - Typical Capacitance  $C_{D} \mbox{ vs.}$  Reverse Voltage  $V_{R}$ 







## VLIN1626-02G

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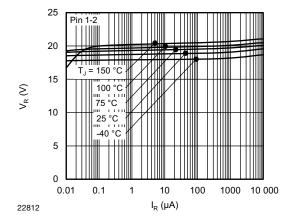


Fig. 5 - Typical Reverse Voltage V<sub>R</sub> vs. Reverse Current I<sub>R</sub>

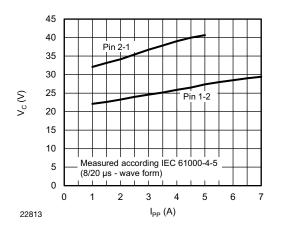


Fig. 6 - Typical Peak Clamping Voltage  $V_{C}$  vs. Peak Pulse Current  $I_{PP}$ 

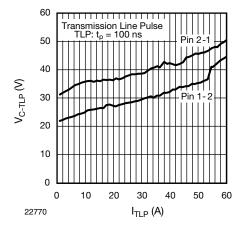


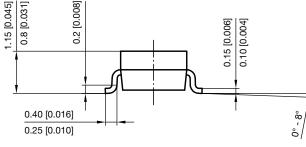
Fig. 7 - Typical Clamping Voltage V<sub>C-TLP</sub> vs. Pulse Current I<sub>TLP</sub>

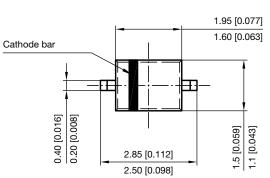


0.1 [0.004] max.

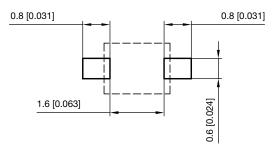


#### PACKAGE DIMENSIONS in millimeters (inches) SOD-323





Footprint recommendation:



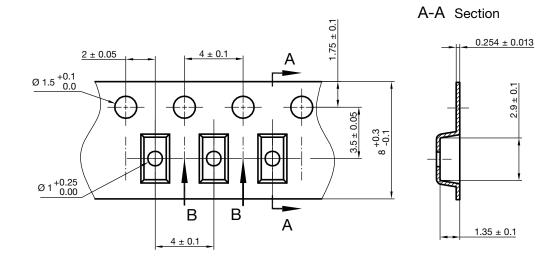
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#### **CARRIER TAPE SOD-323**

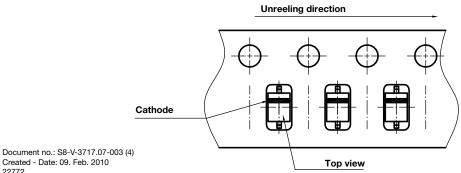


**B-B** Section



Document no.: S8-V-3717.07-002 (4) Created - Date: 09. Feb. 2010 22824

#### **ORIENTATION IN CARRIER TAPE SOD-323**



Created - Date: 09. Feb. 2010 22772

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