

# 30V SYNCHRONOUS N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI3333-8

### **Product Summary**

Device	BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max
Q1	30V	$12m\Omega$ @ $V_{GS} = 5V$ , $I_D = 15A$
Q2	30V	$6m\Omega$ @ $V_{GS} = 5V$ , $I_D = 15A$

### **Features and Benefits**

- 100% Unclamped Inductive Switch (UIS) Test in Production
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

### **Description and Applications**

This new generation MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- DC-DC Converters
- Power Management Functions

### **Mechanical Data**

- Case: PowerDI<sup>®</sup>3333-8
- Case Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
   Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.044 grams (Approximate)

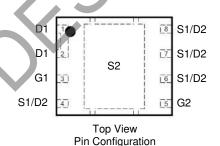
#### PowerDI3333-8 (Type D)







**Bottom View** 



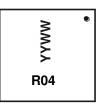
### Ordering Information (Note 4)

Part Number	Case	Packaging
DMN3012LDG-7	PowerDI3333-8 (Type D)	1,000/Tape & Reel
DMN3012LDG-13	PowerDI3333-8 (Type D)	3,000/Tape & Reel

Notes: 1. FU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and
- <1000ppm antimony compounds.</p>
  4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

### **Marking Information**



R04 = Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 19 = 2019) WW = Week Code (01 to 53)



### NOT RECOMMENDED FOR NEW DESIGN **USE DMN3012LEG**

DMN3012LDG

## **Maximum Ratings** (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Q1	Q2	Unit	
Drain-Source Voltage		$V_{DSS}$	30		V
Gate-Source Voltage	V <sub>GSS</sub>	±10		V	
$T_{C} = +25^{\circ}C$ $T_{C} = +70^{\circ}C$		I <sub>D</sub>	20 16		А
Continuous Drain Current @ V <sub>GS</sub> = 5V	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I <sub>D</sub>	10 8		А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I <sub>DM</sub>	70	100	Α
Continuous Source-Drain Diode Current (Note 5)		Is	2.7	3.2	Α
Avalanche Current (Note 6) L = 0.1mH		I <sub>AS</sub>	34	50	Α
Avalanche Energy (Note 6) L = 0.1mH		E <sub>AS</sub>	58	125	mJ

## Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation	$T_{C} = +25^{\circ}C$ $T_{C} = +70^{\circ}C$	P <sub>D</sub>	2.2	W
Thermal Resistance, Junction to Ambient (Note 5)  Steady Status 1<10s		$R_{ heta JA}$	58	°C/W
Thermal Resistance, Junction to Case (Note 5)		Rejc	9.5	
Operating and Storage Temperature Range		$T_{J}$ , $T_{STG}$	-55 to +150	°C

# **Electrical Characteristics Q1** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30	_		V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current	I <sub>DSS</sub>		_	1	μΑ	$V_{DS} = 20V, V_{GS} = 0V$
Gate-Source Leakage	I <sub>GSS</sub>	+	7 -	±100	nA	$V_{GS} = \pm 10V$ , $V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1	_	2.1	V	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>		10.5	12	mΩ	$V_{GS} = 5V, I_{D} = 15A$
Forward Transfer Admittance	YFS		27	l	S	$V_{DS} = 5V, I_{D} = 15A$
Diode Forward Voltage	V <sub>SD</sub>	_	_	1.0	V	$V_{GS} = 0V, I_{S} = 15A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C <sub>iss</sub>	_	650	850		$V_{DS} = 15V, V_{GS} = 0V,$ f = 1.0MHz
Output Capacitance	Coss	_	314	410	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	_	12	16		
Gate Resistance	R <sub>G</sub>	I	1.63	3.3	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1.0MHz$
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Q <sub>G</sub>		4.7	6.1		
Total Gate Charge at V <sub>TH</sub>	Q <sub>G(TH)</sub>	_	0.91	_	nC	V <sub>DS</sub> = 15V, I <sub>D</sub> = 15A
Gate-Source Charge	$Q_{GS}$	_	1.6	I	110	
Gate-Drain Charge	$Q_{GD}$	_	0.9			
Turn-On Delay Time	t <sub>D(ON)</sub>	_	5.1	7.7		
Turn-On Rise Time	t <sub>R</sub>	_	2.7	_	ns	$V_{DD} = 15V, V_{GS} = 4.5V,$ $I_{D} = 15A, R_{G} = 2\Omega$
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	6.4	9.6	115	
Turn-Off Fall Time	t <sub>F</sub>	_	2.3			
Reverse Recovery Time	t <sub>RR</sub>	_	24.5	_	ns	1 154 4:/44 2004/
Reverse Recovery Charge	$Q_{RR}$	_	8.3	_	nC	$I_F = 15A$ , di/dt = 300A/ $\mu$ s

5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate. Notes:

6. IAS and EAS ratings are based on low frequency and duty cycles to keep  $T_J = +25$ °C.

7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to product testing.

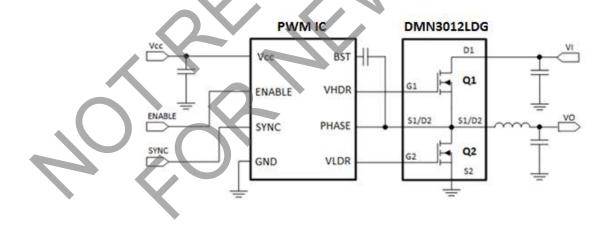
# Electrical Characteristics Q2 (@T<sub>A</sub> = +25°C, unless otherwise specified.)

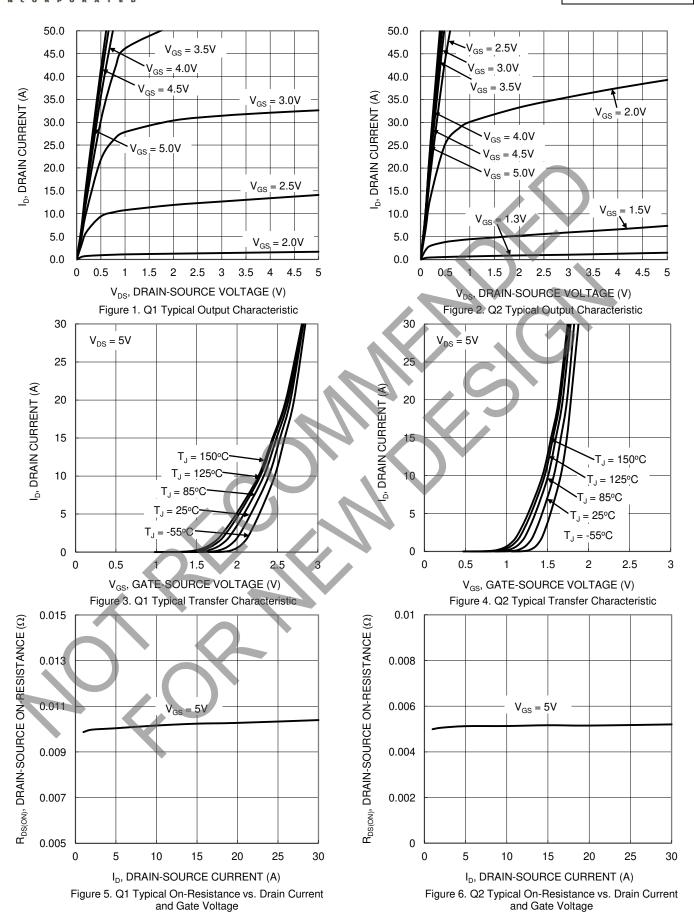
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30	_	_	٧	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	I <sub>DSS</sub>	_	_	1.0	μΑ	$V_{DS} = 20V, V_{GS} = 0V$
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 10V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(TH)}$	0.75	_	1.15	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	5.2	6	mΩ	$V_{GS} = 5V, I_D = 15A$
Forward Transfer Admittance	Y <sub>FS</sub>	_	46	_	S	$V_{DS} = 5V, I_{D} = 15A$
Diode Forward Voltage	$V_{SD}$	_	_	1.0	V	$V_{GS} = 0V, I_{S} = 15A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C <sub>iss</sub>	_	1,137	1,480	pF	
Output Capacitance	Coss	_	620	810	pF	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V, f = 1.0MHz
Reverse Transfer Capacitance	$C_{rss}$	_	24	32	pF	- 1.0Wi i2
Gate Resistance	$R_{G}$	_	0.54	1.1	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$
Total Gate Charge (V <sub>GS</sub> = 4.5V)	$Q_{G}$	_	9.7	12.6	nC	
Total Gate Charge at V <sub>TH</sub>	Q <sub>G(TH)</sub>	_	0.96	1	nC	$V_{DS} = 15V, I_{D} = 15A$
Gate-Source Charge	$Q_{GS}$	_	1.7		nC	VDS = 15V, 1D = 15A
Gate-Drain Charge	$Q_{GD}$	_	1.2	_	пC	
Turn-On Delay Time	t <sub>D(ON)</sub>	-	4.4	6.6	ns	
Turn-On Rise Time	t <sub>R</sub>		3.5		ns	$V_{DD} = 15V, V_{GS} = 4.5V,$
Turn-Off Delay Time	t <sub>D(OFF)</sub>	4	12.4	18.6	ns	$I_D = 15A$ , $R_G = 2\Omega$
Turn-Off Fall Time	t <sub>F</sub>	1	2.9	, <del>\</del>	ns	
Reverse Recovery Time	t <sub>RR</sub>	11-1	30.5		ns	1 15A di/dt 200A/::-
Reverse Recovery Charge	Q <sub>RR</sub>	-	10.8		nC	$I_F = 15A$ , di/dt = 300A/ $\mu$ s

Notes:

- 7. Short duration pulse test used to minimize self-heating effect. 8. Guaranteed by design. Not subject to product testing.

# **Typical Circuit**

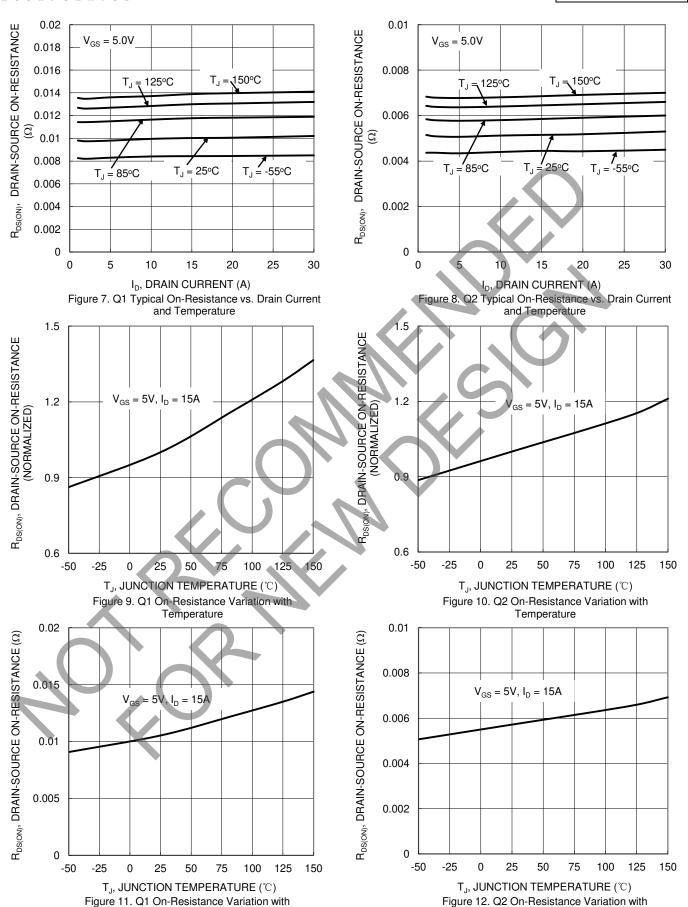






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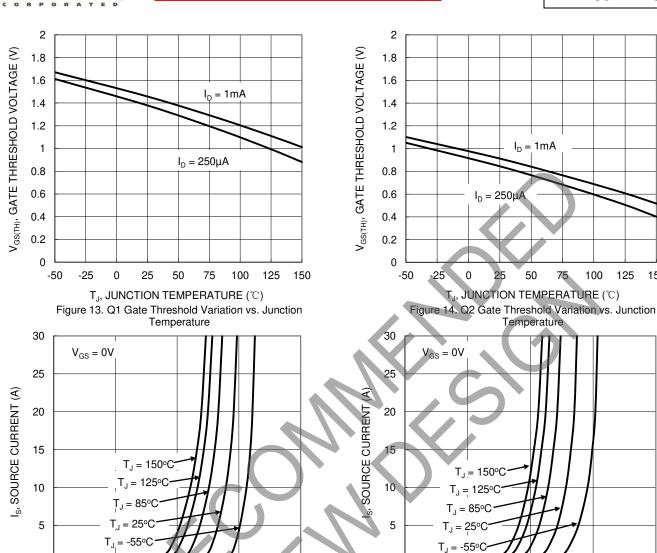
### DMN3012LDG

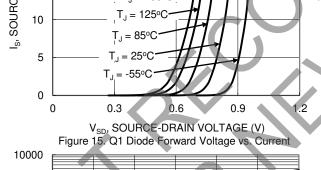


Temperature

Temperature







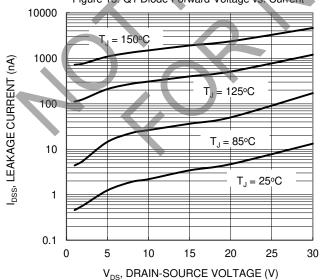
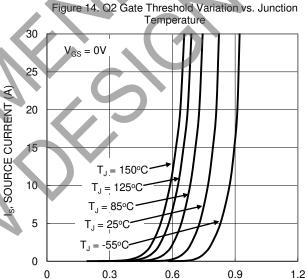


Figure 17. Q1 Typical Drain-Source Leakage Current vs. Voltage



50

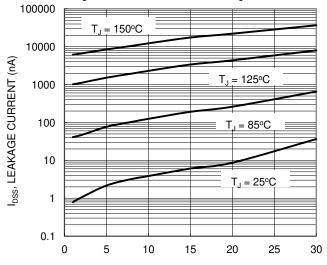
75

100

125

150

 $V_{SD}$ , SOURCE-DRAIN VOLTAGE (V) Figure 16. Q2 Diode Forward Voltage vs. Current



 $V_{DS}$ , DRAIN-SOURCE VOLTAGE (V) Figure 18. Q2 Typical Drain-Source Leakage Current vs. Voltage



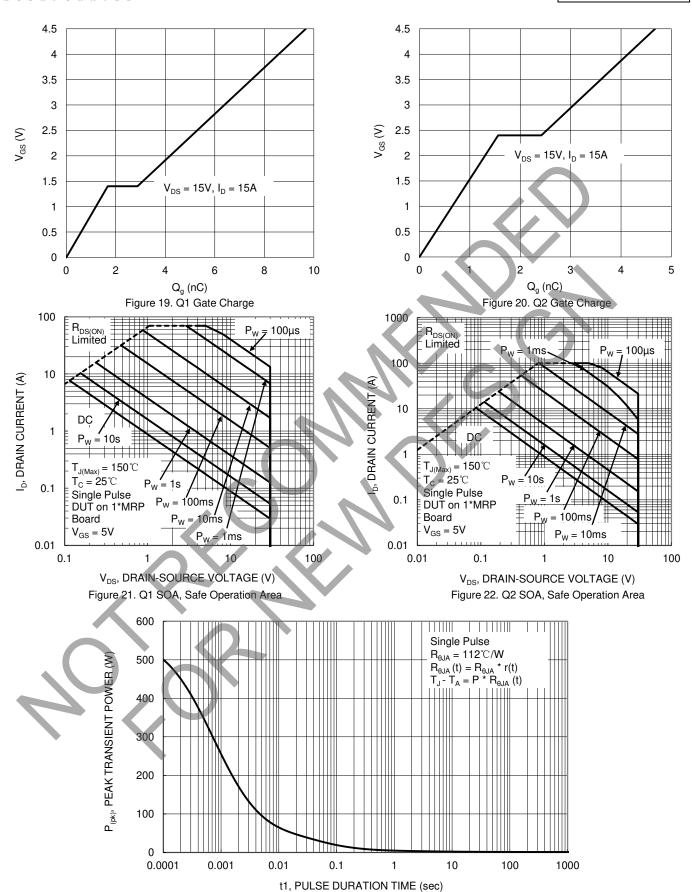


Figure 23. Single Pulse Maximum Power Dissipation



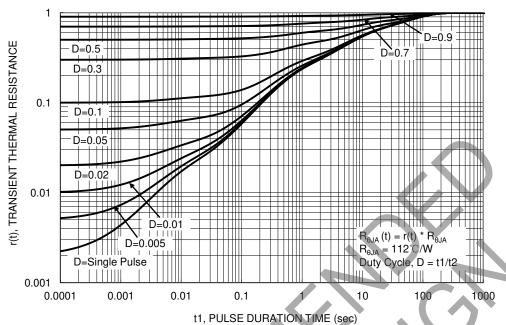


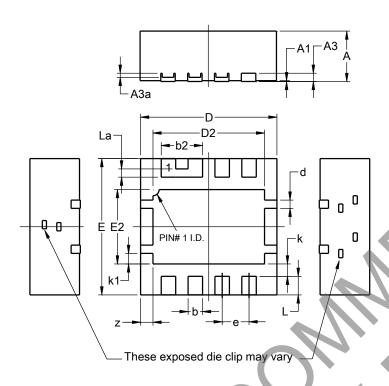
Figure 24. Transient Thermal Resistance



### **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### PowerDI3333-8 (Type D)

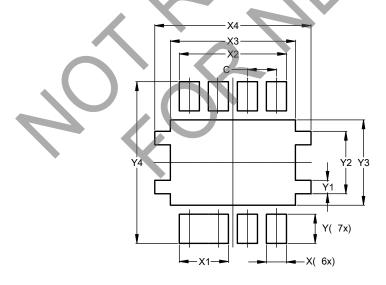


PowerDI33330-8 (Type D)					
Dim	Min	Max	Тур		
Α	1.17	1.23	1.20		
A1	0.00	0.05	0.02		
A3	0.15	0.25	0.20		
A3a	0.05	0.15	0.10		
b	0.30	0.40	0.35		
b2	0.95	1.05	1.00		
D	3.20	3.40	3.30		
D2	2.65	2.75	2.70		
E	3.20	3.40	3.30		
E2	1.75	1.85	1.80		
d	0.15	0.25	0.20		
е		-	0.65		
k		) <u></u>	0.30		
k1	0.21	0.31	0.26		
	0.40	0.50	0.45		
La	0.15	0.25	0.20		
Z	0.25	0.35	0.30		
All Dimensions in mm					

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### PowerDI3333-8 (Type D)



Dimensions	Value
Diffictions	(in mm)
С	0.650
Х	0.450
X1	1.100
X2	2.400
Х3	2.800
X4	3.500
Υ	0.650
Y1	0.300
Y2	1.390
Y3	1.900
Y4	3.600



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DMN3012LDG

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