# PT4120 Series

20-W 48-V Input Isolated DC/DC Converter

SLTS092B

(Revised 2/26/2002)



#### **Features**

- Input Voltage Range:
- 36V to 75V
- 20W Rated
- 82% Efficiency
- 1500 VDC Isolation
- Low Profile (8.5 mm)
- Small Footprint: 1.52in x 1.73in
- Remote On/Off

- Short Circuit Protection
- Over Temperature Shutdown
- Under-Voltage Lockout
- UL1950 Recognized
- CSA 22.2 950 Certified
- EN60950 Approved
- 4×106 Hrs MTBF

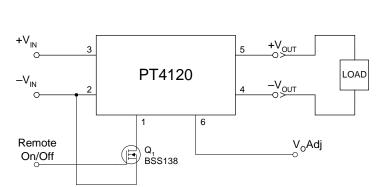
#### **Description**

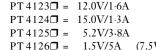
The PT4120 power modules are a series of isolated DC/DC converters housed a lowprofile package. Rated for 20 watts or 5A, the series includes standard output voltages ranging from as low as 1.5VDC to 15VDC. The output may be adjusted  $\pm 10\%$  of nominal. These converters are ideal for Telecom, Industrial, Computer, and other distributed power applications that require input-tooutput isolation.

Using multiple PT4120 modules, system designers can implement a complete custom power supply solution. The flexibility of full isolation also allows the input or output to be configured for negative voltage operation.

The PT4120 series requires no additional components for proper operation.

# Standard Application





PT Series Suffix (PT1234x)

\* Previously known as package style 710.

(Reference the applicable package code drawing for the dimensions and PC board layout)

**Ordering Information**  $PT 4121 \square = 3.3 V/5 A$ 

PT 4122**□** =

Case/Pin

Horizontal

SMD

Configuration

 $PT 4128 \square = 1.5V/5A (7.5W)$   $PT 4127 \square = 1.8V/5A (9W)$   $PT 4128 \square = 2.5V/5A (12.5W)$   $PT 4129 \square = 1.65V/5A (8.25W)$ 

Order

Suffix

A

С

5.0V/4A

(16.5W)

Package

Code

(EGD)

(EGE)

# Pin-Out Information Pin Function

1	Remote On/Off †
2	-Vin
3	+Vin
4	-Vout
5	+Vout
6	Vout Adjust †
+ Fa	w further information, see

application notes.



20-W 48-V Input Isolated **DC/DC Converter** 

				-			
Characteristic	Symbol	Conditions		Min	Тур	Max	Units
Output Current	Io	Over V <sub>in</sub> range	$V_{o} = 15V$ $V_{o} = 12V$ $V_{o} = 5.0V$ $V_{o} \le 3.3V$	0.1 (1) 0.1 (1) 0.1 (1) 0.1 (1)		1.3 1.6 4.0 5.0	А
Input Voltage Range	Vin	Over I <sub>o</sub> Range	V 0 = 9.9 V	36.0	48.0	75.0	VDC
Set Point Voltage Tolerance	Votol	over 10 runge	V₀≥5.0V	_	±1	±1.5	%V <sub>0</sub>
occi onic voitage inclunee	1000		$\frac{V_0 \leq 3.3V}{V_0 \leq 3.3V}$	_	_	±50	mV
Temperature Variation	Reg <sub>temp</sub>	-40° ≤T <sub>a</sub> ≤+85°C	0	_	±0.5	_	%Vo
LineRegulation	Regline	Over V <sub>in</sub> range	V₀≥5.0V	_	±0.2	±1.0	%Vo
e e	0	C C	V₀≤3.3V	_	±7	±33	mV
Load Regulation	Regload	Over I <sub>o</sub> range	Vo≥5.0V	_	±0.4	±1.0	%Vo
			V₀≤3.3V	_	±13	±33	mV
Total Output Voltage Variation	$\Delta V_{o}$ tot	Includes set-point, line load,	V₀≥5.0V		±2	_	%Vo
		$-40^{\circ} \le T_a \le +85^{\circ}C$	V₀≤3.3V	—	±67	—	mV
Efficiency	η		$V_0 = 15V$ $V_0 = 12V$	—	86 83	—	
			$V_0 = 12 V$ $V_0 = 5.0 V$	_	85 82	_	%
			$V_0 = 5.0V$ $V_0 = 3.3V$	—	78	_	
V Diamle (al. al.)	17	20MTI-L., L.: LL	V <sub>0</sub> =1.8V	—	67		0/17
Vo Ripple (pk-pk)	Vr	20MHz bandwidth	$\frac{V_o \ge 5.0V}{V_o \le 3.3V}$	_	0.5		%V <sub>o</sub> mV <sub>pp</sub>
Transient Response	t <sub>tr</sub>	0.1A/µs, load step 50% to 100%		_	100		us
Transient Response		V <sub>o</sub> over/undershoot	$V_0 \ge 5.0V$		±3.0		μs %Vo
	$\Delta V_{tr}$	v <sub>0</sub> over/undershoot	$\frac{V_0 \le 3.0V}{V_0 \le 3.3V}$	_	±150		mV
Short Circuit Current	I <sub>sc</sub>		10=5151	_	2xLomax	_	A
Switching Frequency	fs	Over V <sub>in</sub> range	$V_0 \ge 12.0V$ $V_0 \le 5.2V$	600 800	650 850	700 900	kHz
Under-Voltage Lockout	UVLO			_	31	_	V
Remote On/Off (Pin 1) Input High Voltage Input Low Voltage	$V_{\mathrm{IH}}$ $V_{\mathrm{IL}}$	Referenced to -Vin (pin 2)		2.5 -0.2	_	7.0 <sup>(2)</sup> +0.8	V
Input Low Current	I <sub>IL</sub>			—	-10	-	μA
Standby Input Current	I <sub>in</sub> standby	pins 1 & 2 connected		—	7	50	mA
Internal Input Capacitance	C <sub>in</sub>	n		_	0.5		μF
External Output Capacitance	C <sub>out</sub>	Between +Vo and -Vo		0	-	200	μF
Isolation Voltage Capacitance		Input to output		1500	1100	_	V pF
Resistance				10		_	$M\Omega$
Operating Temperature Range	Ta	Over V <sub>in</sub> range		-40	_	+85 (3)	°C
Storage Temperature	Ts	—		-40	—	+125	°C
Reliability	MTBF	Per Bellcore TR-332 50% stress, T <sub>a</sub> =40°C, ground be	nign	4.0	-	_	106 Hrs
Mechanical Shock	—	Per Mil-Std-883D, method 2002 1mS, half-sine, mounted to a fixt		—	500	—	G's
Mechanical Vibration	—	Per Mil-Std-883D, method 2007 20-2000Hz, soldered in a PC box		_	15	_	G's
Weight	—	_		_	23	_	grams
Flammability	_	Materials meet UL 94V-0					

**Specifications** (Unless otherwise stated,  $T_a = 25^{\circ}$ C,  $V_{in} = 48$ V,  $C_{out} = 0\mu$ F, and  $I_o = I_o max$ )

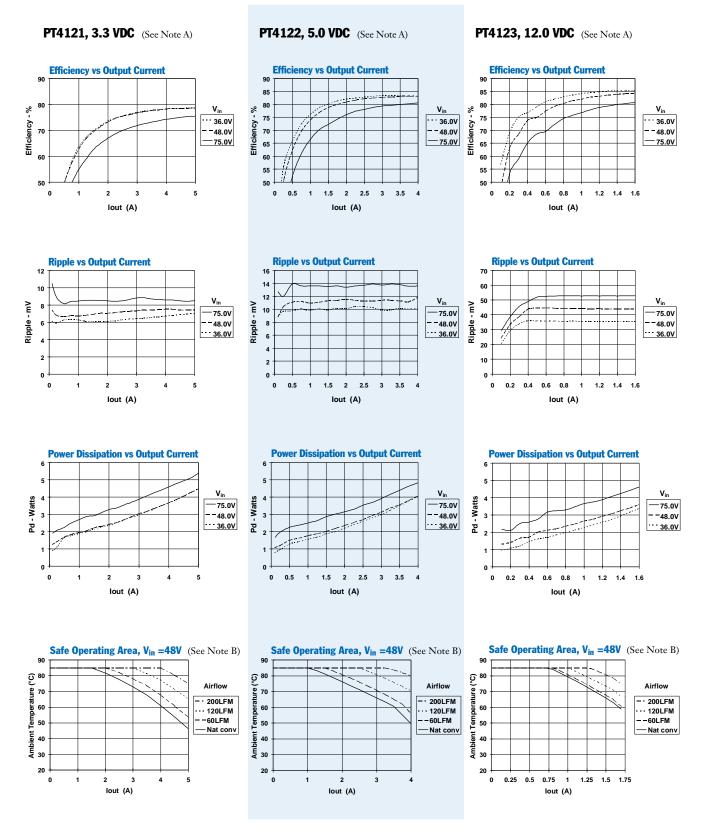
**V** Texas Instruments

Notes: (1) The DC/DC converter will operate at no load with reduced specifications.
(2) The Remote On/Off (pin 1) has an internal pull-up, and if it is left open circuit the PT4120 will operate when input power is applied. Refer to the application notes for interface considerations.
(3) See Safe Operating Area curves or contact the factory for the appropriate derating.

For technical support and more information, see inside back cover or visit www.ti.com

# Typical Characteristics

20-W 48-V Input Isolated DC/DC Converter



**Note A:** Characteristic data has been developed from actual products tested at 25°C. This data is considered typical data for the Converter. **Note B:** SOA curves represent the conditions at which internal components are at or below the manufacturer's maximum operating temperatures



# Using the Remote On/Off Function on the PT4120/ PT4140 Series of Isolated DC/DC Converters

For applications requiring output voltage on/off control, the PT4120/4140 series of DC/DC converters incorporate a remote on/off function. This function may be used in applications that require battery conservation, power-up/ shutdown sequencing, and/or to co-ordinate the powerup of the regulator for active in-rush current control. (See the related application note, AN21).

This function is provided by the *Remote On/Off* control, pin1. If pin 1 is left open-circuit, the converter provides a regulated output whenever a valid source voltage<sup>3</sup> is applied between  $+V_{in}(pin 3)$ , and  $-V_{in}(pin 2)$ . Applying a low-level ground signal <sup>1</sup> to pin 1 will disable the regulator output <sup>5</sup>.

Table 1 provides details of the threshold requirements for the *Remote On/Off* pin. Figure 1 shows how a discrete MOSFET  $(Q_1)^4$ , may be referenced to the negative input voltage rail and used with this control input.

Table 1 Inhibit Control Th	resholds
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Parameter	min	max	
Enable (VIH)	2.5V	(Open Circuit) <sup>2,4</sup>	
Disable (V <sub>IL</sub> )	-0.3V	0.8V	

#### Notes:

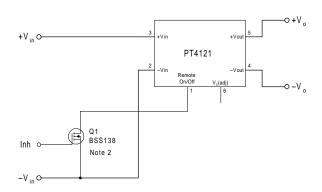
- 1. The on/off control uses  $-V_{in}$  (pin 2), the primary side of the converter as its ground reference. All voltages specified are with respect to  $-V_{in}$ .
- 2. The on/off control internal circuitry is a high impedance  $10\mu$ A current source. The open-circuit voltage may be as high as 8.3Vdc.
- 3. The PT4120/40 series incorporates an "Under Voltage Lockout" (UVLO) function. This function automatically inhibits the converter output until there is sufficient input voltage for the converter to produce a regulated output. Table 2 gives the applicable UVLO thresholds.

Table 2 UVLO Thresholds					
Series	UVLO Threshold	V <sub>in</sub> Range			
PT4120	31V Typical	36-75V			
PT4140	15V Typical	18 - 40V			

- The *Remote On/Off* input of the PT4120/40 series regulators must be controlled with an open-collector (or open-drain) discrete transistor or MOSFET. <u>Do not</u> use a pull-up resistor.
- 5. When the converter output is disabled, the current drawn from the input supply is typically reduced to 8mA (16mA maximum).

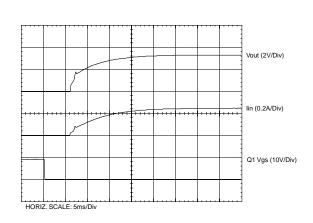
6. Keep the on/off transition to less than 1ms. This prevents erratic operation of the ISR, whereby the output voltage may drift un-regulated between 0V and the rated output during power-up.





**Turn-On Time:** The converter typically produces a fully regulated output voltage within 50ms after the application of power, or the removal of the low voltage signal <sup>6</sup> from the *Remote On/Off* pin. The actual turn-on time will vary with the input voltage, output load, and the total amount of capacitance connected to the output. Using the circuit of Figure 1, Figure 2 shows the output voltage and input current waveforms of a PT4121 after  $Q_1$  is turned off. The turn off of  $Q_1$  corresponds to the drop in  $Q_1 V_{gs}$  voltage. The waveforms were measured with a 48Vdc input voltage, and 2.75-A resistive load.

#### Figure 2



## Adjusting the Output Voltage of the PT4120/ PT4140 Series of Isolated DC/DC Converters

The factory pre-set output voltage of Power Trends' PT4120 and PT4140 series of isolated DC/DC converters may be adjusted within  $\pm 10\%$  of nominal. Adjustment is made from the secondary side of the regulator<sup>1</sup> with a single external resistor. For the input voltage range specified in the data sheet Table 1 gives the allowable adjustment range for each model, as V<sub>o</sub> (min) and V<sub>o</sub> (max).

**Adjust Up:** An increase in the output voltage is obtained by adding a resistor,  $R_2$  between pin 6 (V<sub>o</sub> adjust), and pin 4 (-V<sub>out</sub>).

**Adjust Down:** Add a resistor  $(R_1)$ , between pin 6 (V<sub>o</sub> adjust) and pin 5 (+V<sub>out</sub>).

Refer to Figure 1 and Table 2 for both the placement and value of the required resistor,  $(R_1)$  or  $R_2$ .

### Notes:

- 1. The PT4120 and PT4140 series of DC/DC converters incorporate isolation between the  $V_{in}$  and  $V_o$  terminals. Adjustment of the output voltage is made to the regulation circuit on the secondary or output side of the converter.
- 2. The maximum rated output power for this series is 20W. An increase in the output voltage may therefore require a corresponding reduction in the maximum output current (*see Table 1*). The revised maximum output current must be determined as follows:-

$$I_o(max) = \frac{20}{V_a} A$$
, or 5A, whichever is less.

Where V<sub>a</sub> is the adjusted ouput voltage.

3. Use only a single 1% resistor in either the  $(R_1)$  or  $R_2$  location. Place the resistor as close to the ISR as possible.

Iable	21
DC/DC	C CONVERTER ADJUSTMENT RANGE AND FORMULA PARAMETERS

4. Never connect capacitors to  $V_o$  adjust. Any capacitance added to the  $V_o$  adjust control pin will affect the stability of the ISR.

The values of  $(R_1)$  [adjust down], and  $R_2$  [adjust up], can also be calculated using the following formulas.

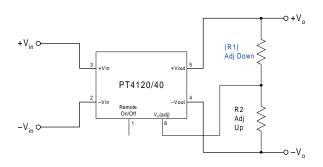
$$(\mathbf{R}_{1}) = \frac{\mathbf{K}_{o} (\mathbf{V}_{a} - \mathbf{V}_{r})}{\mathbf{V}_{r} (\mathbf{V}_{o} - \mathbf{V}_{a})} - \mathbf{R}_{s} \qquad k\Omega$$

$$R_2 = \frac{K_o}{(V_a - V_o)} - R_s \quad k\Omega$$

Where Vo = Original output voltage

- V<sub>a</sub> = Adjusted output voltage
- $V_r$  = Reference voltage (Table 1)
- $K_0$  = Multiplier constant (Table 1)
- R<sub>s</sub> = Internal series resistance (Table 1)

#### Figure 1



Series Pt #									
48V Bus	PT4126	PT4129	PT4127	PT4128	PT4121	PT4122	PT4125	PT4123	PT4124
24V Bus	PT4146		PT4147	PT4148	PT4141	PT4142		PT4143	PT4144
Max Current 2	5A	5A	5A	5A	5A	4A	3.8A	1.6A	1.3A
V <sub>o</sub> (nom)	1.5	1.65	1.8	2.5	3.3	5.0	5.2V	12.0	15.0
Va(min)	1.35	1.49	1.62	2.25	2.95	4.5	4.75	10.8	13.5
Va(max)	1.65	1.81	1.98	2.75	3.65	5.5	5.75	13.2	16.5
Vr	1.225	1.225	1.225	1.225	1.225	2.5	2.5	2.5	2.5
K <sub>o</sub> (V·kΩ)	67.07	63.9	69.7	64.2	69.3	125.2	134.7	139.8	137.6
R <sub>s</sub> (kΩ)	43.2	66.5	110.0	187.0	187.0	187.0	243.0	110.0	90.9



#### PT4120/4140 Series

#### Table 2

PT4126           PT4146           1.5Vdc           (2.8)kΩ           (53.2)kΩ           (204.0)kΩ           1.3MΩ           627.0kΩ           404.0kΩ	PT4127 PT4147 1.8Vdc (51.7)kΩ (161.0)kΩ (489.0)kΩ	PT4128 PT4148 2.5Vdc	PT4121 PT4141 3.3Vdc	V <sub>a</sub> (req'd) 4.5 4.5 4.6 4.6 4.7	PT4122           PT4142           5.0Vdc           (12.6)kΩ           (40.3)kΩ           (75.0)kΩ           (120.0)kΩ	V <sub>a</sub> (req'd) 10.8 11.0 11.2 11.4	PT4123           PT4143           12.0Vdc           (276.0)kΩ           (365.0)kΩ           (497.0)kΩ	PT4124 PT4144 15.0Vdc
PT4146           1.5Vdc           (2.8)kΩ           (53.2)kΩ           (204.0)kΩ           1.3MΩ           627.0kΩ	PT4147 1.8Vdc (51.7)kΩ (161.0)kΩ	PT4148	PT4141	4.5 4.55 4.6 4.65	PT4142           5.0Vdc           (12.6)kΩ           (40.3)kΩ           (75.0)kΩ	10.8 11.0 11.2	PT4143           12.0Vdc           (276.0)kΩ           (365.0)kΩ	PT4144
(2.8)kΩ (53.2)kΩ (204.0)kΩ 1.3MΩ 627.0kΩ	(51.7)kΩ (161.0)kΩ	2.5Vdc	3.3Vdc	4.5 4.55 4.6 4.65	(12.6)kΩ (40.3)kΩ (75.0)kΩ	10.8 11.0 11.2	(276.0)kΩ (365.0)kΩ	15.0Vdc
(53.2)kΩ (204.0)kΩ 1.3MΩ 627.0kΩ	(161.0)kΩ			4.5 4.55 4.6 4.65	(12.6)kΩ (40.3)kΩ (75.0)kΩ	10.8 11.0 11.2	(365.0)kΩ	
(53.2)kΩ (204.0)kΩ 1.3MΩ 627.0kΩ	(161.0)kΩ			4.55 4.6 4.65	(40.3)kΩ (75.0)kΩ	11.0 11.2	(365.0)kΩ	
(53.2)kΩ (204.0)kΩ 1.3MΩ 627.0kΩ	(161.0)kΩ			4.55 4.6 4.65	(40.3)kΩ (75.0)kΩ	11.0 11.2	(365.0)kΩ	
(204.0)kΩ 1.3MΩ 627.0kΩ	(161.0)kΩ			4.6 4.65	(75.0)kΩ	11.2		
1.3MΩ 627.0kΩ	(161.0)kΩ			4.65	· · · · ·		()	
627.0kΩ	(161.0)kΩ						(719.0)kΩ	
	(161.0)kΩ			+./	(179.0)kΩ	11.6	(1.16)MΩ	
	(161.0)kΩ			4.75	(262.0)kΩ	11.8		
	(161.0)kΩ			4.8	(387.0)kΩ	12.0		
	× /			4.85	(595.0)kΩ	12.2	588.0kΩ	·
	( /			4.9	(1.01)MΩ	12.4	239.0kΩ	·
				4.95	~ /	12.6	123.0kΩ	·
	1.28MΩ			5.0		12.8	64.6kΩ	
	587.0kΩ			5.05		13.0	29.7kΩ	-
	355.0kΩ			5.1	1.06MΩ	13.2	6.4kΩ	
		(26.5)kΩ		5.15	645.0kΩ	13.5		(312.0)kΩ
				5.2	437.0kΩ	13.6		(345.0)kΩ
				5.25	312.0kΩ			(427.0)kΩ
		(425.0)kΩ		5.3	229.0kΩ	14.0		(542.0)kΩ
		(1.09)MΩ		5.35	169.0kΩ	14.2		(713.0)kΩ
				5.4	125.0kΩ	14.4		(1.0)MΩ
		1.09MΩ		5.45	90.2kΩ	14.6		(1.57)Mg
		450.0kΩ		5.5	62.4kΩ	14.8		
		237.0kΩ				15.0		
		131.0kΩ				15.2		597.0kΩ
		67.7kΩ		-		15.4		253.0kΩ
			(90.7)kΩ			15.6		138.0kΩ
			(146.0)kΩ			15.8		81.0kΩ
			(224.0)kΩ			16.0		46.6kΩ
			(341.0)kΩ	-		16.5		$0.8 \text{k}\Omega$
			(536.0)kΩ					
			(926.0)kΩ					
			(2.09)MΩ					
			1.19MΩ					
			502.0kΩ					
			272.0kΩ					
			158.0kΩ					
			88.7kΩ					
			42.7kΩ					
			(92.9)kΩ (203.0)kΩ (425.0)kΩ (1.09)MΩ 1.09MΩ 450.0kΩ 237.0kΩ 131.0kΩ	$(92.9) k\Omega$ $(203.0) k\Omega$ $(425.0) k\Omega$ $(1.09) M\Omega$ $1.09 M\Omega$ $450.0 k\Omega$ $237.0 k\Omega$ $131.0 k\Omega$ $67.7 k\Omega$ $(90.7) k\Omega$ $(146.0) k\Omega$ $(224.0) k\Omega$ $(224.0) k\Omega$ $(341.0) k\Omega$ $(341.0) k\Omega$ $(536.0) k\Omega$ $(2.09) M\Omega$ $1.19 M\Omega$ $502.0 k\Omega$ $272.0 k\Omega$ $158.0 k\Omega$ $88.7 k\Omega$	$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	$\begin{array}{ c c c c c c c c c c c c c c c c c c c$

 $R_1 = (Blue)$   $R_2 = Black$ 

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