Designated client product

This product will be discontinued its production in the near term. And it is provided for customers currently in use only, with a time limit. It can not be available for your new project. Please select other new or existing products.

For more information, please contact our sales office in your region.

New Japan Radio Co.,Ltd.

www.njr.com

HIGH SPEED DIFFERENTIAL COMPARATOR

GENERAL DESCRIPTION

The NJM360 is a very high speed differential input, complementary TTL output voltage comparator. The device has been optimized for greater speed, input impedance and fan-out and lower input offset voltage.

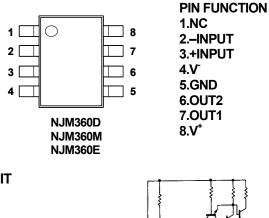
Applications involve high speed analog to digital converters and zero-crossing detectors in disc file systems.

FEATURES

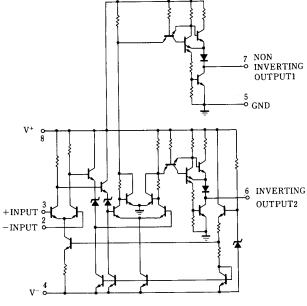
- Operating Voltage (±4.5V~±6.5V)
- High Speed Guarantee (20ns max.)
- Both output delay time has been precisely adjusted
- Complementary TTL Output
- High Input Impedance
- Stabilized Speed for Over Driving Change
- Bipolar Technology

Package Outline

- Fan-out is 4
- Low Input Offset Voltage
- DIP8, DMP8, SOP8 JEDEC 150mil
- PIN CONFIGURATION







PACKAGE OUTLINE





NJM360D (DIP8)

NJM360M (DMP8)



NJM360E

(SOP8)

■ ABSOLUTE MAXIMUM RATINGS

			(Ta=25°C)
PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V ⁺ /V ⁻	±8	V
Differential Input Voltage	VID	±5	V
Input Voltage	VI	±8 (note1)	V
Power Dissipation	PD	(DIP8) 500 (DMP8) 300 (SOP8) 300	mW
Maximum Output Current	lo	± 20	mA
Operating Temperature Range	T _{opr}	-40~+85	D°
Storage Temperature Range	T _{stg}	-40~+125	°C

(note1) For supply voltage less than $\pm 8V$, the absolute input voltage is equal to the supply voltage.

■ ELECTRICAL CHARACTERISTICS

		(Ta=25°C)				
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Operating Supply Voltage	V ⁺		4.5	5	6.5	V
Operating Supply Voltage	V		-4.5	-5	-6.5	V
Input Offset Voltage	V _{IO}	R _s ≤200Ω	-	2	5	mV
Input Offset Current	I _{IO}		-	0.5	3	μA
Input Bias Current	IB		-	5	20	μA
Output Resistance	Ro	V _{OUT} =V _{OM}	-	100	-	Ω
Response Time 1	t _{R1}	V ⁺ /V ⁻ =±5V (note1)	-	13	25	ns
Response Time 2	t _{R2}	V^{\dagger} $V^{=\pm}$ 5V (note2)	-	12	20	ns
Response Time 3	t _{R3}	V ⁺ /V ⁻ =±5V (note3)	-	14	-	ns
Response Time Difference Between Outputs						
(t _{pd} of+V _{IN1})-(t _{pd} of-V _{IN2})		(note1)	-	2	-	ns
(t _{pd} of+V _{IN2})-(t _{pd} of-V _{IN1})		(note1)	-	2	-	ns
(t _{pd} of+V _{IN1})-(t _{pd} of+V _{IN2})		(note1)	-	2	-	ns
$(t_{pd} \text{ Of-V}_{IN1}) - (t_{pd} \text{ Of-V}_{IN2})$	_	(note1)	-	2	-	ns
Input Resistance	R _{IN}	f=1MHz	-	17	-	kΩ
Input Capacitance	CIN	f=1MHz	-	3	-	pF
Average Temperature Coefficient of Input Offset Voltage	$\Delta V_{IO} / \Delta T$	R _s =50Ω	-	8	-	µV/°C
Average Temperature Coefficient of Input Offset Current	ΔΙ _{ΙΟ} /ΔΤ		-	7	-	nA/°C
Common Mode Input Voltage Range	VICM	V ⁺ ∕V ⁻ =± 6.5V	±4	± 4.5	-	V
Differential Input Voltage Range	VID		±5	-	-	V
Output High Voltage (High)	Vон	V ⁺ /V ⁻ =± 4.5V,I _{OUT} =-320µA	2.4	3	-	V
Output Low Voltage (Low)	V _{OL}	V ⁺ /V ⁻ =± 4.5V,I _{SINK} =6.4mA	-	0.25	0.4	V
Positive Supply Current	l l	$V^{+}N^{-}=\pm 6.5V$	-	18	32	mA
Negative Supply Current		V ⁺ ∕√=±6.5V	-	-9	-16	mA

(note1) Response time measured from the 50% point of a $30mV_{PP}$ 10MHz sinusoidal input to the 50% point of the output.

(note2) Response time measured from the 50% point of a $2V_{\text{P,P}}$ 10MHz sinusoidal input to the 50% point of the output.

(note3) Response time measured from the start of a 100mV input step with 5mV overdrive to the time when the output crosses the logic threshold.

+140

+140

t_{Pd} (L→H)

150

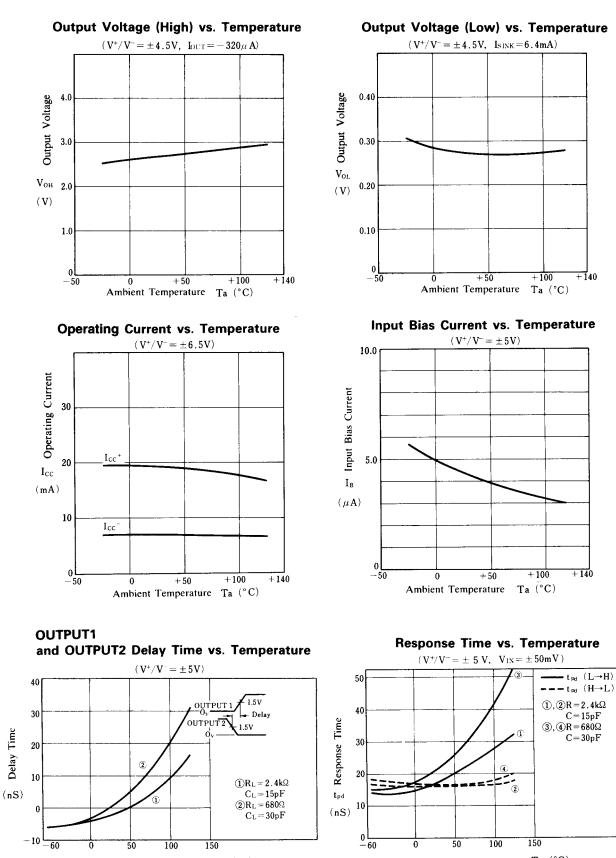
100

 $\label{eq:ambient} \textbf{Ambient Temperature} \quad \textbf{Ta} \ (\ ^{\circ}\textbf{C})$

50

0

■ TYPICAL CHARACTERISTICS



60

50

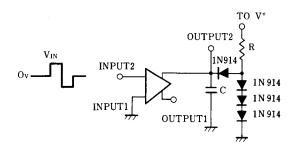
100

Ambient Temperature Ta (°C)

150

- 3 -

■ AC TEST CIRCUIT



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