

UT6JB5

-40V Pch+Pch Power MOSFET

V _{DSS}	-40V
R _{DS(on)} (Max.)	122mΩ
I _D	±3.5A
P _D	2W

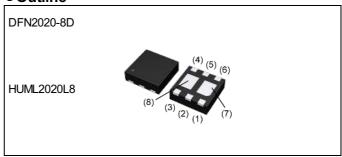
Features

- 1) Low on resistance
- 2) Small surface mount package
- 3) Pb-free plating; RoHS compliant
- 4) Halogen Free

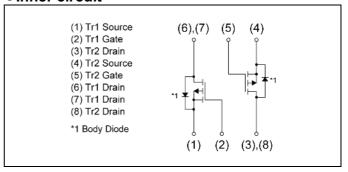
Application

Switching

Outline



●Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	180
Туре	Tape width (mm)	8
,	Quantity (pcs)	3000
	Taping code	TCR
	Marking	JB5

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified) < Tr1 and Tr2>

Parameter	Symbol	Value	Unit
Drain - Source voltage	V _{DSS}	-40	V
Continuous drain current	I _D	±3.5	Α
Pulsed drain current	I _{DP} *1	±14	Α
Gate - Source voltage	V _{GSS}	±20	V
Avalanche current, single pulse	I _{AS} *2	-3.5	Α
Avalanche energy, single pulse	E _{AS} *2	0.9	mJ
Power dissipation	P _D *3	2	W
Junction temperature	T _j	150	°C
Operating junction and storage temperature range	T _{stg}	-55 to +150	°C

●Thermal resistance

Doromotor	Cymbol	Values			Linit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - ambient	R _{thJA} *3	1	-	62.5	°C/W

● Electrical characteristics (T_a = 25°C) < Tr1 and Tr2>

Davamatav	Cymah ol	Conditions	Values			l leit
Parameter Symbol Conditions		Conditions	Min.	Тур.	Max.	Unit
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V$, $I_D = -1mA$	-40	-	-	V
Breakdown voltage	ΔV _{(BR)DSS}	I _D = -1mA		-22		mV/°C
temperature coefficient	ΔT _j	referenced to 25°C	-	-22	-	IIIV/ C
Zero gate voltage drain current	I _{DSS}	V _{DS} = -40V, V _{GS} = 0V		-	-1	μA
Gate - Source leakage current	I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V		-	±100	nA
Gate threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -1 \text{mA}$	-1.0	-	-2.5	V
Gate threshold voltage	$\Delta V_{GS(th)}$	I _D = -1mA		3.7		mV/°C
temperature coefficient	ΔT_{j}	referenced to 25°C	1	3.1	-	mv/C
Static drain - source	D *4	$V_{GS} = -10V, I_D = -3.5A$	-	95	122	m0
on - state resistance	R _{DS(on)} *4	V_{GS} = -4.5V, I_D = -3.5A	-	121	155	mΩ
Gate resistance	R _G	f=1MHz, open drain	ı	15	-	Ω
Forward Transfer Admittance	Y _{fs} *4	$V_{DS} = -5V, I_{D} = -3.5A$	2.6	-	-	S

^{*1} Pw \leq 10µs, Duty cycle \leq 1%

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^{*2} L \simeq 0.1mH, V_{DD} = -20V, R_G = 25 Ω , Starting T_j = 25 $^{\circ}$ C Fig.3-1,3-2

^{*3} Mounted on a Cu board (40×40×0.8mm)

^{*4} Pulsed

● Electrical characteristics (T_a = 25°C) < Tr1 and Tr2>

Darameter	Cumbal	Conditions	Values			Unit
Parameter	Symbol Conditions		Min.	Тур.	Max.	Uniit
Input capacitance	C _{iss}	V _{GS} = 0V	-	265	-	
Output capacitance C _{oss}		V _{DS} = -20V	-	18	-	pF
Reverse transfer capacitance	C _{rss}	C _{rss} f = 1MHz		13	-	
Turn - on delay time	t _{d(on)} *4	$V_{DD} \simeq -20V, V_{GS} = -10V$	-	7.4	-	
Rise time t_r^{*4}		I _D = -1.75A	-	8.5	-	
Turn - off delay time	t _{d(off)} *4	$R_L = 11.4\Omega$	-	42	-	ns
Fall time	t _f *4	$R_G = 10\Omega$	-	18	-	

● Gate charge characteristics (T_a = 25°C) < Tr1 and Tr2>

Darameter	Cumbal	Canditions		Values			1 1:4
Parameter	Symbol Conditions -		Min.	Тур.	Max.	Unit	
Total mate above	O *4		V _{GS} = -10V	-	6.2	-	
Total gate charge	Q_g^{*4}	V _{DD} ≃ -20V		-	3.3	-	0
Gate - Source charge	Q _{gs} *4	I _D = -3.5A	V _{GS} = -4.5V	-	1.4	-	nC
Gate - Drain charge	Q _{gd} *4			-	1.1	-	

● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

<Tr1 and Tr2>

Parameter	Symbol	Conditions	Values			Unit	
r al allietei	Symbol	Conditions	Min.	Тур.	Max.	Offic	
Continuous forward current	I _S	T _a = 25°C	-	-	-1.67	^	
Pulse forward current	I _{SP} *1	1 _a – 25 C	1	ı	-14	A	
Forward voltage	V _{SD} *4	V _{GS} = 0V, I _S = -1.67A	-	-	-1.2	V	
Reverse recovery time	t _{rr} *4	I _S = -3.5A, V _{GS} = 0V	-	22	-	ns	
Reverse recovery charge	Q _{rr} *4	di/dt = 100A/μs	-	23	-	nC	

Fig.1 Power Dissipation Derating Curve

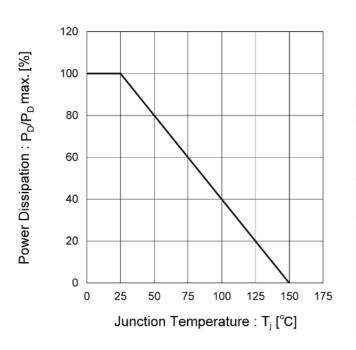


Fig.2 Maximum Safe Operating Area

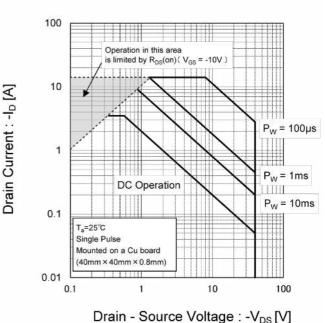


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

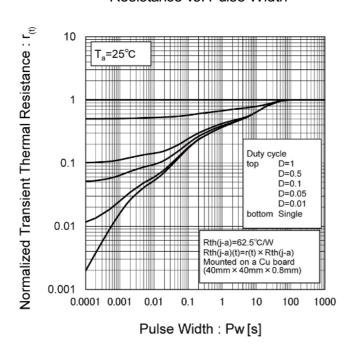


Fig.4 Single Pulse Maximum Power Dissipation

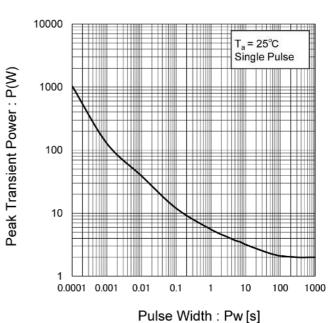


Fig.5 Typical Output Characteristics(I)

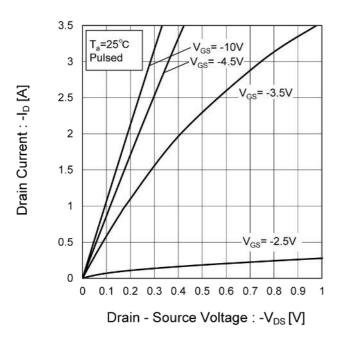
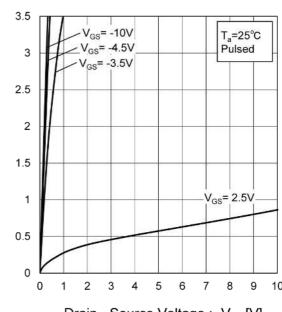


Fig.6 Typical Output Characteristics(II)

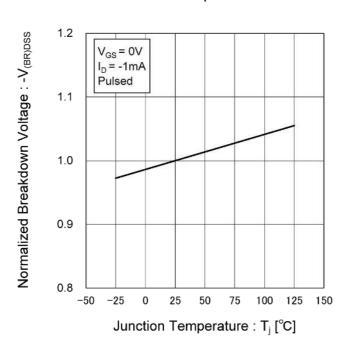


Drain Current: -I_D [A]

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Drain - Source Voltage : -VDS [V]

Fig.7 Breakdown Voltage vs.
Junction Temperature



150

• Electrical characteristic curves

Fig.8 Typical Transfer Characteristics

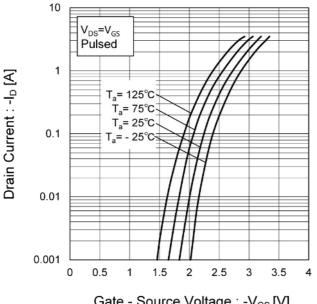
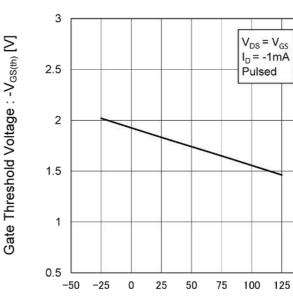


Fig.9 Gate Threshold Voltage vs. **Junction Temperature**



Junction Temperature : T_j [°C]

Gate - Source Voltage : -V_{GS} [V]

Fig.10 Forward Transfer Admittance vs. **Drain Current**

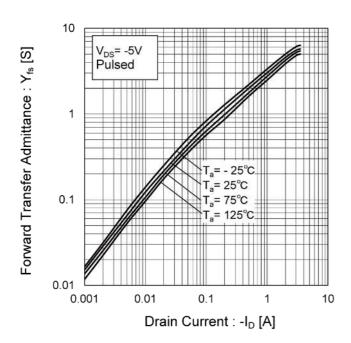


Fig.11 Drain Current Derating Curve

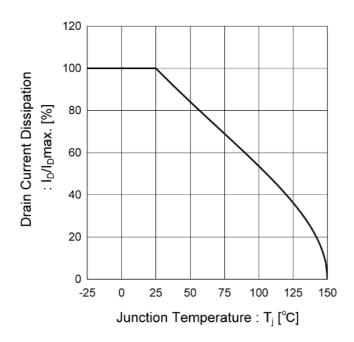


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

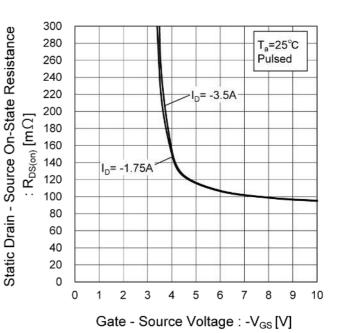


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

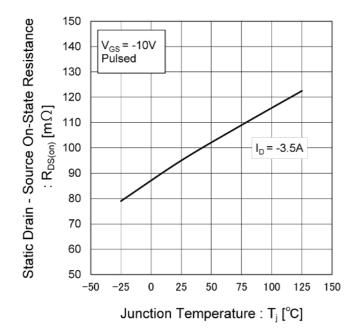


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

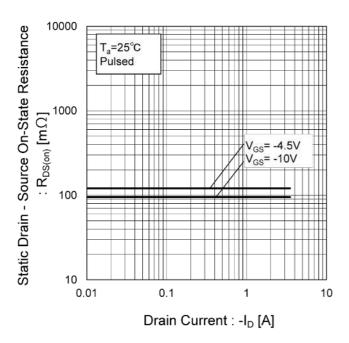


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

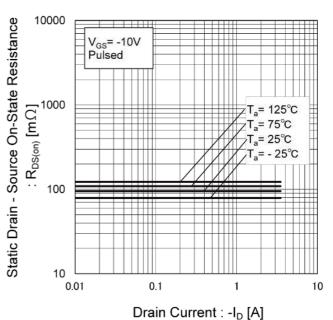


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

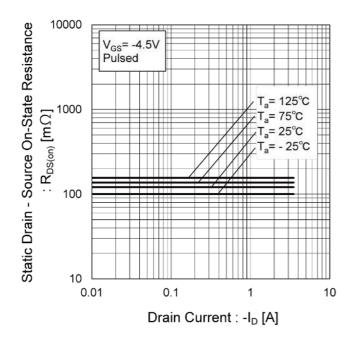


Fig.17 Typical Capacitances vs.

Drain - Source Voltage

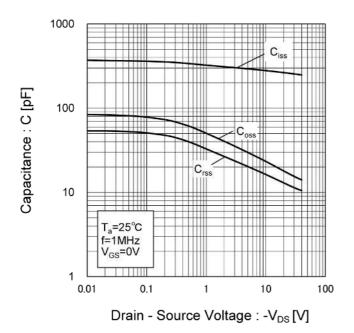


Fig.18 Switching Characteristics

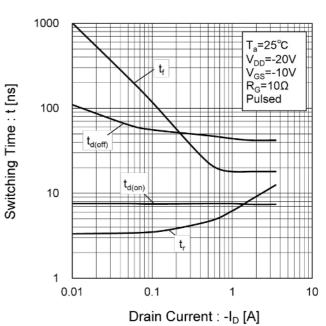


Fig.19 Typical Gate Charge

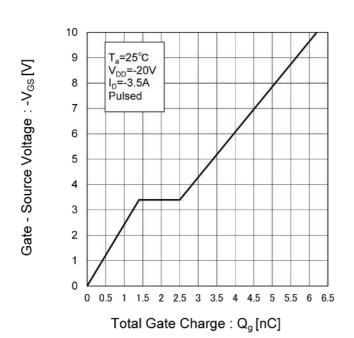
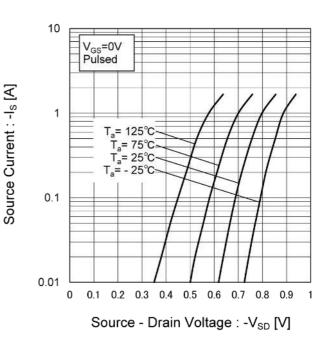


Fig.20 Source Current vs.

Source Drain Voltage



• Measurement circuits < It is the same for the Tr1 and Tr2>

Fig.1-1 Switching Time Measurement Circuit

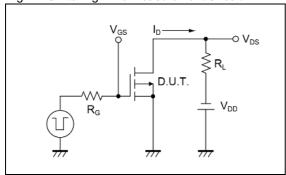


Fig.2-1 Gate Charge Measurement Circuit

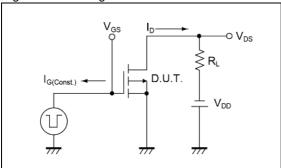


Fig.3-1 Avalanche Measurement Circuit

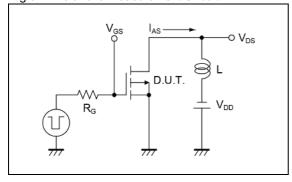


Fig.1-2 Switching Waveforms

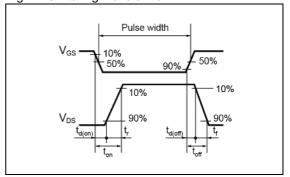


Fig.2-2 Gate Charge Waveform

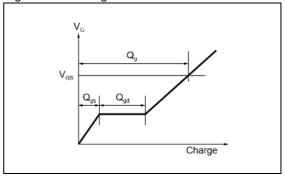
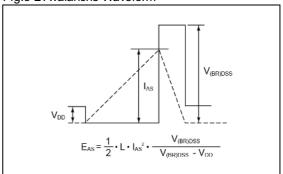


Fig.3-2 Avalanche Waveform

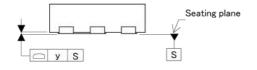


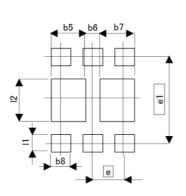
Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

Dimensions

DFN2020-8D





Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIME	TERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	0.55	0.65	0.022	0.026
A1	0.00	0.05	0.000	0.002
b	0.25	0.35	0.010	0.014
b1	0.3	25	0.0	10
b2	0.60	0.70	0.024	0.028
b3	0	.3	0.0	12
b4	0.60	0.70	0.024	0.028
D	1.90	2.10	0.075	0.083
E	1.90	2.10	0.075	0.083
е	0.0	65	0.0	26
Lp	0.225	0.325	0.009	0.013
Lp1	0.80	1.00	0.031	0.039
x	-	0.10	(H)	0.004
V	-	0.10	(1 5)	0.004

DIM -	MILIME	MILIMETERS		HES
DIIVI	MIN	MAX	MIN	MAX
b5	-	0.70	3.51	0.028
b6	0.20	0.30	0.008	0.012
b7	*	0.70		0.028
b8		0.45	-	0.018
e1	1 1.725		0.0	068
11		0.425		0.017
12	-	1.00	104	0.039

Dimension in mm/inches



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CLASSⅢ	CL ACCTI	CLASS II b	СГУССШ
CLASSIV	CLASSII	CLASSⅢ	CLASSⅢ

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 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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