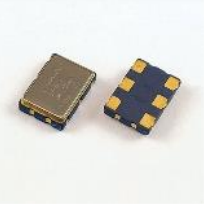
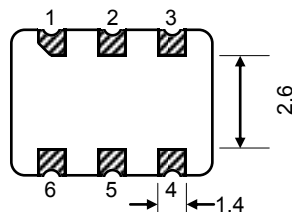
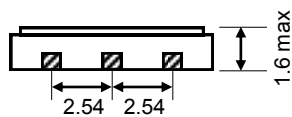
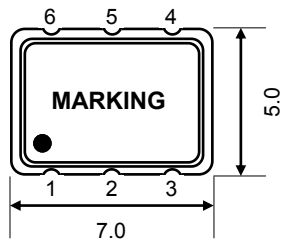


**CERAMIC SMD LVPECL OUTPUT OSCILLATOR (7.0 x 5.0 x 1.6mm)**
**FEATURES:**

- HIGH FREQUENCY
- ULTRA LOW PHASE JITTER PERFORMANCE
- ENABLE / DISABLE

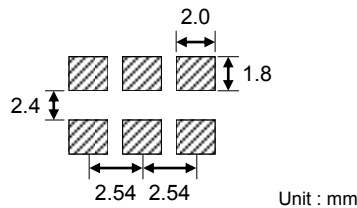

**■ SPECIFICATION**

<b>FREQUENCY RANGE</b>	<b>2.5V:</b> 12.000MHz ~ 312.500MHz <b>3.3V:</b> 12.000MHz ~ 800.000MHz	
<b>FREQUENCY STABILITY</b>	±15ppm ~ ±100ppm	
<b>OPERATING TEMPERATURE RANGE</b>	STD: 0°C ~ 70°C, Option: -40°C ~ 85°C (See P/N guide for other options)	
<b>STORAGE TEMPERATURE RANGE</b>	-55°C ~ 125°C	
<b>SUPPLY VOLTAGE ± 10%</b>	2.5V <sub>DC</sub> ~ 3.3V <sub>DC</sub>	
<b>SUPPLY CURRENT</b>	100mA max.	
<b>OUTPUT</b>	<b>LOGIC FAMILY</b>	LVPECL
	<b>LOAD</b>	50Ω (into V <sub>DD</sub> - 2.0V <sub>DC</sub> )
	<b>LEVEL LOGIC '1' (V<sub>OH</sub>)</b>	V <sub>DD</sub> - 1.025V <sub>DC</sub> min.
	<b>LEVEL LOGIC '0' (V<sub>OL</sub>)</b>	V <sub>DD</sub> - 1.620V <sub>DC</sub> max.
	<b>PEAK TO PEAK</b>	600mV min. 800mV typ.
	<b>SYMMETRY(DUTY CYCLE)</b>	STD: 40% / 60% , Option: 45% / 55%
	<b>RISE AND FALL TIME (Tr/Tf)</b>	250pS typ. 1.0nS max.
<b>STAND-BY VOLTAGE</b>	<b>ENABLE (V<sub>IH</sub>)</b>	0.7 x V <sub>DD</sub> ≤ V <sub>IH</sub> ≤ V <sub>DD</sub> or N/C
	<b>DISABLE (V<sub>IL</sub>)</b>	V <sub>IL</sub> ≤ 0.3 x V <sub>DD</sub>
<b>AGING @ +25°C ±3°C</b>	±3.0ppm / 1 <sup>st</sup> year max.	
<b>PHASE JITTER (12kHz ~ 20MHz)</b>	1.0pS max.	

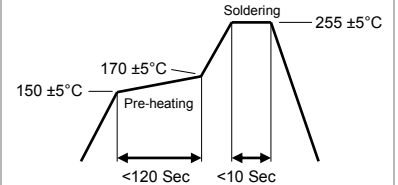
**■ PACKAGE DIMENSIONS**


Unit : mm

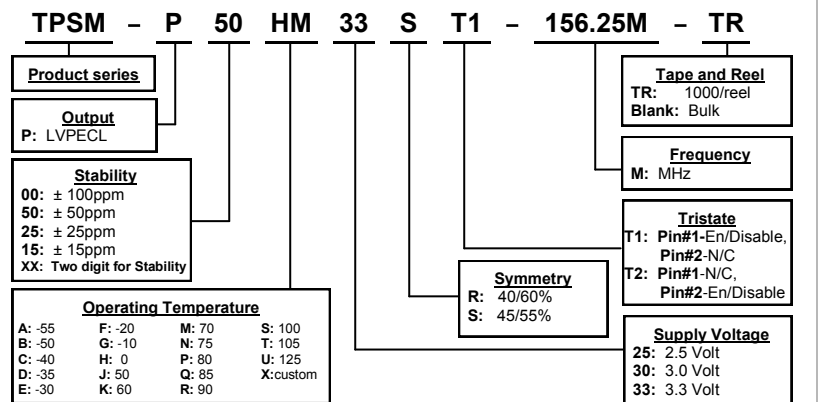
Pin Configuration	
1	N.C. or En/Dis
2	N.C. or En/Dis
3	GND
4	Output
5	C - output
6	V <sub>DD</sub>

**■ SOLDER PATTERN**


Unit : mm

**■ REFLOW PROFILE**


Note: Consult factory for detail

**■ PART NUMBERING GUIDE**


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