General purpose transistor (isolated dual transistors)

IMX25

ROHM

Features

1) Two 2SD2704K chips in a SMT package.

- 2) Mounting possible with SMT3 automatic mounting machine.
- 3) Transistor elements are independent, eliminating interference.

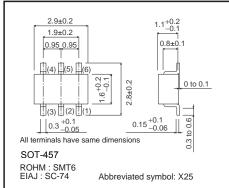
4) Mounting cost and area can be cut in half.

Structure

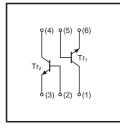
Epitaxial planar type NPN silicon transistor

The following characteristics apply to both Tr1 and Tr2.

•Dimensions (Unit : mm)



Inner circuit



•Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Collector-base voltage	Vсво	50	V
Collector-emitter voltage	Vceo	20	V
Emitter-base voltage	Vebo	25	V
Collector current	lc	300	mA
Power dissipation	Pd	300(TOTAL)	mW *
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

* 200mW per element must not be exceeded.

•Electrical characteristics (Ta=25°C)

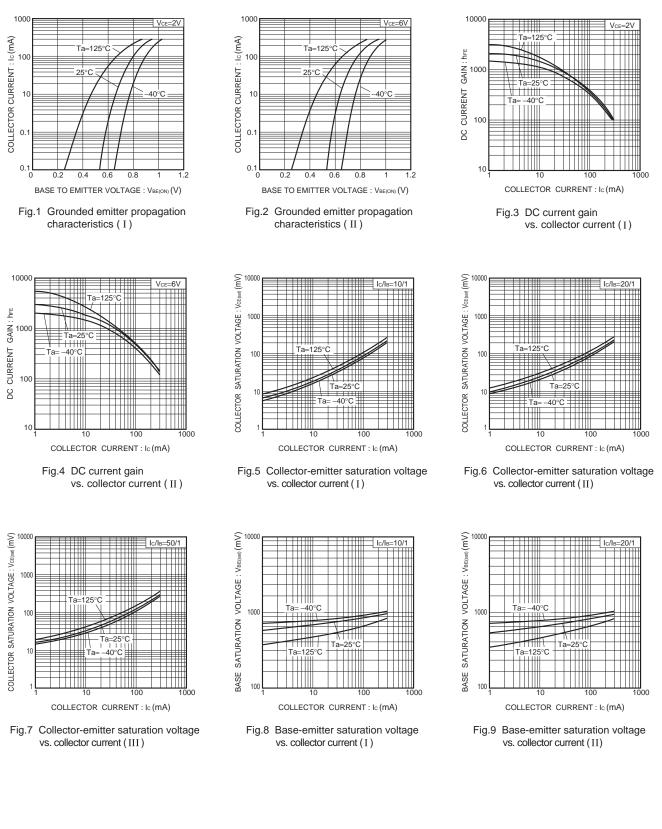
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions		
Collector-base breakdown voltage	ВУсво	50	-	-	V	Ic=10μA		
Collector-emitter breakdown voltage	BVCEO	20	-	-	V	Ic=1mA		
Emitter-base breakdown voltage	ВVево	25	-	-	V	Iε=10μA		
Collector cutoff current	Ісво	_	-	0.1	μΑ	Vcb=50V		
Emitter cutoff current	Іево	_	-	0.1	μΑ	VEB=25V		
Collector-emitter saturation voltage	VCE(sat)	_	50	100	mV	Ic/IB=30mA/3mA		
DC current transfer ratio	hfe	820	-	2700	-	Vce=2V, Ic=4mA		
Transition frequency	fт	_	35	-	MHz	Vce=6V, Ie=-4mA, f=10MHz		
Output capacitance	Cob	_	3.9	-	pF	Vcb=10V, Ie=0A, f=1MHz		
Output On-resistance	Ron	_	0.7	-	Ω	Iв=5mA, Vi=100mVrms, f=1kHz		

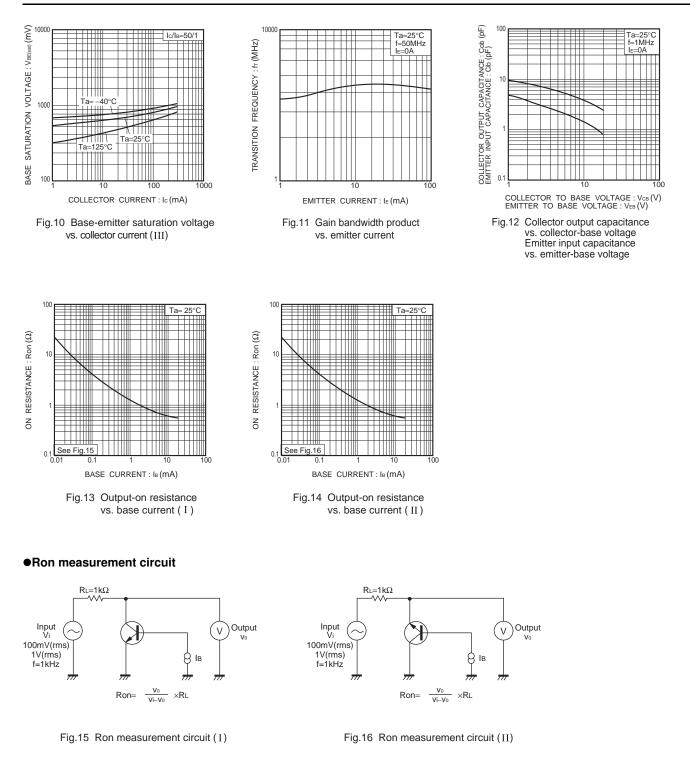
Packaging specifications

	Packaging type	Taping
	Code	T110
Part No.	Basic ordering unit (pieces)	3000
IMX25		0

Data Sheet

•Electrical characteristic curves





This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

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