

CP767V-2N3467

PNP - General Purpose Transistor Die 1.0 Amp, 40 Volt

The CP767V-2N3467 is a silicon PNP transistor designed for general purpose switching applications.



MECHANICAL SPECIFICATIONS:

Die Size	29.9 x 29.9 MILS
Die Thickness	7.1 MILS
Base Bonding Pad Size	3.9 x 4.3 MILS
Emitter Bonding Pad Size	7.9 x 3.9 MILS
Top Side Metalization	AI – 13,000Å
Back Side Metalization	Au – 9,000Å
Scribe Alley Width	1.97 MILS
Wafer Diameter	5 INCHES
Gross Die Per Wafer	18,960

MAXIMUM RATINGS: (T _A =25°C)	SYMBOL		UNITS
Collector-Base Voltage	V_{CBO}	40	V
Collector-Emitter Voltage	V_{CEO}	40	V
Emitter-Base Voltage	V_{EBO}	5.0	V
Continuous Collector Current	IC	1.0	Α
Operating and Storage Junction Temperature	T 1 T-4-	-65 to +150	°C

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SYMBOL	TEST CONDITIONS	T _A =25°C unless otherwise noted MIN) MAX	UNITS
ICBO	V _{CB} =30V		100	nA
ICEV	V_{CE} =30V, V_{BE} =3.0V		100	nA
I _{BEV}	V_{CE} =30V, V_{BE} =3.0V		120	nA
BV CBO	I _C =10μA	40		V
BVCEO	I _C =10mA	40		V
BV _{EBO}	I _E =10μA	5.0		V
V _{CE(SAT)}	I _C =150mA, I _B =15mA		0.3	V
VCE(SAT)	I_C =500mA, I_B =50mA		0.5	V
V _{CE(SAT)}	I _C =1.0A, I _B =100mA		1.0	V
V _{BE} (SAT)	I_C =150mA, I_B =15mA		1.0	V
V _{BE} (SAT)	I_C =500mA, I_B =50mA	0.8	1.2	V
V _{BE(SAT)}	I _C =1.0A, I _B =100mA		1.6	V
h _{FE}	V_{CE} =1.0V, I_{C} =150mA	40		
hFE	V_{CE} =1.0V, I_{C} =500mA	40	120	
h _{FE}	V_{CE} =5.0V, I_{C} =1.0A	40		

R0 (11-September 2019)

CP767V-2N3467



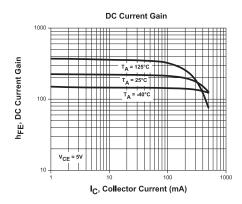
PNP - General Purpose Transistor Die 1.0 Amp, 40 Volt

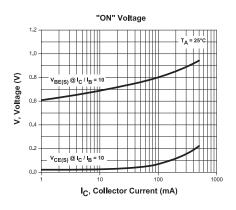
ELECTRIC	CAL CHARACTERISTICS - Continued: (T	_Δ =25°C unless o	therwise noted)	
SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
f_{T}	V_{CE} =10V, I_{C} =50mA, f=100MHz	175		MHz
C_{ob}	V_{CB} =10V, I_E =0, f=100kHz		25	pF
c_{ib}	V_{EB} =0.5V, I_{C} =0, f=100kHz		100	pF
ton	V_{CC} =30V, V_{BE} =2.0V, I_{C} =500mA, I_{B1} =50)mA	40	ns
toff	V_{CC} =30V, I_{C} =500mA, I_{B1} = I_{B2} =50mA		90	ns
Q_{T}	V_{CC} =30V, I_{C} =500mA, I_{B} =50mA		6.0	nC

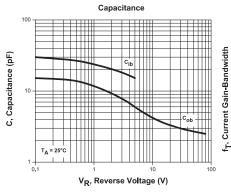
CP767V-2N3467Typical Electrical Characteristics

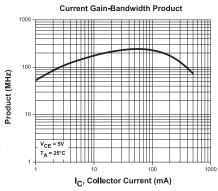


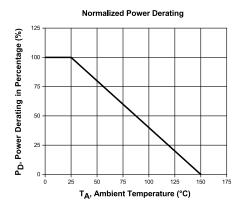
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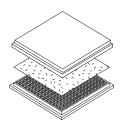






BARE DIE PACKING OPTIONS

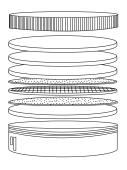




BARE DIE IN TRAY (WAFFLE) PACK

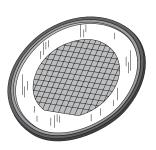
CT: Singulated die in tray (waffle) pack. (example: CP211-PART NUMBER-CT)

CM: Singulated die in tray (waffle) pack 100% visually inspected as per MIL-STD-750, (method 2072 transistors, method 2073 diodes). (example: CP211-PART NUMBER-CM)



UNSAWN WAFER

WN: Full wafer, unsawn, 100% tested with reject die inked. (example: CP211-PART NUMBER-WN)



SAWN WAFER ON PLASTIC RING

 $\boldsymbol{WR} :$ Full wafer, sawn and mounted on plastic ring,

100% tested with reject die inked. (example: CP211-PART NUMBER-WR)

Please note: Sawn Wafer on Metal Frame (WS) is possible as a special order. Please contact your Central Sales Representative at 631-435-1110.



Visit the Central website for a complete listing of specifications: www.centralsemi.com/bdspecs

R2 (3-April 2017)

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- Customer specific screening
- · Up-screening capabilities

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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Worldwide Distributors:

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Product End of Life Notification

PDN ID:	PDN01245
Notification Date:	8/15/22
Last Buy Date:	
Last Shipment Date	8/15/23

Summary: The CP767V wafer process is discontinued and now classified as End of Life (EOL).

Although Central Semiconductor Corp. makes every effort to continue to produce devices that have been proclaimed EOL (End of Life) by other manufacturers, it is an accepted industry practice to discontinue certain devices when customer demand falls below a minimum level of sustainability. Accordingly, the following product(s) have been transitioned to End of Life status as part of Central's ongoing Product Portfolio Management. Any replacement products are noted below. The effective date for placing last purchase orders will be six (6) months from the date of this notice and twelve (12) months from the notice date for final shipments, and minimum order quantities may apply. The last purchase and shipment dates may be extended if inventory is available.

* All Plating types (PBFREE,TIN/LEAD) for each item listed are included in this notice.

Central Part Number	Suggested Replacement
CEN1273-1	N/A
CP767V-2N3467-CT	N/A
2N3467	N/A
2N3468	N/A
2N3763	N/A

Central would be happy to assist you by providing additional information or technical data to help locate an alternate source if we have no replacement available. If you would like assistance, please visit https://my.centralsemi.com/submit-inquiry?type=ER to submit an online inquiry.

DISCLAIMER: This End of Life (EOL) notification is in accordance with JEDEC standard JESD48 - Product Discontinuance. Central Semiconductor Corp. will make every effort to offer life-time buy (LTB) opportunities and/or offer replacement devices to existing customers for discontinued devices, however, one or both may not be possible for all devices. Please contact your local Central Semiconductor sales representative for LTB opportunities/additional information.

CCC785 REV 002