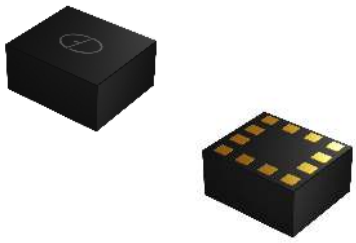
GSDA213
Three-Axis Digital Accelerometer

Features

- Low Profile and Small Footprint
- Selectable Full-scale Measurement Range
- Wide Data Output Range
- Digital I^2C /SPI Output Interface
- High Resolution
- Low Power Consumption
- Two Programmable Interrupt Generators Operating Independently for Motion Detection
- Factory Programmable Offset and Sensitivity
- RoHS Compliant



PACKAGE: LGA-12
2 x 2 x 1.1 mm
(LxWxH max value in mm)

Applications

- User Interface for Mobile and PMP
- Display Orientation
- Gesture Recognition
- Active/Inactive Monitoring
- Free-fall Detection
- Double/Click Recognition
- Power Management
- Vibration Monitoring
- Inclination and Tilt Sensing
- Pedometer

Key Specifications

- LGA-12 Package 2x2x1.1mm
- User Selectable Range ±2g, ±4g, ±8g, ±16g
- Data Output Rate from 1Hz to 1K Hz
- Supply Voltage 1.62V to 3.6V
- Digital Resolution 14-bit
- Operation Temperature Range -40°C to +85°C

Description

The GSDA213 is a capacitive three-axis linear accelerometer specifically designed to meet the requirements for low-power consumer electronics. Packaged in 2x2x1.1mm land grid array (LGA), the device has an outstanding operating temperature range of -40°C to +85°C. Utilizing state of the art techniques and process, GSDA213 sensor element is fabricated by single crystal silicon with DRIE process and is protected by hermetically sealed silicon cap. The device features full-scale measurement range of ±2g, ±4g, ±8g, ±16g, high resolution of 14-bit and a wide range of data output rate while embedding signal condition, temperature compensation, and motion detection. Power-down mode, two independent interrupts, digital interface of I^2C and SPI offer design engineers most flexibility to configure desired patterns and functionalities.

Mechanical Characteristics

(V_{DD}=2.5V, T = 25°C unless otherwise noted)

Symbol	Parameter	Test conditions	Min	Type	Max	Unit
FS	Measurement Range	FS bit set to 00		±2		g
		FS bit set to 01		±4		g
		FS bit set to 10		±8		g
		FS bit set to 11		±16		g
So	Sensitivity	FS bit set to 00		4096		LSB/g
		FS bit set to 01		2048		LSB/g
		FS bit set to 10		1024		LSB/g
		FS bit set to 11		512		LSB/g
TCSo	Sensitivity Change vs. Temperature	FS bit set to 00		0.01		%/°C
Tyoff	Typical Zero-g Level Offset Accuracy After SMT			150		mg
Tcoff	Zero-g Level Change vs. Temperature	Max delta from 25°C		±1		mg/°C
An	Acceleration Noise Density	FS bit set to 00, Normal Mode		150	200	ug/sqrt(Hz)
Vst	Self-Test Output Change	X: FS bit set to 00		400		mg
		Y: FS bit set to 00		400		mg
		Z: FS bit set to 00		400		mg
Top	Operation Temperature Range		-40		85	°C

Note:

1. The product is factory calibrated at 2.5 V. The operational power supply range is from 1.62V to 3.6 V.

Electrical Characteristics

(V_{DD} =2.5V, T = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Unit
V_{DD}	Supply Voltage		1.62	2.5	3.6	V
V_{DD_IO}	I/O Pins Supply Voltage		1.62		3.6	V
I_{DD}	Current Consumption in Normal Mode	Top=25°C, ODR=1kHz		180		uA
I_{DD_IP}	Current Consumption in Low Power Mode	Top=25°C, ODR=62.5Hz, BW=500Hz		32		uA
I_{DD_SM}	Current Consumption in Suspend Mode	Top=25°C		1		uA
V_{IH}	Digital High Level Input Voltage	SPI & I^2C	$0.7*V_{DD\_IO}$			V
V_{IL}	Digital Low Level Input Voltage	SPI & I^2C			$0.3*V_{DD\_IO}$	V
V_{OH}	High Level Output Voltage		$0.9*V_{DD\_IO}$			V
V_{OL}	Low Level Output Voltage				$0.1*V_{DD\_IO}$	V
BW	System Bandwidth		1.95		500	Hz
ODR	Output Data Rate		1		1000	Hz
Wake-up time	twu	From stand-by		1		ms
Start-up time	tsu	From Power-off		3		ms
PSRR	Power Supply Rejection Rate	Top=25°C			20	mg/V

Absolute Maximum Ratings

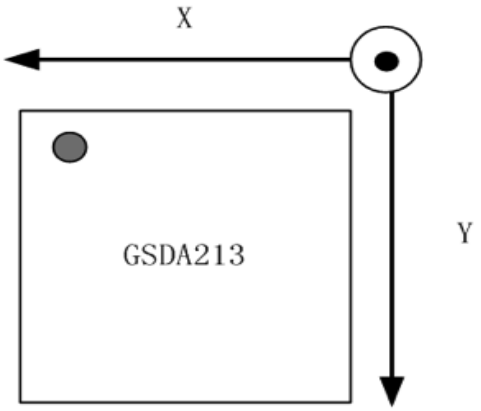
Parameter	Test conditions	Min	Max	Unit
Storage Temperature		-45	125	°C
Supply Voltage	Supply Pins	-0.3	4.25	V
Supply Voltage	Logic Pins	-0.3	Vdd_IO+0.3	V
ESD Rating	HMB, R=1.5k, C=100pF		±2	kV
Mechanical Shock	Duration<200us		10,000	g

Note:

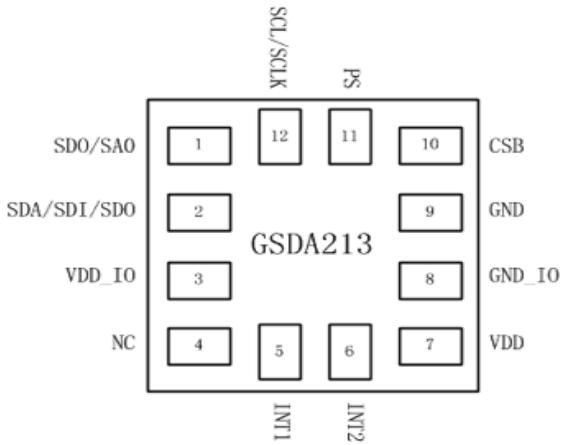
1. Stresses above those listed as “absolute maximum ratings” may cause permanent damage to the device. This is a stress rating only and functional operation of the device under these conditions is not implied. Exposure to maximum rating conditions for extended periods may affect device reliability.
2. Supply voltage on any pin should never exceed 4.25V
3. This is a mechanical shock sensitive device, improper handling can cause permanent damages to the part.

4. This is an ESD sensitive device, improper handling can cause permanent damages to the part.

Pin Configuration



(Top View)

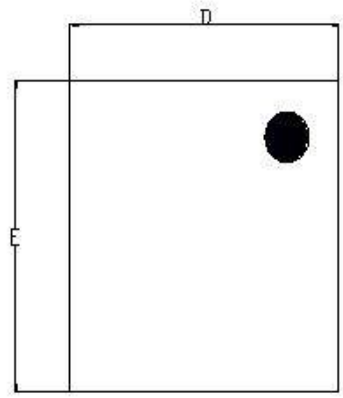


(Bottom View)

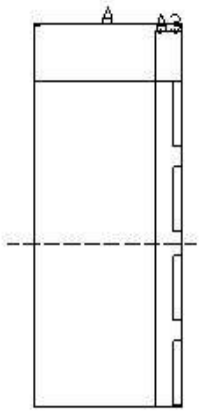
Pin Description

Pin No.	Name	Function Description
1	SDO SA0	SPI Serial Data Output (SDO) I²C Less Significant Bit of the Device Address (SA0)
2	SDA SDI SDO	I²C Serial Data Input/Output (SDA) SPI (4-wire Mode) Serial Data Input (SDI) 3-wire Interface Serial Data Input/Output (SDO)
3	VDDIO	Power Supply for I/O pins
4	NC	Not Connected
5	INT1	Inertial Interrupt 1
6	INT2	Inertial Interrupt 2
7	VDD	Power Supply
8	GNDIO	0 V Supply for I/O pins
9	GND	0 V Supply
10	CS	Chip Select for SPI
11	PS	0: SPI Mode; 1: I²C Mode
12	SCL SPC	I²C Serial Clock (SCL) SPI Serial Port Clock (SCLK)

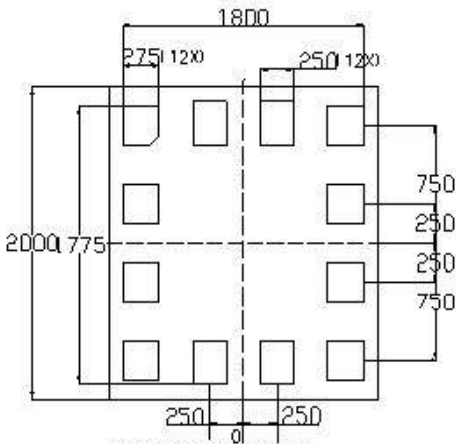
Mechanical Data and Package Dimensions: 12 Pin LGA



(TOP VIEW)



(SIDE VIEW)



(BOTTOM VIEW)

COMMON DIMENSIONS (MM)			
PACKAGE	LGA-12 PIN		
REF.	MIN.	NOM.	MAX.
A	1.00	1.10	1.20
A3	0.20 REF.		
D	1.90	2.00	2.10
E	1.90	2.00	2.10