

R07DS0881EJ0102

Rev.1.02

Nov 28, 2012

# μ**ΡΑ2764**Τ1Α

N-channel MOSFET

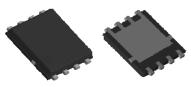
30~V , 130~A ,  $1.10~m\Omega$ 

# Description

The  $\mu$  PA2764T1A is N-channel MOS Field Effect Transistor designed for high current switching application.

## Features

- $V_{DSS} = 30 V (T_A = 25^{\circ}C)$
- Low on-state resistance
  - ---  $R_{DS(on)} = 1.10 \text{ m}\Omega \text{ MAX.} (V_{GS} = 10 \text{ V}, I_D = 46 \text{ A})$
  - ----  $R_{DS(on)}$  = 2.45 m $\Omega$  MAX. ( $V_{GS}$  = 4.5 V,  $I_D$  = 35 A)
- 4.5 V Gate-drive available
- Thin type surface mount package with heat spreader
- Halogen free



8-pin HVSON(6051)

# **Ordering Information**

Pa	art No.	LEAD PLATING	PACKING	Package
μ PA2764T1A-E2	2-AY <sup>*1</sup>	Pure Sn	Tape 3000 p/reel	8-pin HVSON(6051) 0.1 g TYP.

Note: \*1. Pb-free (This product does not contain Pb in external electrode.)

# Absolute Maximum Ratings (T<sub>A</sub> = 25°C)

Item	Symbol	Ratings	Unit
Drain to Source Voltage (V <sub>GS</sub> = 0 V)	V <sub>DSS</sub>	30	V
Gate to Source Voltage (V <sub>DS</sub> = 0 V)	V <sub>GSS</sub>	±20	V
Drain Current (DC) ( $T_c = 25^{\circ}C$ )	I <sub>D(DC)</sub>	±130	A
Drain Current (pulse) *1	I <sub>D(pulse)</sub>	±280	A
Total Power Dissipation *2	P <sub>T1</sub>	1.5	W
Total Power Dissipation (PW = 10 sec) *2	P <sub>T2</sub>	4.6	W
Total Power Dissipation ( $T_c = 25^{\circ}C$ )	P <sub>T3</sub>	83	W
Channel Temperature	T <sub>ch</sub>	150	°C
Storage Temperature	T <sub>stg</sub>	-55 to +150	°C
Single Avalanche Current *3	I <sub>AS</sub>	50	A
Single Avalanche Energy *3	E <sub>AS</sub>	250	mJ

# **Thermal Resistance**

Channel to Ambient Thermal Resistance *2	R <sub>th(ch-A)</sub>	83.3	°C/W
Channel to Case(Drain) Thermal Resistance	R <sub>th(ch-C)</sub>	1.5	°C/W

Notes: \*1. PW  $\leq$  10  $\mu$ s, Duty Cycle  $\leq$  1%

- \*2. Mounted on a glass epoxy board of 25.4 mm x 25.4 mm x 0.8 mmt
- \*3. Starting T<sub>ch</sub> = 25°C, V<sub>DD</sub> = 15 V, R<sub>G</sub> = 25  $\Omega$ , V<sub>GS</sub> = 20  $\rightarrow$  0 V, L = 100  $\mu$ H

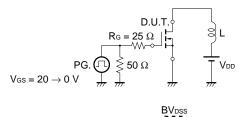


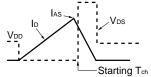
# **Electrical Characteristics (T<sub>A</sub> = 25°C)**

Item	Symbol	MIN.	TYP.	MAX.	Unit	Test Conditions
Zero Gate Voltage Drain Current	I <sub>DSS</sub>			10	μA	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V
Gate Leakage Current	I <sub>GSS</sub>			±100	nA	$V_{GS}$ = ±20 V, $V_{DS}$ = 0 V
Gate Cut-off Voltage	V <sub>GS(off)</sub>	1.0		2.5	V	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA
Forward Transfer Admittance *1	y <sub>fs</sub>	27			S	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 35 A
Drain to Source On-state	R <sub>DS(on)1</sub>		0.90	1.10	mΩ	$V_{GS}$ = 10 V, $I_{D}$ = 46 A
Resistance *1	R <sub>DS(on)2</sub>		1.60	2.45	mΩ	$V_{GS}$ = 4.5 V, $I_{D}$ = 35 A
Input Capacitance	C <sub>iss</sub>		7930		pF	V <sub>DS</sub> = 10 V,
Output Capacitance	Coss		2900		pF	V <sub>GS</sub> = 0 V,
Reverse Transfer Capacitance	C <sub>rss</sub>		2550		pF	f = 1 MHz
Turn-on Delay Time	t <sub>d(on)</sub>		47		ns	$V_{DD}$ = 15 V, $I_D$ = 35 A,
Rise Time	t <sub>r</sub>		160		ns	V <sub>GS</sub> = 10 V,
Turn-off Delay Time	t <sub>d(off)</sub>		310		ns	R <sub>G</sub> = 10 Ω
Fall Time	t <sub>f</sub>		320		ns	
Total Gate Charge	Q <sub>G</sub>		180		nC	V <sub>DD</sub> = 15 V,
Gate to Source Charge	Q <sub>GS</sub>		25		nC	V <sub>GS</sub> = 10 V,
Gate to Drain Charge	Q <sub>GD</sub>		70		nC	I <sub>D</sub> = 70 A
Body Diode Forward Voltage *1	V <sub>F(S-D)</sub>		0.8	1.5	V	I <sub>F</sub> = 46 A, V <sub>GS</sub> = 0 V
Reverse Recovery Time	t <sub>rr</sub>		117		ns	I <sub>F</sub> = 50 A, V <sub>GS</sub> = 0 V,
Reverse Recovery Charge	Q <sub>rr</sub>		157		nC	di/dt = 100 A/ <i>µ</i> s

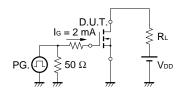
Note: \*1. Pulsed

#### **TEST CIRCUIT 1 AVALANCHE CAPABILITY**

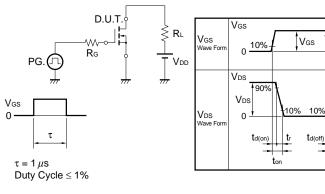




#### TEST CIRCUIT 3 GATE CHARGE



#### **TEST CIRCUIT 2 SWITCHING TIME**





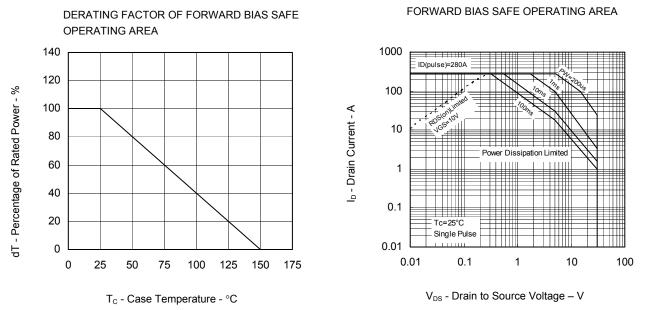
90%

90%

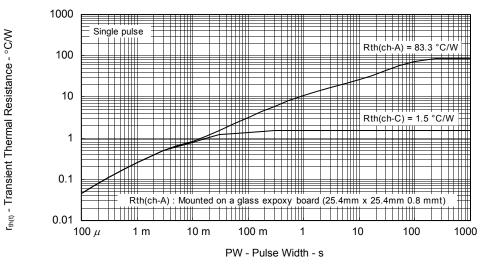
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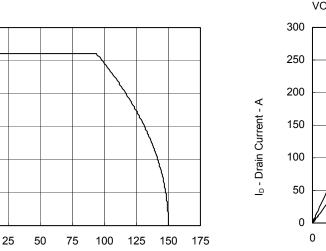
## TYPICAL CHARACTERISTICS ( $T_A = 25^{\circ}C$ )



TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH

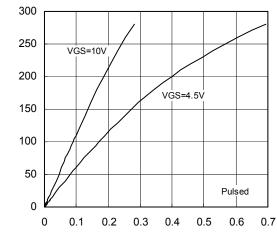


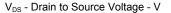
#### DRAIN CURRENT(DC) vs. CASE TEMPERATURE



T<sub>c</sub> - Case Temperature - °C

DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE





150

125

100

75

50

25

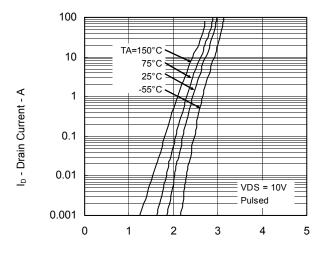
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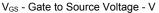
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I<sub>D(DC)</sub> - Drain Current(DC) - A

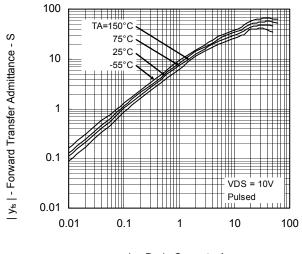


#### FORWARD TRANSFER CHARACTERISTICS

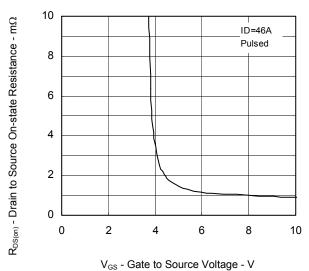




FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT

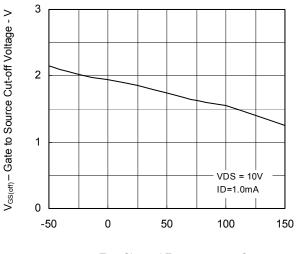


 $I_{\mbox{\scriptsize D}}$  - Drain Current - A



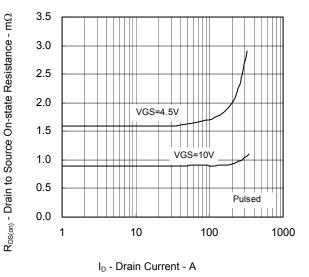
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE

GATE TO SOURCE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE

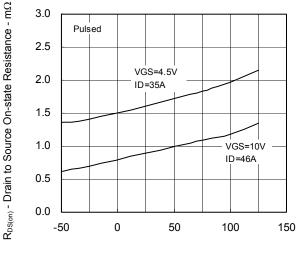


 $T_{ch}$  - Channel Temperature -  $^{\circ}C$ 

DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

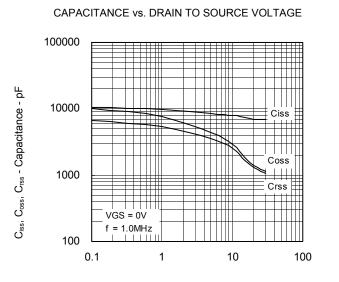






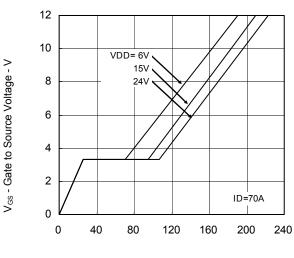
T<sub>ch</sub> - Channel Temperature - °C





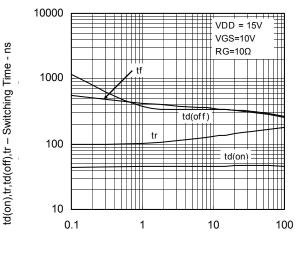
 $V_{\text{DS}}$  - Drain to Source Voltage - V

DYNAMIC INPUT CHARACTERISTICS



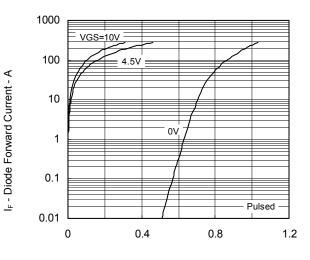
Q<sub>G</sub> - Gate Charge - nC

SWITCHING CHARACTERISTICS



I<sub>D</sub> - Drain Current - A

SOURCE TO DRAIN DIODE FORWARD VOLTAGE

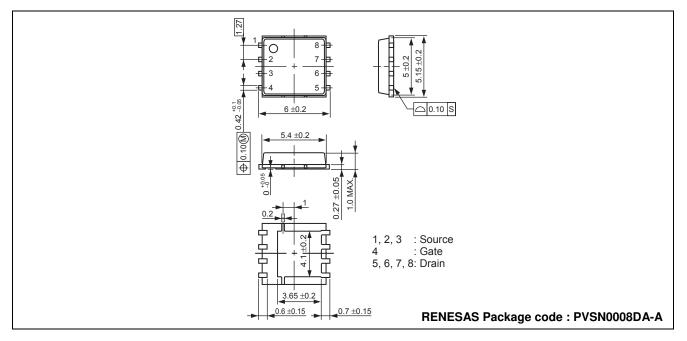


 $V_{\text{F(S-D)}}$  - Source to Drain Voltage - V

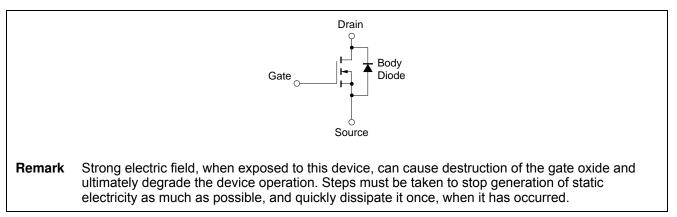


### Package Drawings (Unit: mm)

#### 8pin-HVSON(6051)



# **Equivalent Circuit**





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