TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

2SK3403

Switching Regulator Applications

- Low drain-source ON-resistance: $R_{DS (ON)} = 0.29 \Omega$ (typ.)
- High forward transfer admittance: |Y_{fs}| = 5.8 S (typ.)
- Low leakage current: $I_{DSS} = 100 \mu A (max) (V_{DS} = 450 V)$
- Enhancement mode: V_{th} = 3.0 to 5.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics			Symbol	Rating	Unit	
Drain-source voltage			V_{DSS}	450	(y)	
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)			V_{DGR}	450	(
Gate-source voltage			V_{GSS}	±30	v	
Drain current	DC	(Note 1)	ΙD	13	A	
	Pulse	(Note 1)	I_{DP}	52	> A	
Drain power dissipation (Tc = 25°C)			P_{D}	100	W	
Single pulse avalanche energy (Note 2)			E _{AS}	350	mJ	
Avalanche current			IAR	13	A	
Repetitive avalanche energy (Note 3)			EAR)) 10	mJ	
Channel temperature			Ten	150	√ °C	
Storage temperature range			(T _{stg}))	-55 to 150		

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions") "Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

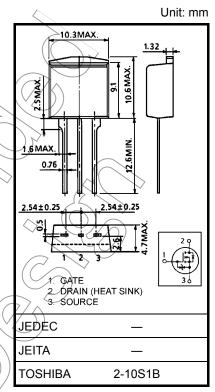
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	1.25	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	83.3	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

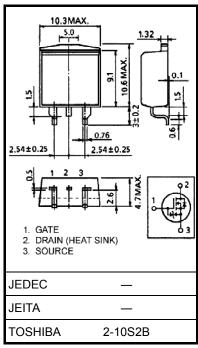
Note 2: $V_{DD} = 90 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), L = 3.46 mH, $R_G = 25 \Omega$,

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.



Weight: 1.5 g (typ.)



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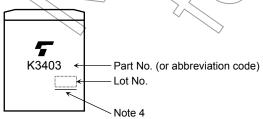
Electrical Characteristics (Tc = 25°C)

Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit		
Gate leakage cur	rent	I _{GSS}	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μΑ		
Gate-source brea	kdown voltage	V (BR) GSS	$I_G=\pm 10~\mu A,~V_{DS}=0~V$	±30	_	_	V		
Drain cut-off curre	ent	I _{DSS}	V _{DS} = 450 V, V _{GS} = 0 V		_	100	μА		
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10$ mA, $V_{GS} = 0$ V	450	_	_	V		
Gate threshold voltage		V _{th}	V _{DS} = 10 V, I _D = 1 mA	3.0) >_	5.0	V		
Drain-source ON-resistance		R _{DS (ON)}	V _{GS} = 10 V, I _D = 6 A	<u> </u>	0.29	0.4	Ω		
Forward transfer admittance		Y _{fs}	V _{DS} = 10 V, I _D = 6 A	3.0	5.8	_	S		
Input capacitance		C _{iss}		_	1600	_			
Reverse transfer capacitance		C _{rss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	· —	17	_	pF		
Output capacitance		Coss		_	220	_			
Switching time	Rise time	t _r	V _{GS} D=6A Output	- (28	<u>></u>			
	Turn-on time	t _{on}	0 V — — — — — — — — — — — — — — — — — —	7	45) _			
	Fall time	t _f	SRL = 33.3 Ω V _{DD} ≈ 200 V	7	10	_	ns		
	Turn-off time	t _{off}	Duty ≤ 1%, t _W = 10 μs) —	56	_			
Total gate charge		Qg		_	34	_			
Gate-source charge		Q _{g\$}	$V_{DD} \approx 360 \text{ V}, V_{GS} = 10 \text{ V}, V_{D} = 13 \text{ A}$		19		nC		
Gate-drain charge		Qgd			15				

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}		_	_	13	Α
Pulse drain reverse current (Note 1)	IDRP	_	_	_	52	Α
Forward voltage (diode)	VDSF	I _{DR} = 13 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	1 _{DR} = 13 A, V _{GS} = 0 V,	_	300	_	ns
Reverse recovery charge	Qrr	dI _{DR} /dt = 100 A/μs	_	3.4	_	μС

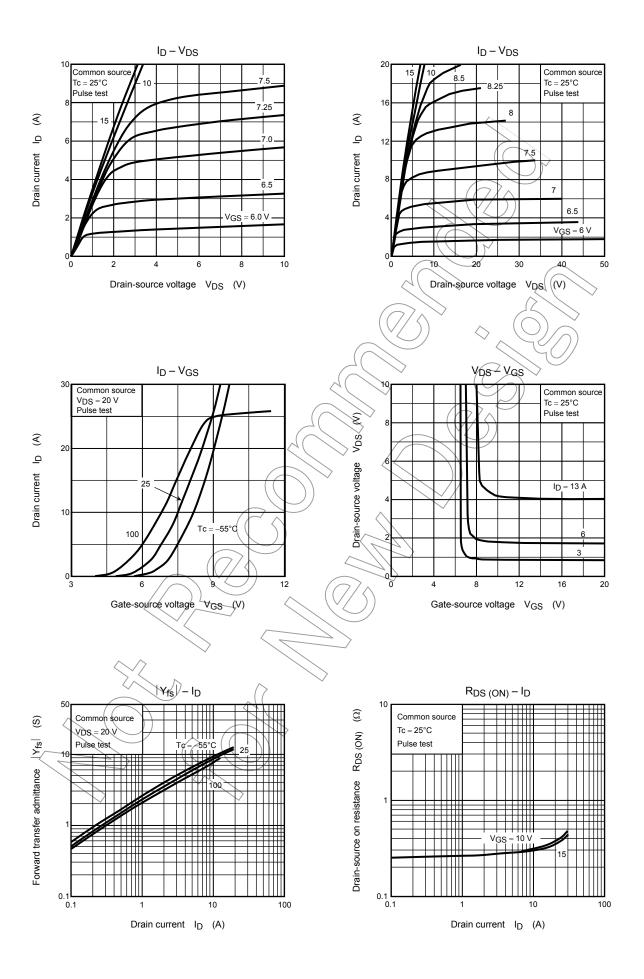


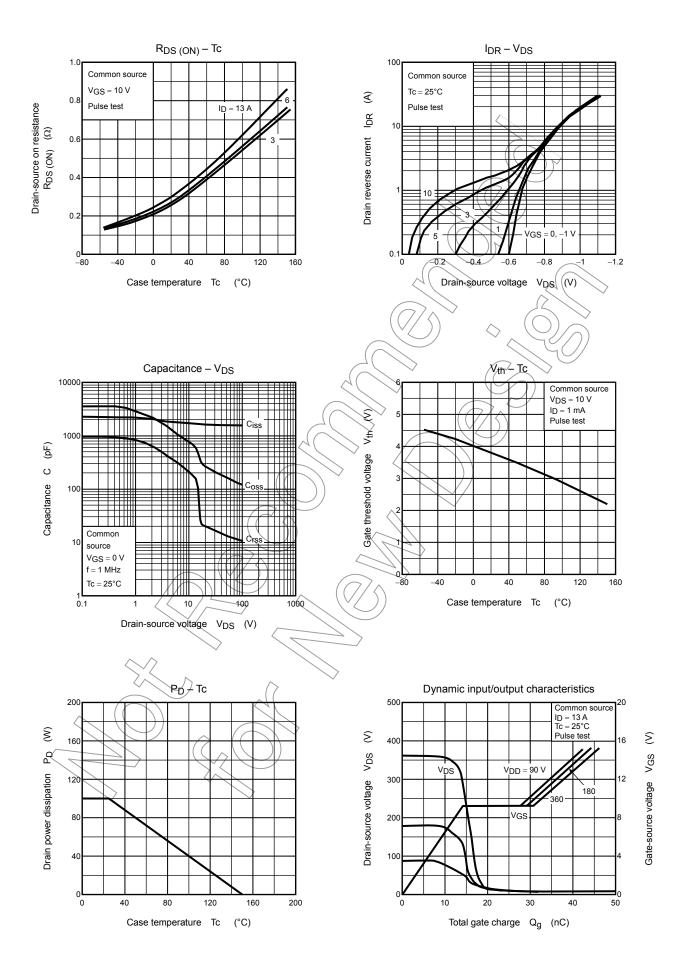


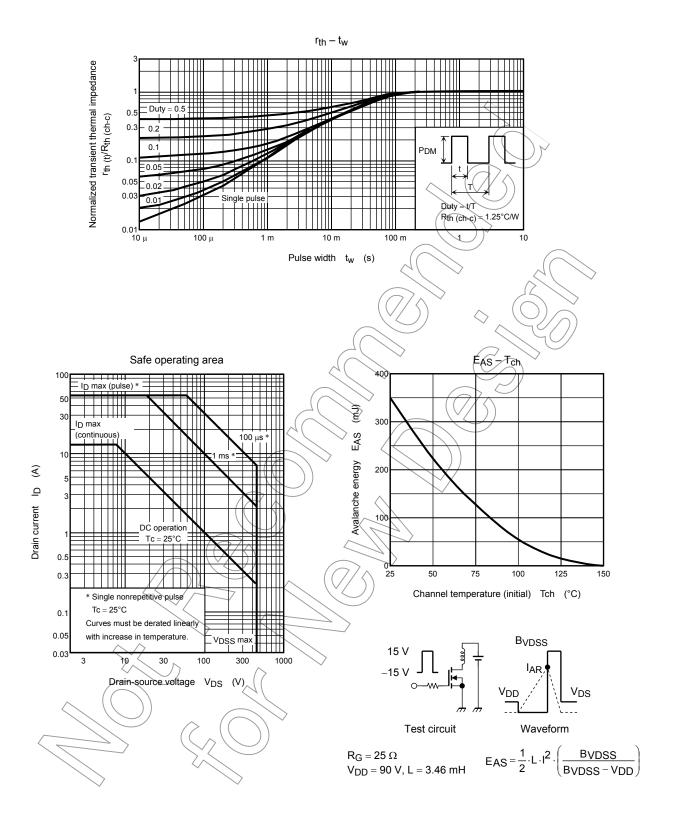
Note 4: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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