

N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{SSS}	Rss(on) max	I _S T _A = +25°C
12V	$6.5 \text{m}\Omega$ @ $V_{GS} = 4.5 \text{V}$	16.6A
12 V	$11.4 \text{m}\Omega$ @ $V_{GS} = 2.5 \text{V}$	12.1A

Features

- CSP with Footprint 2.70mm × 1.81mm
- Height = 0.21mm for Low Profile
- ESD Protection of Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Description

This new generation MOSFET is designed to minimize the on-state resistance (R_{SS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- **Battery Management**
- Load Switch
- **Battery Protection**

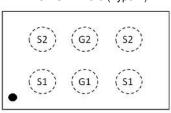
Mechanical Data

- Case: X3-DSN2718-6 (Type B)
- Terminal Connections: See Diagram Below
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu. Solderable per MIL-STD-202, Method 208 **e4**

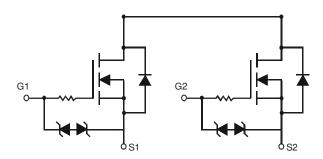
X3-DSN2718-6 (Type B)



ESD PROTECTED



Top View



Equivalent Circuit

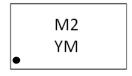
Ordering Information (Note 4)

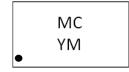
Ī	Part Number	Case	Packaging
	DMN16M9UCA6-7	X3-DSN2718-6 (Type B)	3000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information





M2 / MC = Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: E = 2017) M or \overline{M} = Month (ex: 9 = September)

Date Code Key

Year	201	5	2016		2017	20	18	2019		2020	2	2021
Code	С		D		Е		F	G		Н		1
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



$\hline \textbf{Maximum Ratings} \ (@T_A = +25^{\circ}C, \, \text{unless otherwise specified.})$

Characteristic	Symbol	Value	Unit		
Source-Source Voltage	V _{SSS}	12	V		
Gate-Source Voltage			V _{GSS}	±12	V
Continuous Source Current (Note 5) V _{GS} = 4.5V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	Is	16.6 13.2	Α
Continuous Source Current (Note 5) V _{GS} = 2.5V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	Is	12.1 9.7	А
Pulsed Source Current (Note 6)	•	•	I _{SM}	80	Α

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 7)	P_{D}	1.0	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 7)	$R_{ heta JA}$	124.6	°C/W
Power Dissipation (Note 5)	P _D	2.4	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 5)	$R_{\theta JA}$	51.5	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

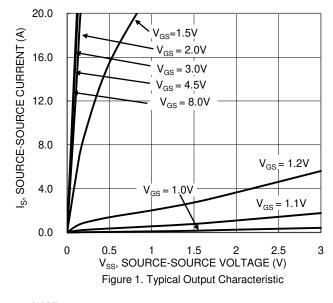
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 8)								
Source-Source Breakdown Voltage	BV _{SSS}	12	-	-	V	$V_{GS} = 0V$, $I_S = 1mA$		
Zero Gate Voltage Drain Current T _J = +25°C	I _{SSS}	-	-	1	μA	$V_{SS} = 10V, V_{GS} = 0V$		
Gate-Source Leakage	I_{GSS}	-	-	±10	μA	$V_{GS} = \pm 8V, V_{SS} = 0V$		
ON CHARACTERISTICS (Note 8)								
Gate Threshold Voltage	$V_{GS(TH)}$	0.5	-	1.3	V	V _{SS} = 6V, I _S = 1mA		
		2.3	5.0	6.5		$V_{GS} = 4.5V, I_S = 3A$		
		2.5	5.2	6.8		$V_{GS} = 4.0V, I_S = 3A$		
Static Source-Source On-Resistance	R _{SS(ON)}	2.6	5.3	6.9	mΩ	$V_{GS} = 3.8V, I_S = 3A$		
		2.8	5.5	8.8		$V_{GS} = 3.1V, I_S = 3A$		
		3.0	6.0	11.4		$V_{GS} = 2.5V, I_S = 3A$		
Diode Forward Voltage	V _{SS}	-	0.7	1.2	V	$V_{GS} = 0V, I_{S} = 3A$		
DYNAMIC CHARACTERISTICS (Note 9)								
Input Capacitance	C _{iss}	-	2360	-		.,		
Output Capacitance	Coss	-	666	-	pF	$V_{SS} = 6V, V_{GS} = 0V,$ f = 1.0MHz		
Reverse Transfer Capacitance	C_{rss}	-	325	-		I = 1.0IVIHZ		
Total Gate Charge	Q_g	-	35.2	-				
Gate-Source Charge	Q _{gs}	-	7.0	-	nC	$V_{SS} = 6V, V_{GS} = 4.5V,$		
Gate-Drain Charge	Q_{gd}	-	8.3	-	IIC	I _S = 18A		
Gate Charge at V _{TH}	Q _{g(TH)}	-	4.2	-				
Turn-On Delay Time	t _{D(ON)}	-	615	-				
Turn-On Rise Time	t _R	-	1447	-		$V_{SS} = 6V, V_{GS} = 4.5V,$		
Turn-Off Delay Time	t _{D(OFF)}	-	2736	-	ns	$I_S = 3A$		
Turn-Off Fall Time	t _F	-	3812	-				

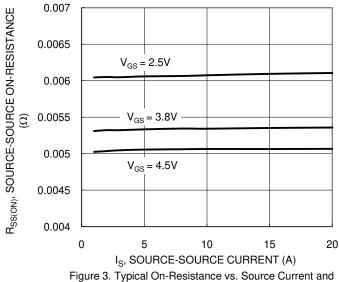
Notes:

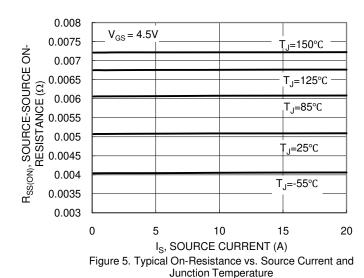
- Device mounted on FR-4 material with 1inch² (6.45cm²), 2oz. (0.071mm thick) Cu.
 Repetitive rating, pulse width limited by junction temperature.
 Device mounted on FR-4 PCB with minimum recommended pad layout, single sided.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to production testing.



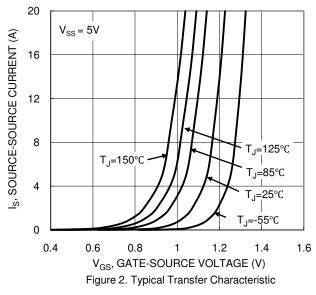


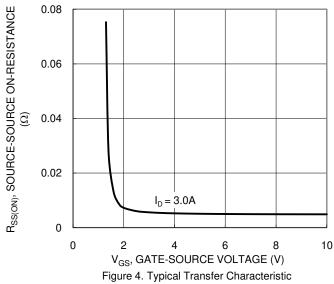






Gate Voltage





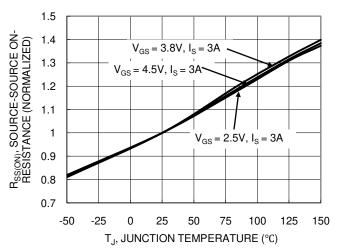


Figure 6. On-Resistance Variation with Junction Temperature





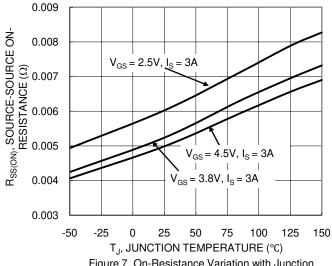
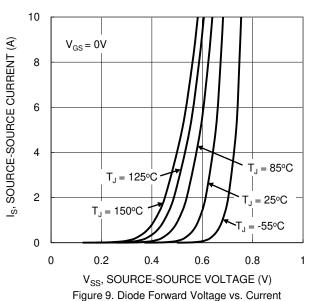


Figure 7. On-Resistance Variation with Junction Temperature



 $V_{SS} = 6V, I_{D} = 18A$ $V_{SS} = 6V, I_{D} = 18A$

20

Qg (nC)
Figure 11. Gate Charge

30

40

50

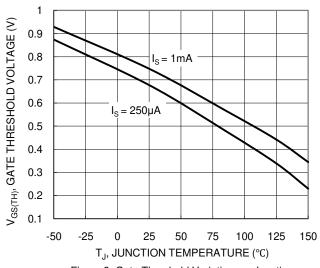
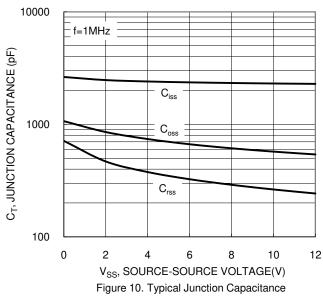
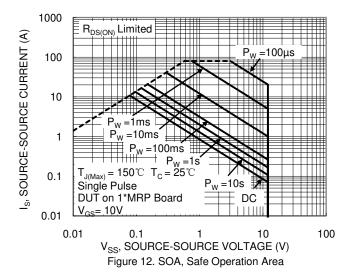


Figure 8. Gate Threshold Variation vs. Junction Temperature





0

10

8

7

6

60



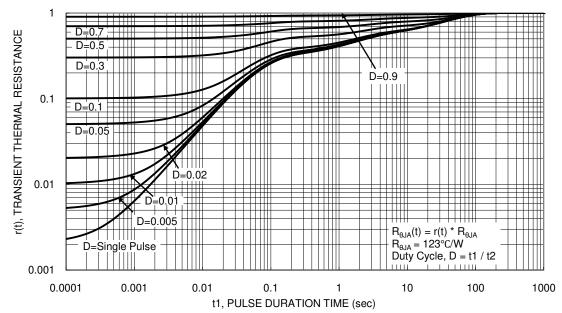


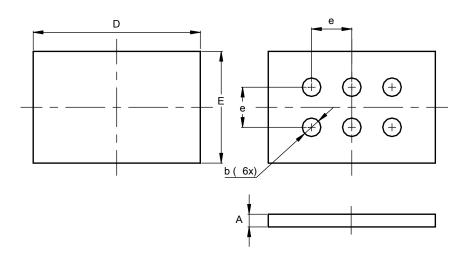
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

X3-DSN2718-6 (Type B)

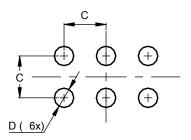


X3-DSN2718-6 (Type B)								
Dim	Dim Min Max Typ							
Α	0.15	0.27	0.21					
b	0.27	0.33	0.30					
D	2.64	2.76	2.70					
Е	E 1.75 1.87 1.81							
e 0.62 0.68 0.65								
All Dimensions in mm								

Suggested Pad Layout

 $Please \ see \ http://www.diodes.com/package-outlines.html \ for \ the \ latest \ version.$

X3-DSN2718-6 (Type B)



Dimensions	Value (in mm)		
С	0.65		
D	0.30		



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