

nRF51422 Evaluation Kit (nRF51422-EK)

The evaluation kits gives you the lowest cost entry point to development with the nRF51422.

The kit operates stand alone, and is based around a small module with headers for all IO pins, 2 buttons, 2 LEDs as well as a built in Segger on board programming and debugging over USB solution. This board is accompanied by an USB dongle with identical except for access to I/O pins. This board can act as wireless peer device or it can act as a very small Software development platform in itself. The kit also contains 5 IC samples. The evaluation kit is supported by the ANT stack offered as a Soft Device as well as (limited) library and code example support in the nRF51 Software Development kit supporting all nRF51 series devices.

Please note that programming and debugging functionality on the evaluation kit is limited to the devices in the kit itself. To be able to program and debug on your own hardware, you will need a standalone programmer/ debugger (JLink lite or JLink Pro) which are included in the nRF51422 Development Kit as well as available for purchase on www.segger.com

For more details and product related downloads, go to the [nRF51422 Evaluation Kit](#) page.



Description

The nRF51422 Evaluation kit is a stand-alone platform for evaluation and initial prototyping of ANT[®]-based designs with the nRF51422 SoC. The kit enables full access to all device pins and peripherals and incorporates a coin-cell battery holder compartment for portability thus enabling in-situ evaluation and test. Each board has full Segger J-Link debug capability. When used in conjunction with the nRF514 SDK a range of software examples can be used with the evaluation kit.

Features

- Evaluation and initial prototyping solution for nRF51422 SoC
- Independent coin cell operation
- Full GPIO accessibility
- Support for Segger J-link hardware debug
- nRF514 SDK examples

Content

- Compact nRF51422 board with PCB antenna
- nRFready ANT USB Dongle (nRF6910)