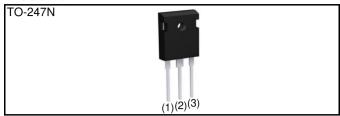


SCT3030AL

N-channel SiC power MOSFET

V_{DSS}	650V
R _{DS(on)} (Typ.)	30mΩ
I _D *1	70A
P_{D}	262W

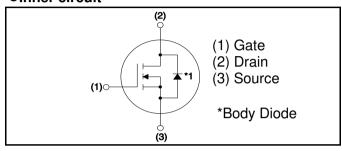
Outline



Features

- 1) Low on-resistance
- 2) Fast switching speed
- 3) Fast reverse recovery
- 4) Easy to parallel
- 5) Simple to drive
- 6) Pb-free lead plating; RoHS compliant

●Inner circuit



Application

- · Solar inverters
- DC/DC converters
- · Switch mode power supplies
- · Induction heating
- Motor drives

Packaging specifications

	Packing	Tube
	Reel size (mm)	-
Typo	Tape width (mm)	-
Type	Basic ordering unit (pcs)	30
	Taping code	C11
	Marking	SCT3030AL

● Absolute maximum ratings (T_{vj} = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Drain - Source Voltage		V_{DSS}	650	V
Continuous Drain augrent	$T_c = 25^{\circ}C$	I _D *1	70	Α
Continuous Drain current	T _c = 100°C	I _D *1	49	Α
Pulsed Drain current (T _c = 25°C)		I _{D,pulse} *2 175		Α
Gate - Source voltage (DC)		V_{GSS}	-4 to +22	V
Gate - Source surge voltage (t _{surge} < 300nsec)		$V_{\rm GSS_surge}^{*3}$	-4 to +26	V
Recommended drive voltage		$V_{\mathrm{GS_op}}^{}^{*4}}$	0 / +18	V
Virtual Junction temperature		T _{vj}	175	°C
Range of storage temperature		T_{stg}	-55 to +175	°C

ullet Electrical characteristics ($T_{vj} = 25^{\circ}C$ unless otherwise specified)

Dorometer	Cumbal	Conditions	Values			Unit
Parameter	Symbol		Min.	Тур.	Max.	Offit
		$V_{GS} = 0V$, $I_D = 1mA$				
Drain - Source breakdown voltage	$V_{(BR)DSS}$	$T_{vj} = 25^{\circ}C$	650	-	-	V
ronago		$T_{vj} = -55^{\circ}C$	650	-	-	
		$V_{GS} = 0V, V_{DS} = 650V$				
Zero Gate voltage Drain current	I _{DSS}	$T_{vj} = 25^{\circ}C$	-	1	10	μΑ
Drain Garrein		$T_{vj} = 150$ °C	-	2	-	
Gate - Source leakage current	I _{GSS+}	$V_{GS} = +22V$, $V_{DS} = 0V$	-	-	100	nA
Gate - Source leakage current	I _{GSS-}	$V_{GS} = -4V$, $V_{DS} = 0V$	-	-	-100	nA
Gate threshold voltage	V _{GS (th)}	$V_{DS} = 10V, I_{D} = 13.3mA$	2.7	ı	5.6	V
		$V_{GS} = 18V, I_D = 27A$				
Static Drain - Source on - state resistance	R _{DS(on)} *5	$T_{vj} = 25^{\circ}C$	-	30	39	mΩ
on state resistance		$T_{vj} = 150$ °C	-	43	-	
Gate input resistance	R_{G}	f = 1MHz, open drain	-	7	-	Ω

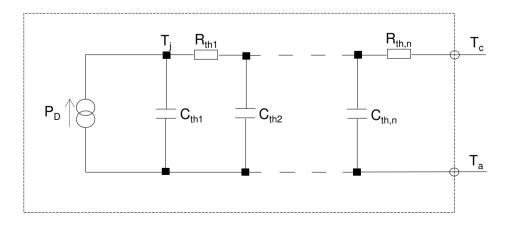
Thermal resistance

Parameter	Symbol	Values			Unit
		Min.	Тур.	Max.	Offic
Thermal resistance, junction - case	R_{thJC}	-	0.44	0.57	K/W

● Typical Transient Thermal Characteristics

Symbol	Value	Unit
R _{th1}	2.56E-02	
R _{th2}	1.95E-01	K/W
R _{th3}	2.20E-01	

Symbol	Value	Unit
C _{th1}	1.39E-03	
C _{th2}	1.00E-02	Ws/K
C_{th3}	3.57E-02	



ullet Electrical characteristics ($T_{vj} = 25^{\circ}C$ unless otherwise specified)

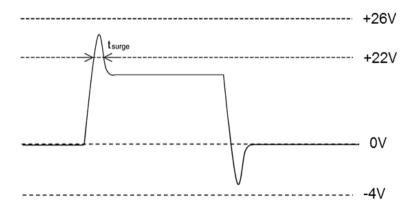
Parameter	Cymbal	Conditions		Values		
i didilictei	Symbol	Conditions	Min.	Тур.	Max.	Unit
Transconductance	g fs *5	$V_{DS} = 10V, I_{D} = 27A$	-	9.4	-	S
Input capacitance	C _{iss}	$V_{GS} = 0V$	-	1526	-	
Output capacitance	C _{oss}	V _{DS} = 500V	-	89	-	pF
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	42	-	
Effective output capacitance, energy related	C _{o(er)}	$V_{GS} = 0V$ $V_{DS} = 0V \text{ to } 300V$	-	230	-	pF
Total Gate charge	Qg *5	$V_{DS} = 300V$ $I_{D} = 27A$	-	104	-	
Gate - Source charge	Q _{gs} *5	$V_{GS} = 18V$	-	19	-	nC
Gate - Drain charge	Q _{gd} *5	See Fig. 1-1.	-	55	-	
Turn - on delay time	t _{d(on)} *5	$V_{DS} = 300V$ $I_{D} = 18A$	ī	22	-	
Rise time	t _r *5	$V_{GS} = 0V/+18V$	ı	41	-	ns
Turn - off delay time	t _{d(off)} *5	$R_G = 0\Omega$ $R_L = 17\Omega$	ı	48	-	115
Fall time	t _f *5	See Fig. 1-1, 1-2.	ı	27	-	
Turn - on switching loss	E _{on} *5	$V_{DS} = 300V$ $V_{GS} = 0V/18V$, $I_{D} = 27A$ $R_{G} = 0\Omega$, $L = 250\mu H$	-	168	-	.,, 1
Turn - off switching loss	E _{off} *5	E_{on} includes diode reverse recovery $L_{\sigma} = 50$ nH, $C_{\sigma} = 200$ pF See Fig. 2-1, 2-2.	-	112	-	μJ

ullet Body diode electrical characteristics (Source-Drain) ($T_{vj} = 25^{\circ}$ C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
raiailletei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Body diode continuous, forward current	I _S *1	T _c = 25°C	-	-	70	Α
Body diode direct current, pulsed	I _{SM} *2	1 _c = 23 0	ı	i	175	Α
Forward voltage	V _{SD} *5	$V_{GS} = 0V, I_{S} = 27A$	ı	3.2	ı	V
Reverse recovery time	t _{rr} *5	$I_F = 27A$ $V_B = 300V$	-	26	-	ns
Reverse recovery charge	Q _{rr} *5	di/dt = 1100A/µs	-	130	-	nC
Peak reverse recovery current	l _{rrm} *5	$L_{\sigma} = 50$ nH, $C_{\sigma} = 200$ pF See Fig. 3-1, 3-2.	-	10	-	Α

^{*1} Limited by maximum T_{vi} and for Max. R_{thJC} .

*3 Example of acceptable V_{GS} waveform



*5 Pulsed

^{*2} PW \leq 10 μ s, Duty cycle \leq 1%

 $^{^{\}star}4$ Please be advised not to use SiC-MOSFETs with V_{GS} below 13V as doing so may cause thermal runaway.

Fig.1 Power Dissipation Derating Curve

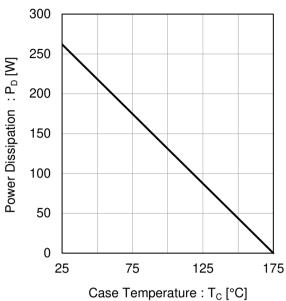


Fig.2 Maximum Safe Operating Area

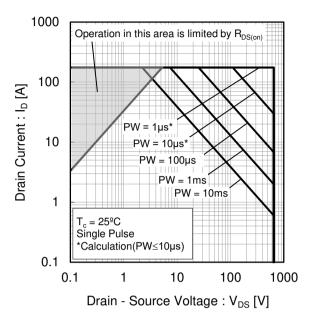
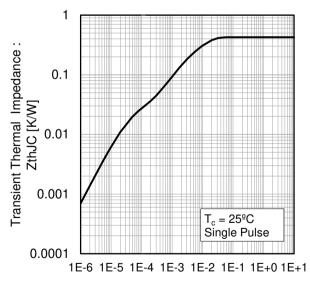


Fig.3 Typical Transient Thermal Resistance vs. Pulse Width



Pulse Width: PW [s]

Fig.4 Typical Output Characteristics(I)

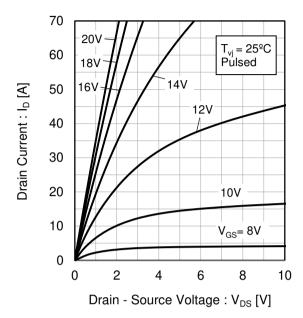


Fig.5 Typical Output Characteristics(II)

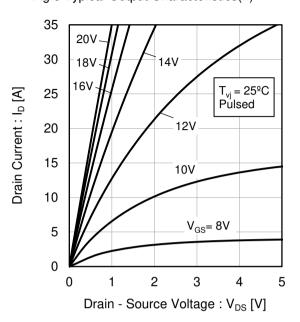
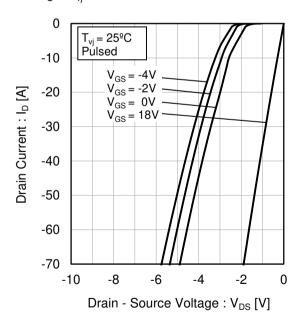
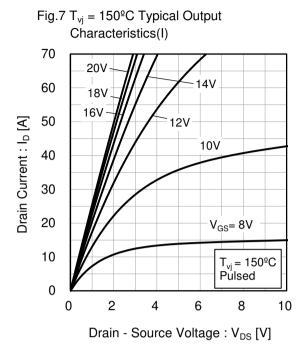
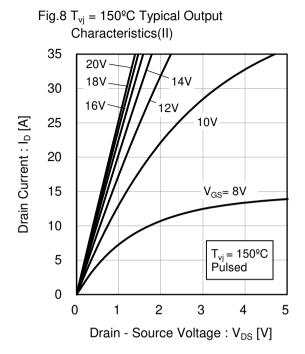


Fig.6 $T_{v_i} = 25^{\circ}C$ 3rd Quadrant Characteristics



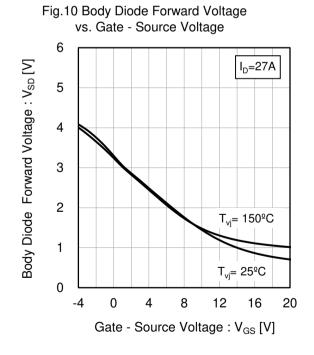




Characteristics 0 $T_{vi} = 150^{\circ}C$ Pulsed -10 $V_{GS} = -4V$ $V_{GS} = -2V$ $V_{GS} = 0V$ $V_{GS} = 18V$ -20 Drain Current : I_D [A] -30 -40 -50 -60 -70 -10 -8 -6 -4 -2 0

Drain - Source Voltage : V_{DS} [V]

Fig.9 $T_{vj} = 150^{\circ}C$ 3rd Quadrant



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Fig.11 Typical Transfer Characteristics (I)

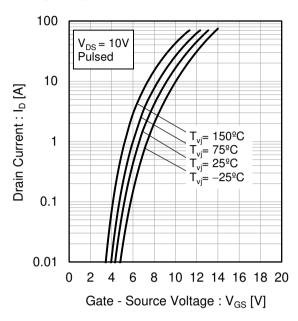


Fig.12 Typical Transfer Characteristics (II)

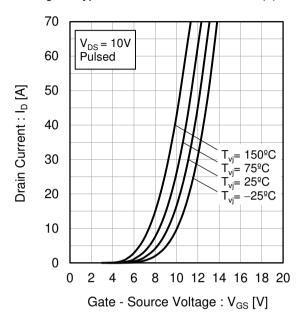


Fig.13 Gate Threshold Voltage vs. Junction Temperature

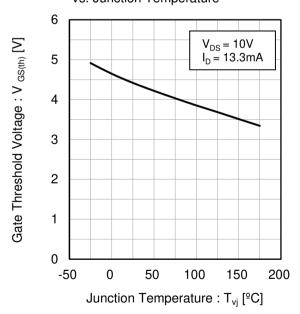
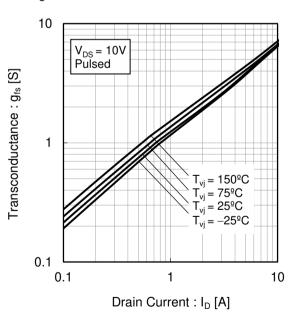
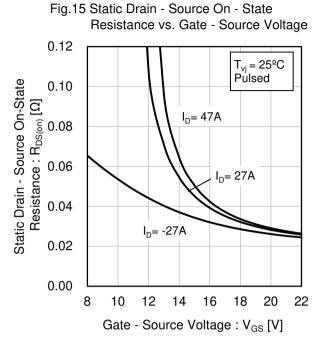


Fig.14 Transconductance vs. Drain Current





Resistance vs. Junction Temperature 0.06 $V_{GS} = 18V$ Pulsed I_D= 47A $I_D = 27A$ I_D= -27A 0.00 0 100 -50 50 150 200 Junction Temperature : T_{vi} [°C]

Fig.16 Static Drain - Source On - State

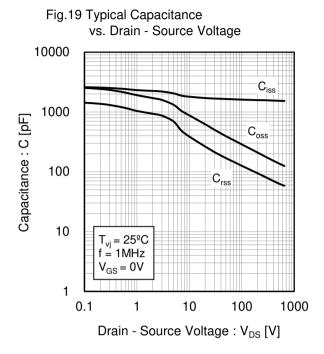
Fig. 17 Static Drain - Source On - State Resistance vs. Drain Current

0.1 $T_{vj} = 150^{\circ}\text{C}$ $T_{vj} = 125^{\circ}\text{C}$ $T_{vj} = 75^{\circ}\text{C}$ $T_{vj} = 25^{\circ}\text{C}$ $T_{vj} = 25^{\circ}\text{C}$ $T_{vj} = -25^{\circ}\text{C}$ $T_{vj} = -25^{\circ}\text{C}$

Voltage vs. Junction Temperature 1.04 1.03 Normalized Drain - Source Breakdown Voltage 1.02 1.01 1.00 0.99 0.98 0 50 100 200 -50 150 Junction Temperature : T_{vj} [ºC]

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Fig.18 Normalized Drain - Source Breakdown



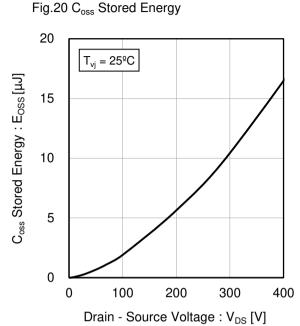
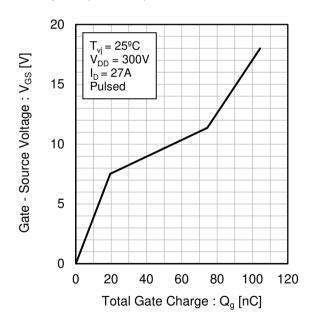
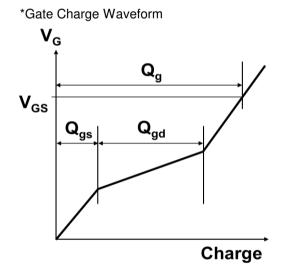


Fig.21 Dynamic Input Characteristics





0.1

Fig.19 Typical Switching Time

1

10

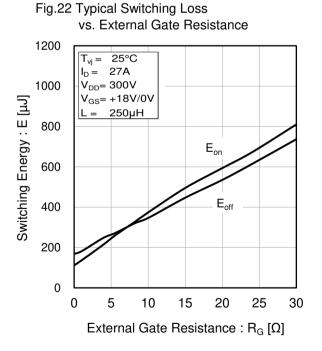
Drain Current: ID [A]

100

vs. Drain - Source Voltage 400 25°C 27A 350 $I_D =$ $V_{GS} = +18V/0V$ $R_G = 0\Omega$ Switching Energy : E [µJ] 300 L = 250µH 250 200 E_{on} 150 100 E_{off} 50 0 200 500 100 300 400 Drain - Source Voltage: V_{DS} [V]

Fig.20 Typical Switching Loss

Fig.21 Typical Switching Loss vs. Drain Current 1200 25°C V_{DD}= 300V 1000 $V_{GS} = +18V/0V$ Switching Energy : E [µJ] $R_G = 0\Omega$ 250µH 800 600 E_{on} 400 $\mathsf{E}_{\mathsf{off}}$ 200 0 0 10 20 40 70 80 Drain Current: ID [A]



Measurement circuits and waveforms

Fig.1-1 Gate Charge and Switching Time Measurement Circuit

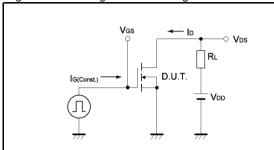


Fig.2-1 Switching Energy Measurement Circuit

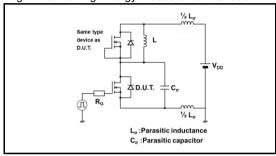


Fig.3-1 Reverse Recovery Time Measurement Circuit

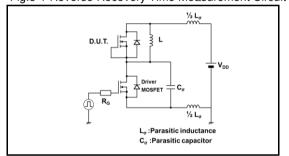


Fig.1-2 Waveforms for Switching Time

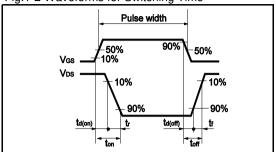


Fig.2-2 Waveforms for Switching Energy Loss

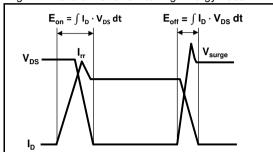
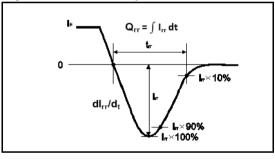
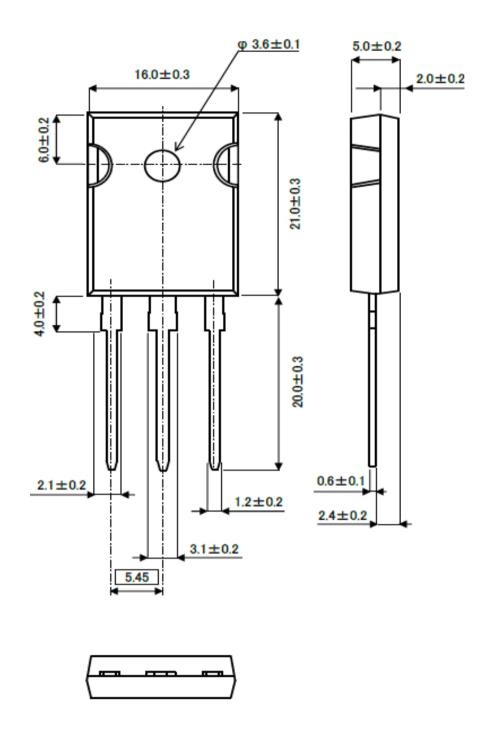


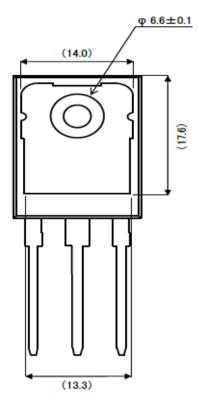
Fig.3-2 Reverse Recovery Waveform



●Package Dimensions

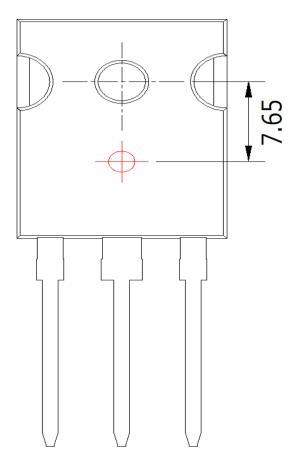


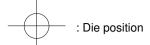
Unit: mm



Unit: mm

●Die Bonding Layout





- •Front view of the packaging.
- •Dimensions are design values.
- ·If the heat sink is to be installed, it should be in contact with the die bonding point.

Unit: mm

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