# 75 $\Omega$ , Differential RF Amplifier 5 - 1218 MHz

### Features

- Single Stage, Differential Amplifier
- 5 V, 290 mA Operation
- 17 dB Flat Gain
- Low Noise
- Low Distortion Performance
- ESD Class 1B for HBM
- Lead-Free SOIC-8EP Plastic Package
- Halogen-Free "Green" Mold Compound
- RoHS\* Compliant

### Description

The MAAM-011240 is high gain, high linearity and low noise differential RF amplifier assembled in a SOIC-8EP plastic package. This amplifier provides 17 dB of flat gain with very low noise figure. The differential push-pull topology provides superior 2nd order intermodulation performance.

The MAAM-011240 provides high gain, low noise and low distortion making it ideally suited for 75  $\Omega$  infrastructure applications.

The MAAM-011240 is fabricated using GaAs pHEMT technology.

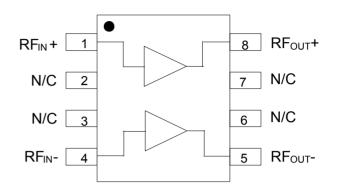
## Ordering Information<sup>1,2</sup>

Part Number	Package		
MAAM-011240	Bulk Packaging		
MAAM-011240-TR1000	1000 Part Reel		
MAAM-011240-TR3000	3000 Part Reel		
MAAM-011240-001SMB	Sample Board, 45 - 1218 MHz		
MAAM-011240-002SMB	Sample Board, 5 - 300 MHz		

1. Reference Application Note M513 for reel size information.

2. All sample boards include 5 loose parts.

## **Functional Schematic**



### **Pin Configuration**

Pin No.	Pin Name	Function		
1	RF <sub>IN</sub> +	RF Input +		
2	N/C	No Connection		
3	N/C	No Connection		
4	RF <sub>IN</sub> -	RF Input -		
5	RF <sub>out</sub> -	RF Output - / V <sub>DD</sub>		
6	N/C	No Connection		
7	N/C	No Connection		
8	RF <sub>OUT</sub> +	RF Output + / V <sub>DD</sub>		
9	Pad <sup>3</sup>	RF and DC Ground		

3. The exposed pad centered on package bottom must be connected to RF and DC ground.

\* Restrictions on Hazardous Substances, European Union Directive 2011/65/EU.

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## 75 Ω, Differential RF Amplifier

### 5 - 1218 MHz

Rev. V1

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#### Electrical Specifications: $T_A = 25^{\circ}C$ , $V_{DD} = 5 V$ , $Z_0 = 75 \Omega$ Performance specified with input/output balun MABA-009210-CT1760

**Test Conditions** Parameter Units Min. Max. Typ. Gain 1218 MHz dB 16.2 17 18.5 Tilt 45 - 1218 MHz dB 0 **Reverse Isolation** 45 - 1218 MHz dB 21 Input Return Loss 45 - 1218 MHz dB 20 \_ **Output Return Loss** 45 - 1218 MHz dB 20 45 MHz 1.7 Noise Figure dB 1218 MHz 2.6 45 - 1218 MHz, tone spacing 6 MHz Output IP2 dBm 63  $P_{OUT}$  per tone = +13 dBm 45 - 1218 MHz, tone spacing 6 MHz **Output IP3** dBm 44  $P_{OUT}$  per tone = +13 dBm P1dB 45 - 1218 MHz dBm 25 79 channels, 0 dB Tilt, 39 dBmV per Composite Triple Beat, CTB dBc -75 channel output, QAM to 1000 MHz 79 channels, 0 dB Tilt, 39 dBmV per Composite Second Order, CSO dBc -77 channel output, QAM to 1000 MHz 62 dBmV output, Single Channel: ACPR<sup>4</sup> 79 MHz dBc -70 1218 MHz -64  $V_{DD} = 5 V$ 290 mΑ 350 IDD

4. Adjacent Channel (750 kHz from channel block edge to 6 MHz from channel block edge), 256 QAM, 5.36 Msym/sec.

## Absolute Maximum Ratings<sup>5,6,7</sup>

Parameter	Absolute Maximum		
Max Input Power	10 dBm		
Operating Voltage	8 V		
Operating Temperature	-40°C to +85°C		
Storage Temperature	-65°C to +150°C		
Junction Temperature <sup>8</sup>	+150°C		

5. Exceeding any one or combination of these limits may cause permanent damage to this device.

- 6. MACOM does not recommend sustained operation near these survivability limits.
- 7. Operating at nominal conditions with T\_J < 150°C will ensure MTTF > 1 x 10<sup>6</sup> hours.
- 8. Junction Temperature (T<sub>J</sub>) = Case Temperature (T<sub>C</sub>) +  $\Theta_{JC}^*(V^*I)$ Typical thermal resistance ( $\Theta_{JC}$ ) = 29°C/W.

a) For  $T_C = 25^{\circ}C$ ,

 $T_J = 67^{\circ}C @ 5 V, 290 mA$ b) For  $T_c = 85^{\circ}C$ .

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### Handling Procedures

Please observe the following precautions to avoid damage:

### **Static Sensitivity**

Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these (HBM) Class 1B devices.

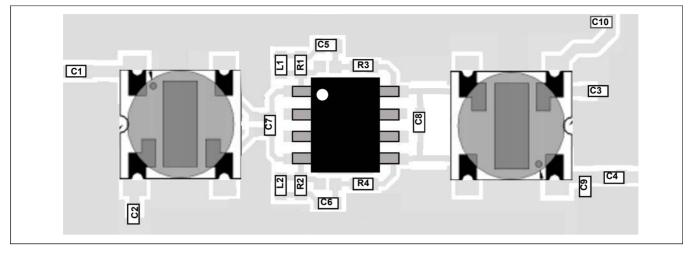
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## MACOM

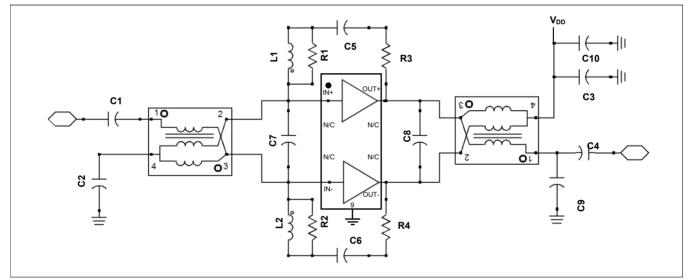
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## **Recommended PCB Layout**



## **Schematic Including Off-Chip Components**



### **Parts List**

Component	Value	Package	Component	Value	Package	
C1, C4	270 pF	0402	L1, L2	33 nH	0402	
C2,C3,C5,C6,C10	10 nF	0402	R1, R2	62 Ω	0402	
C7	0.5 pF	0402	R3, R4	316 Ω	0402	
C8	1.0 pF	0402	T1, T2	1:1 Balun <sup>9</sup>	—	
C9	Do Not Install	0402				

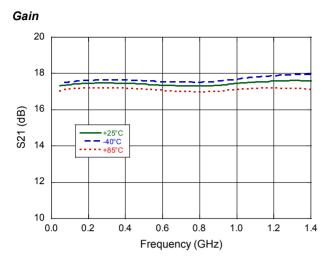
9. MABA-009210-CT1760

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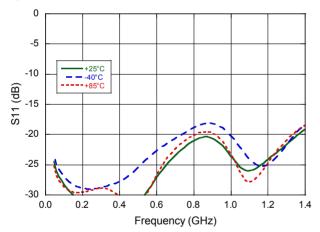
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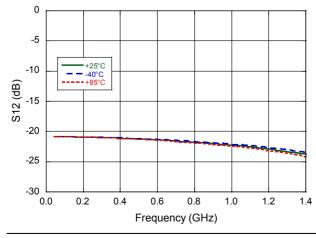
## Typical Performance Curves: V<sub>DD</sub> = 5 V

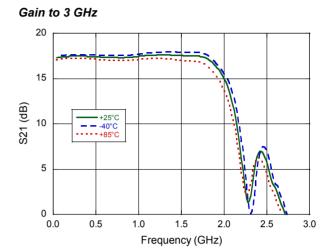


Input Return Loss

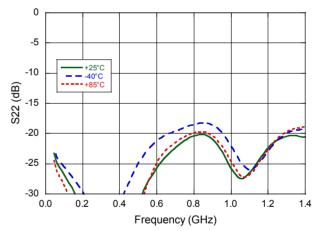


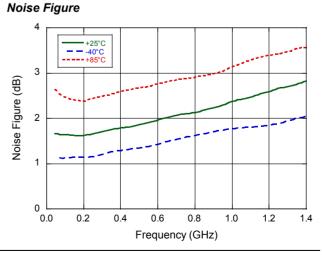
#### **Reverse Isolation**





**Output Return Loss** 





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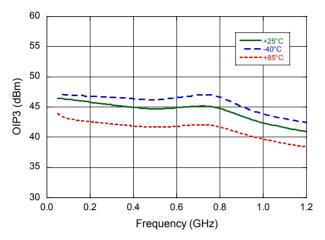
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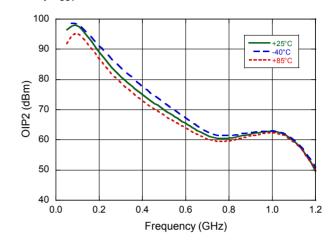


## Typical Performance Curves: V<sub>DD</sub> = 5 V

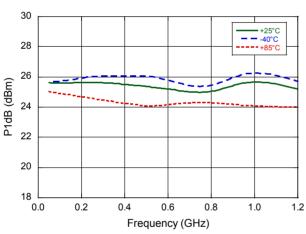
#### OIP3, P<sub>OUT</sub> = +13 dBm/tone



OIP2, P<sub>OUT</sub> = +13 dBm/tone







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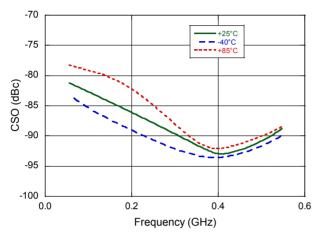


## 75 Ω, Differential RF Amplifier 5 - 1218 MHz

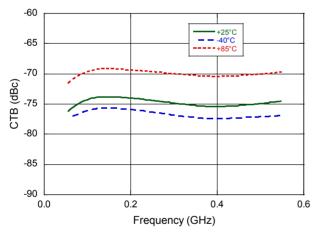
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### Typical Performance Curves: V<sub>DD</sub> = 5 V

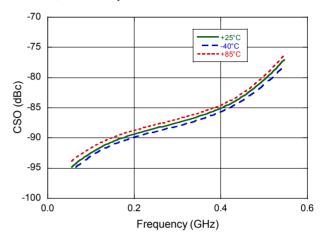
#### CSO Lower, 79 channels + QAM to 1 GHz, 0 dB tilt, 39 dBmV per channel



CTB, 79 channels + QAM to 1 GHz, 0 dB tilt, 39 dBmV per channel



CSO Upper, 79 channels + QAM to 1 GHz, 0 dB tilt, 39 dBmV per channel



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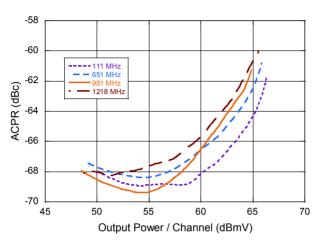


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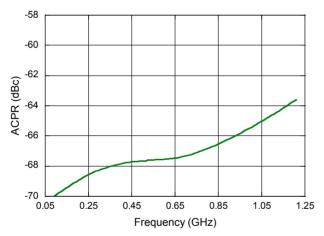
## 75 Ω, Differential RF Amplifier 5 - 1218 MHz

## Typical Performance Curves: $V_{DD} = 5 V$ , Temp = +25°C

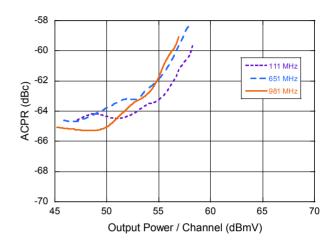
#### ACPR vs. POUT, Single Channel



ACPR vs. Frequency,  $P_{OUT}$  = +62 dBmV, Single Channel



ACPR vs. POUT, 4 Channels

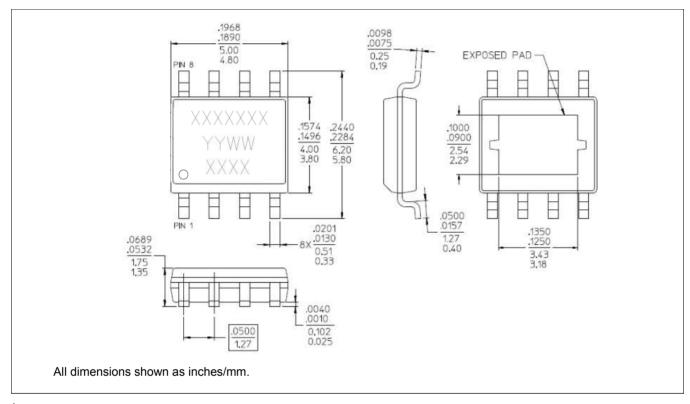


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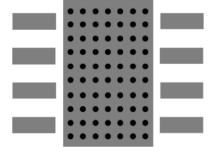
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<sup>†</sup> Reference Application Note S2083 for lead-free solder reflow recommendations. Meets JEDEC moisture sensitivity level 1 requirements. Plating is 100% matte tin over copper.

## **Recommended PCB Land Pattern**

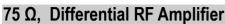


<sup>70</sup> ground vias 0.008 inch finished hole diameter

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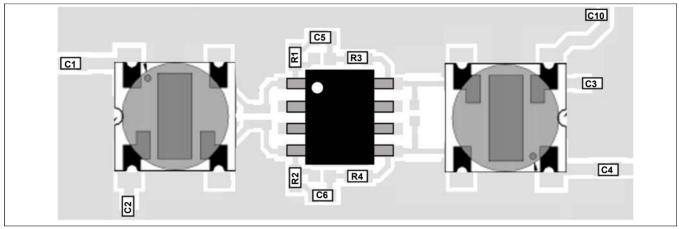
### 5 - 1218 MHz

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### Applications Section: 5 - 300 MHz Application

The MAAM-011240 may be tuned for operation in the 5 - 300 MHz band for CATV reverse path (upstream) applications using an alternate balun and other external tuning components as identified in the table below. The recommended PCB layout and schematic are the same as identified on page 4.

### **Recommended PCB Layout for Upstream**



### Parts List : 5 - 300 MHz Tune

Component	Value	Package	Component	Value	Package	
C1, C2, C4 - C6	10 nF	0402	C3	0.1 µF	0402	
C10	2200 pF	0402	R1, R2	150 Ω	0402	
T1, T2	1:1 Balun <sup>10</sup>		R3, R4	180 Ω	0402	

10. MABA-011085

## Electrical Specifications: 5 - 300 MHz Tune, $T_A = 25^{\circ}C$ , $V_{DD} = 5 V$ , $Z_0 = 75 \Omega$

Parameter	Test Conditions	Units	Min.	Тур.	Max.
Gain	5 - 300 MHz	dB		17	_
Reverse Isolation	5 - 300 MHz	dB		21	_
Input Return Loss	5 - 300 MHz	dB	_	23	—
Output Return Loss	5 - 300 MHz	dB	_	21	_
Noise Figure	5 - 10 MHz 20 - 300 MHz	dB		2.3 2.0	_
Output IP2	5 - 300 MHz, tone spacing 6 MHz P <sub>OUT</sub> per tone = +13 dBm	dBm	_	75	_
Output IP3	5 - 300 MHz, tone spacing 6 MHz P <sub>OUT</sub> per tone = +13 dBm	dBm	_	45	_
P1dB	5 - 300 MHz	dBm		25	_
I <sub>DD</sub>	$V_{DD} = 5 V$	mA	_	290	—
Noise Power Ratio	5 - 85 MHz, 41 MHz Notch, Peak NPR 5 - 204 MHz, 100 MHz Notch, Peak NPR	dB		72 71	_

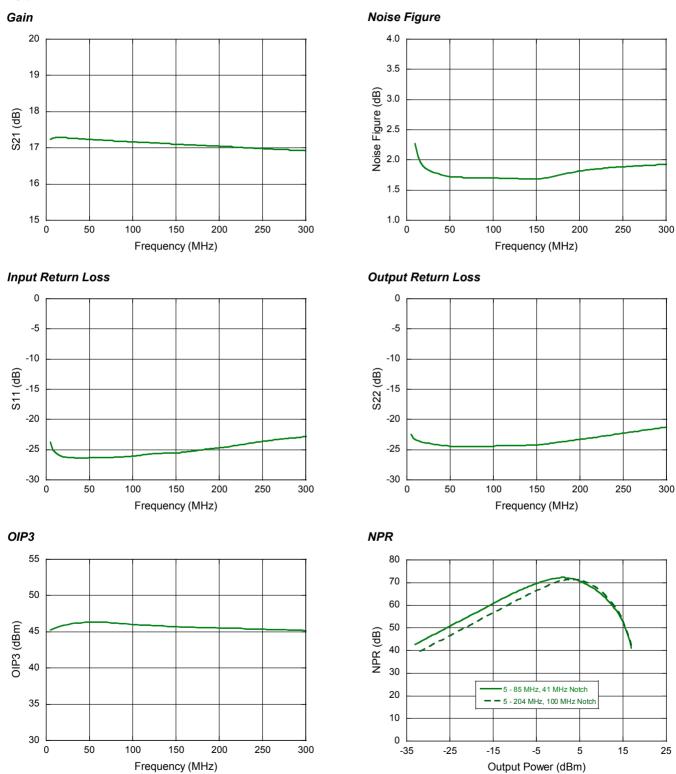
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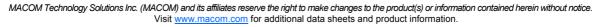


## 75 Ω, Differential RF Amplifier 5 - 1218 MHz

### Typical Performance Curves: 5 - 300 MHz Tune, V<sub>DD</sub> = 5 V, +25°C



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75  $\Omega$ , Differential RF Amplifier 5 - 1218 MHz



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