



P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(ON)} MAX	Package	I _D T _A = +25°C
-30V	$70 \text{m}\Omega @V_{GS} = -10 \text{V}$	SO-8	-3.8A
-30 V	$95m\Omega @V_{GS} = -4.5V$	30-0	-3.2A

Description

This MOSFET has been designed to minimize the on-state resistance $(R_{DS(ON)})$ and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Backlighting
- Power Management Functions
- DC-DC Converters

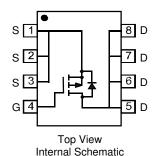
Features

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 standards for High Reliability

Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See diagram
- Terminals: Finish Matte Tin annealed over Copper leadframe.
 Solderable per MIL-STD-202, Method 208 63
- Weight: 0.008 grams (approximate)





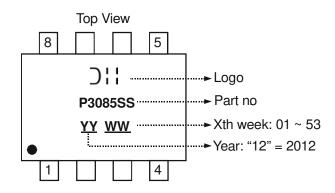
Ordering Information

Part Number	Case	Packaging
DMP3085LSS-13	SO-8	2500/Tape & Reel

Notes:

- $1.\ No\ purposely\ added\ lead.\ Fully\ EU\ Directive\ 2002/95/EC\ (RoHS)\ \&\ 2011/65/EU\ (RoHS\ 2)\ compliant.$
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html

Marking Information





Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V_{DSS}	-30	V
Gate-Source Voltage			V_{GSS}	±20	V
Continuous Dusin Courset (Note C) V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I _D	-3.8 -3	Α
Continuous Drain Current (Note 6) V _{GS} = -10V	t<10s	$T_A = +25$ °C $T_A = +70$ °C	I _D	-5.3 -4.2	Α
Maximum Continuous Body Diode Forward Current (Note 6)			Is	-2.5	Α
Pulsed Drain Current (10µs pulse, duty cycle = 1%)			I _{DM}	20	Α

Thermal Characteristics

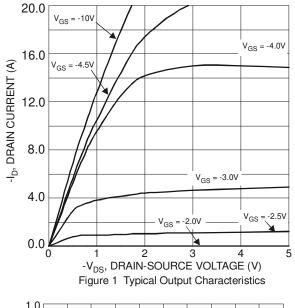
Characteristic	Symbol	Value	Units		
Total Bower Dissipation (Note 5)	$T_A = +25^{\circ}C$	D-	1.3	W	
Total Power Dissipation (Note 5)	$T_A = +70$ °C	P_{D}	0.8	VV	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Reja	96	°C/W	
Thermal Resistance, suriction to Ambient (Note 5)	t<10s	ПθЈА	48		
Total Power Dissipation (Note 6)	$T_A = +25^{\circ}C$	D-	1.6	W	
Total Fower Dissipation (Note o)	$T_A = +70$ °C	P_{D}	1	VV	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{0JA}	78	°C/W	
Thermal Resistance, suriction to Ambient (Note 6)	t<10s	пθЈА	39		
Thermal Resistance, Junction to Case		$R_{ heta JC}$	18		
Operating and Storage Temperature Range		$T_{J_1}T_{STG}$	-55 to 150	°C	

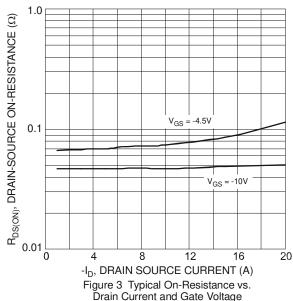
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Cymphol	Min	T.m	Max	Unit	Toot Condition
OFF CHARACTERISTICS (Note 8)	Symbol	IVIIII	Тур	IVIAX	Unit	Test Condition
	D) /	00	1	1		V 07 1 050 A
Drain-Source Breakdown Voltage	BV _{DSS}	-30	_		V	$V_{GS} = 0V, I_{D} = -250\mu A$
Zero Gate Voltage Drain Current	I _{DSS}		_	-1	μΑ	$V_{DS} = -30V, V_{GS} = 0V$
Gate-Source Leakage	Igss	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(th)}	1	_	-3	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
Static Drain-Source On-Resistance	D		50	70	mΩ	$V_{GS} = -10V, I_D = -5.3A$
Static Drain-Source On-nesistance	R _{DS (ON)}	_	75	95	11122	$V_{GS} = -4.5V$, $I_D = -4.2A$
Forward Transfer Admittance	Y _{fs}	_	5.8	_	S	$V_{DS} = -5V, I_{D} = -5.3A$
Diode Forward Voltage	V_{SD}	_	-0.7	-1.2	V	$V_{GS} = 0V, I_{S} = -1A$
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{iss}		563	_		VDS = -25V, VGS = 0V, f = 1.0MHz
Output Capacitance	Coss	I	48	_	pF	
Reverse Transfer Capacitance	C _{rss}	l	41	_		
Gate Resistance	R_{G}	I	10.3	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$
Total Gate Charge (V _{GS} = -4.5V)	Qg	l	5.2	_		
Total Gate Charge (V _{GS} = -10V)	Q_g	l	11	_	nC	$V_{DS} = -15V$, $I_D = -3.8A$
Gate-Source Charge	Q_{gs}	I	1.7	_	110	
Gate-Drain Charge	Q_{gd}	1	1.9	_		
Turn-On Delay Time	t _{D(on)}	l	4.8	_		
Turn-On Rise Time	t _r	_	5		nS	VDS = -15V, $VGS = -10V$,
Turn-Off Delay Time	Time $t_{D(off)}$ — 31 — nS $ID = -1A, RG = nS$		$ID = -1A$, $RG = 6.0\Omega$			
Turn-Off Fall Time	t _f	_	14.6	_		

5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
7. I_{AR} and E_{AR} rating are based on low frequency and duty cycles to keep T_J = 25°C
8. Short duration pulse test used to minimize self-heating effect.
9. Guaranteed by design. Not subject to product testing.







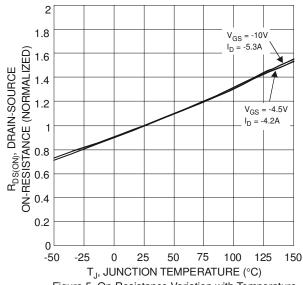
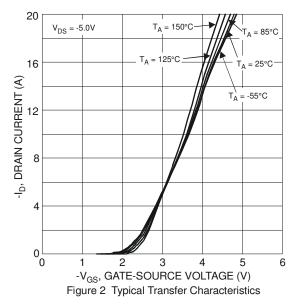
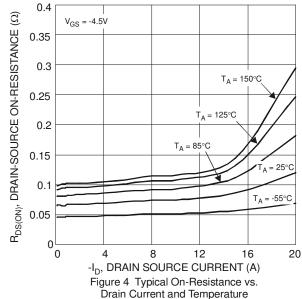


Figure 5 On-Resistance Variation with Temperature





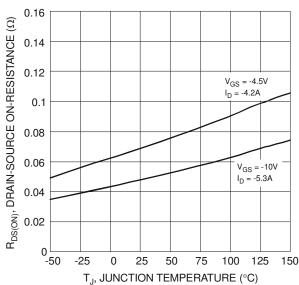


Figure 6 On-Resistance Variation with Temperature



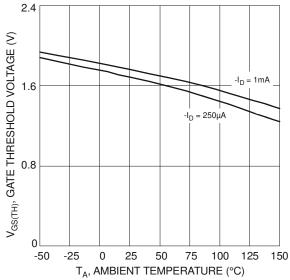
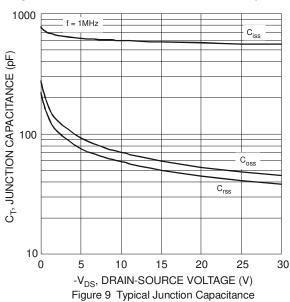
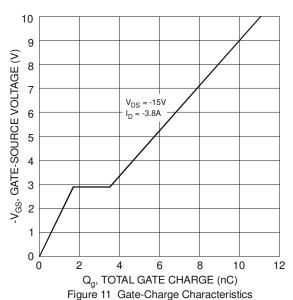


Figure 7 Gate Threshold Variation vs. Ambient Temperature





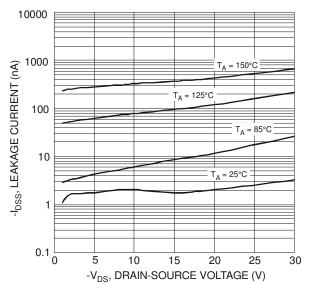
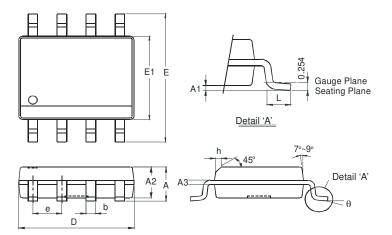


Figure 10 Typical Drain-Source Leakage Current vs. Voltage



Package Outline Dimensions

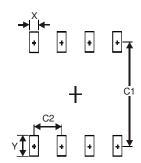
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SO-8						
Dim	Min	Max				
Α	-	1.75				
A 1	0.10	0.20				
A2	1.30	1.50				
A3	0.15	0.25				
b	0.3	0.5				
D	4.85	4.95				
Е	5.90	6.10				
E1	3.85	3.95				
е	e 1.27 Typ					
h	-	0.35				
L	0.62	0.82				
θ	0°	8°				
All Dimensions in mm						

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)			
Х	0.60			
Υ	1.55			
C1	5.4			
C2	1.27			



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