

Data Sheet

Features

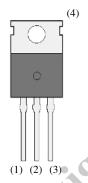
- $R_{DS(ON)}$ -----3.2 m Ω max. ($V_{GS} = 10 \text{ V}$, $I_D = 82.5 \text{ A}$)
- Q_g -----44.9 nC (V_{GS} = 4.5 V, V_{DS} = 20 V, I_D = 82.5 A)
- Low Total Gate Charge
- High Speed Switching
- Low On-Resistance
- Capable of 4.5 V Gate Drive
- 100 % UIL Tested
- RoHS Compliant

Applications

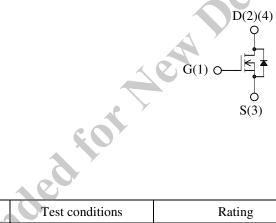
- DC-DC converters
- Synchronous Rectification
- Power Supplies

Package

• TO220-3L



Not to scale



Absolute Maximum Ratings

• Unless otherwise specified, $T_A = 25$ °C

Parameter	Symbol	Test conditions	Rating	Unit
Drain to Source Voltage	V _{DS}	Y	40	V
Gate to Source Voltage	V_{GS}		± 20	V
Continuous Drain Current	I_{D}	T _C = 25 °C	85	A
Pulsed Drain Current	I_{DM}	$PW \le 100 \mu s \\ Duty \ cycle \le 1 \ \%$	170	A
Continuous Source Current (Body Diode)	I_S		85	A
Pulsed Source Current (Body Diode)	I_{SM}	PW ≤ 100μs Duty cycle ≤ 1 %	170	A
Single Pulse Avalanche Energy	E _{AS}	V_{DD} = 20 V, L = 1 mH, I_{AS} = 13 A, unclamped, R_G = 4.7 Ω Refer to Figure 1	170	mJ
Avalanche Current	I_{AS}		30	A
Power Dissipation	P_{D}	T _C = 25 °C	135	W
Operating Junction Temperature	T_{J}		150	°C
Storage Temperature Range	T_{STG}		- 55 to 150	°C

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Thermal Characteristics

• Unless otherwise specified, $T_A = 25$ °C

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Thermal Resistance (Junction to Case)	$R_{ heta JC}$		1	-	0.9	°C/W
Thermal Resistance (Junction to Ambient)	$R_{ heta JA}$		-	_	62.5	°C/W

Electrical Characteristics

• Unless otherwise specified, $T_A = 25$ °C

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Drain to Source Breakdown Voltage	V _{(BR)DSS}	$I_D = 100 \ \mu A, \ V_{GS} = 0 \ V$	40	- U		V
Drain to Source Leakage Current	I_{DSS}	$V_{DS} = 40 \text{ V}, V_{GS} = 0 \text{ V}$			100	μΑ
Gate to Source Leakage Current	I_{GSS}	$V_{GS} = \pm 20 \text{ V}$		_	± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 1.5 \text{ mA}$	1.0	2.0	2.5	V
Static Drain to Source On-Resistance	R _{DS(ON)}	$I_D = 82.5 \text{ A}, V_{GS} = 10 \text{ V}$ $I_D = 41.3 \text{ A}, V_{GS} = 4.5 \text{ V}$	<u></u>	2.6	3.2 4.0	mΩ mΩ
Gate Resistance	R_{G}	f = 1 MHz	_	0.8	_	Ω
Input Capacitance	C _{iss}	W 05W	_	6200	_	pF
Output Capacitance	C _{oss}	$V_{DS} = 25 V$ $V_{GS} = 0 V$	_	960	_	
Reverse Transfer Capacitance	C_{rss}	f = 1 MHz	_	640	_	
Total Gate Charge ($V_{GS} = 10 \text{ V}$)	Q_{g1}	$V_{DS} = 20 \text{ V}$ $I_{D} = 82.5 \text{ A}$	_	93.7	_	nC
Total Gate Charge ($V_{GS} = 4.5 \text{ V}$)	Q_{g2}		_	44.9	_	
Gate to Source Charge	Q_{gs}		_	15.2	_	
Gate to Drain Charge	Q_{gd}		_	16.2	_	
Turn-On Delay Time	$t_{d(on)}$		_	9.7	_	ns
Rise Time	t _r	$\begin{split} V_{DD} &= 20 \text{ V} \\ I_D &= 82.5 \text{ A} \\ V_{GS} &= 10 \text{ V}, R_G = 4.7 \Omega \\ \text{Refer to Figure 2} \end{split}$	_	13.2	_	
Turn-Off Delay Time	$t_{d(off)}$		_	45.5	_	
Fall Time	t_{f}		-	28.0	_	
Source to Drain Diode Forward Voltage	V_{SD}	$I_S = 82.5 \text{ A}, V_{GS} = 0 \text{ V}$	_	0.9	1.5	V
Source to Drain Diode Reverse Recovery Time	t _{rr}	$I_F = 82.5 \text{ A}$ di/dt = 100 A/\mus	_	43.9	_	ns
Source to Drain Diode Reverse Recovery Charge	Qrr	Refer to Figure 3	_	44.8	_	nC

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Test Circuits and Performance Curves

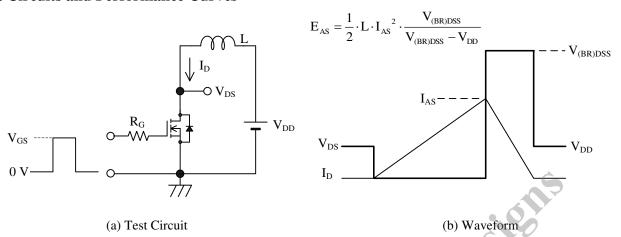


Figure 1. Unclamped Inductive Switching

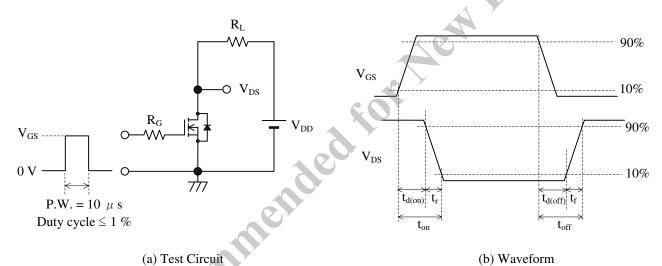


Figure 2. Switching Time

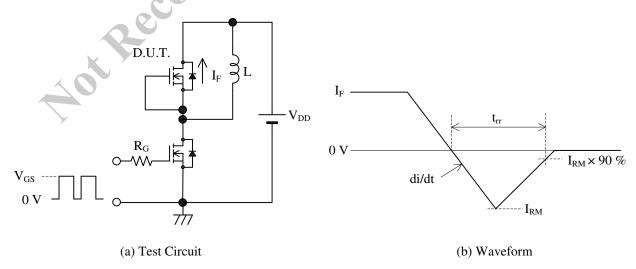
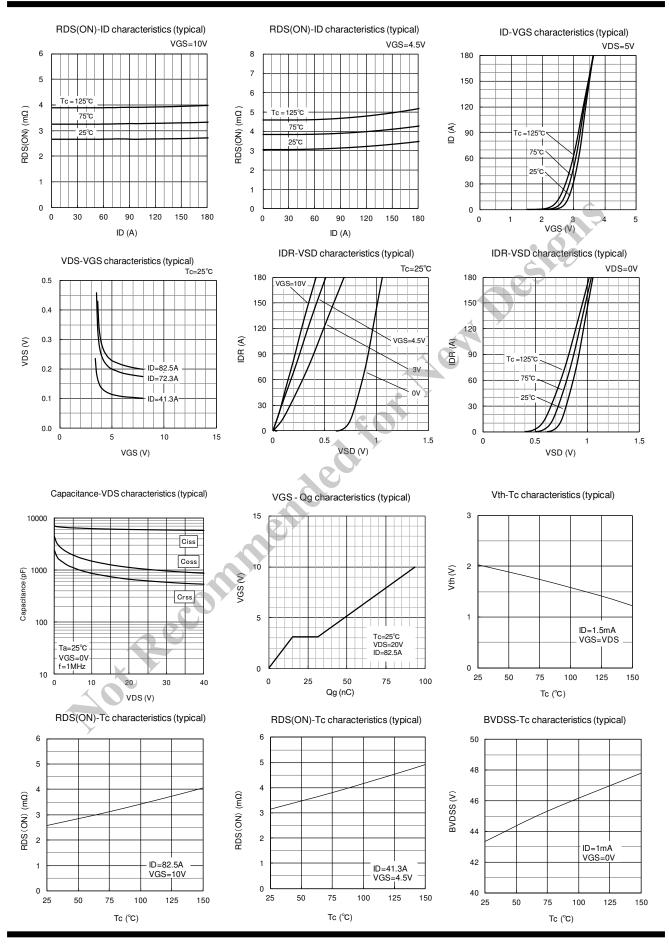
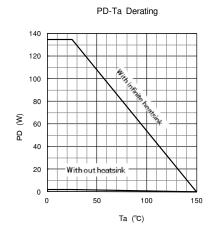
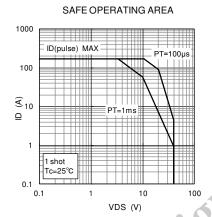
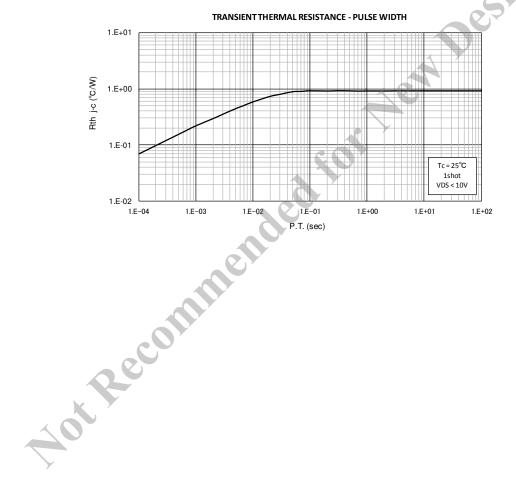


Figure 3. Diode Reverse Recovery Time



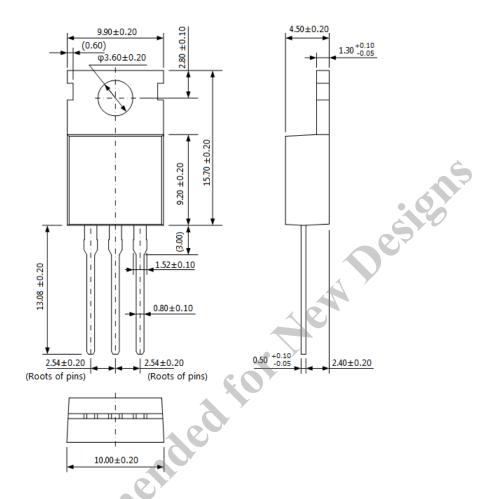






Physical Dimensions

• TO220-3L



NOTES:

- Dimensions in millimeters
- Maximum gate burr height is 0.3 mm.
- Bare lead frame: Pb-free (RoHS compliant)
- When soldering the products, it is required to minimize the working time, within the following limits:

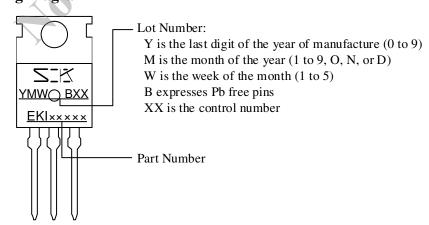
Flow: $260 \pm 5 \, ^{\circ}\text{C} / 10 \pm 1 \, \text{s}, 2 \, \text{times}$

Soldering Iron: 380 ± 10 °C / 3.5 ± 0.5 s, 1 time

Soldering should be at a distance of at least 1.5 mm from the body of the product.

- Recommended screw torque for TO220: 0.490 N·m to 0.686 N·m (5 kgf·cm to 7 kgf·cm)

Marking Diagram



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