

PC912L0NSZ Series

High Speed 25Mb/s, High CMR DIP 8 pin *OPIC Photocoupler



■ Description

PC912L0NSZ Series contains a LED optically coupled to an OPIC chip.

It is packaged in a 8 pin DIP, available in SMT gullwing lead-form option.

Input-output isolation voltage(rms) is 5.0 kV. Data transfer rate is MAX. 25 Mb/s and CMR is MIN. 20 kV/ μs .

■ Features

- 1. DIP 8 pin package
- 2. Double transfer mold package (Ideal for Flow Soldering)
- 3. High speed response

 $(t_{PHL}: MAX. 40ns, t_{PLH}: MAX. 40ns)$

4. High noise immunity due to high instantaneous common mode rejection voltage

 $(CM_H : MIN. 20 kV/\mu s, CM_L : MIN. -20 kV/\mu s)$

5. High isolation voltage between input and output (V_{iso(rms)}: 5.0 kV)

■ Agency approvals/Compliance

- 1. Recognized by UL1577 (Double protection isolation), file No. E64380 (as model No. **PC912L**)
- 2. Approved by TÜV (VDE0884) (as an option), file No. 87446 (as model No. **PC912L**)
- 3. Package resin: UL flammability grade (94V-0)

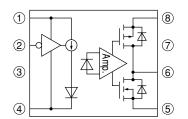
■ Applications

1. FA equipment

^{* &}quot;OPIC"(Optical IC) is a trademark of the SHARP Corporation. An OPIC consists of a light-detecting element and a signal-processing circuit integrated onto a single chip.



■ Internal Connection Diagram



- ① V_{CC1} ⑤ GND2
- \bigcirc V_{IN} 6 V_O
- * ③ NC 7 NC 4 GND1
 - $\otimes V_{CC2}$

*pin ③ is not allowed external connection

■ Truth table

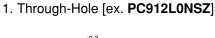
Input	LED	Output
L	ON	L
<u>— н</u>	OFF	Н

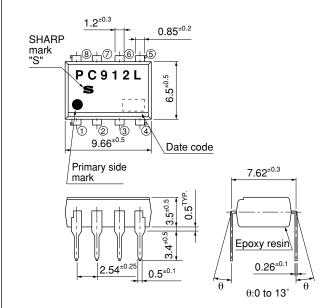
L : Logic (0)

H:Logic (1)

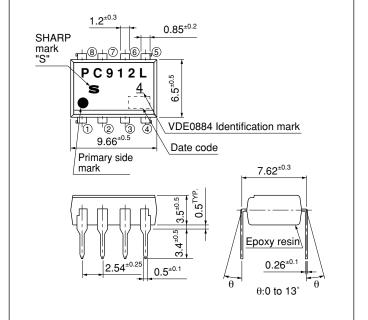
■ Outline Dimensions

(Unit: mm)





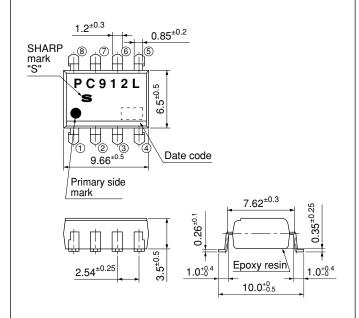
2. Through-Hole (VDE0884 option) [ex. PC912V0YSZ]



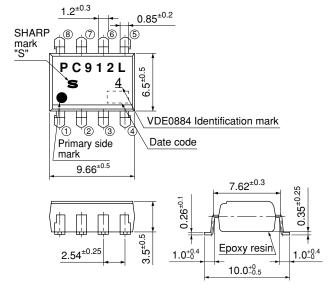


(Unit: mm)

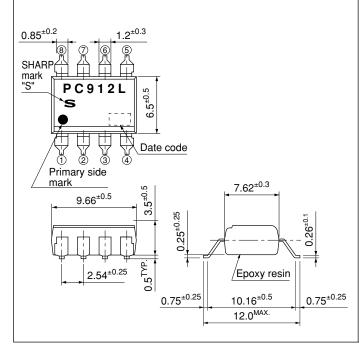




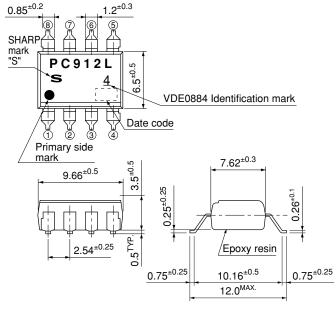
4. SMT Gullwing Lead-Form (VDE0884 option) [ex. **PC912L0YIZ**]



5. Wide SMT Gullwing Lead-Form [ex. PC912L0NUZ]



6. Wide SMT Gullwing Lead-Form (VDE0884 option) [ex. **PC912L0YUZ**]



Product mass: approx. 0.49g



Date code (2 digit)

	1st o	digit		2nd digit		
Year of production				Month of production		
A.D.	Mark	A.D	Mark	Month	Mark	
1990	A	2002	P	January	1	
1991	В	2003	R	February	2	
1992	С	2004	S	March	3	
1993	D	2005	T	April	4	
1994	Е	2006	U	May	5	
1995	F	2007	V	June	6	
1996	Н	2008	W	July	7	
1997	J	2009	X	August	8	
1998	K	2010	A	September	9	
1999	L	2011	В	October	0	
2000	M	2012	С	November	N	
2001	N	:	:	December	D	

repeats in a 20 year cycle

Country of origin Japan



■ Absolute Maximum Ratings

(Unless otherwise specified T_a = T_{opr})

		•		
	Parameter	Symbol	Rating	Unit
Innut	Supply voltage	V_{CC1}	$V_{\rm CC1}$ 0 to 5.5	
Input	Input voltage	V_{IN}	-0.5 to V _{CC1} +0.5	V
	Supply voltage	V_{CC2}	0 to 5.5	V
Output	High level output voltage	V_{O}	-0.5 to V _{CC2} +0.5	V
	Low level output current	I_{O}	10	mA
*1 Isolation voltage		$V_{iso(rms)}$	5.0	kV
Ope	rating temperature	T_{opr}	-40 to +85	°C
Storage temperature		T_{stg}	-55 to +125	°C
*2 Soldering temperature		T_{sol}	270	°C

^{*1 40} to 60%RH, AC for 1minute, f=60Hz

■ Electro-optical Characteristics*3

(Unless otherwise specified T_a=T_{opr})

	(Omess ductwise specified 1 _a -						- a - opi/	
Parameter Symbol Condition			Conditions	MIN.	TYP.	MAX.	Unit	
^ h	Low level supply current		I_{CC1L}	$V_{IN}=0V$	_	6.0	10.0	mA
	High level supply current		I_{CC1H}	$V_{IN}=V_{CC1}$	_	0.8	3.0	mA
	Input current		I_{IN}	V _{CC1} =5V	-10	_	10	μΑ
	High level supply current		I_{CC2H}	$V_{CC1}=V_{CC2}=5V, V_{IN}=5V$	-	7.0	9.0	mA
	Low level supply current		I_{CC2L}	$V_{CC1}=V_{CC2}=5V, V_{IN}=0V$	_	5.5	9.0	mA
	11:0	High level output voltage		$I_O=-20\mu A,\ V_{IN}=5V$	4.4	5.0	_	V
Output	Hig			$I_O=-4mA$, $V_{IN}=5V$	4.0	4.8	-	V
				$I_{O}=20\mu A, V_{IN}=0V$	_	0	0.1	V
	Lov	Low level output voltage		$I_O=400\mu A,V_{IN}=0V$	_	-	0.1	V
				$I_{O}=4mA, V_{IN}=0V$	_	0.5	1.0	V
	Isolation resistance		R _{ISO}	DC500V, 40 to 60%RH	5×10 ¹⁰	1011	-	Ω
	time	"High-Low" propagation delay time	t _{PHL}		-	20	40	ns
		"Low→High" propagation delay time	t_{PLH}	$V_{CC1}=V_{CC2}=5V$ $C_L=15pF$, CMOS Logic level	_	23	40	ns
		Pulse width distortion t _{pHL} -t _{pLH}	Δtw		_	_	6	ns
	esbonse	Propagation delay skew	T_{PSK}	$V_{IN}=0\rightarrow 5V$ $t_r=t_f<1$ ns	_	-	20	ns
Transfer	spc	Data transfer rate	T	Pulse width 40ns	_	-	25	Mb/s
charac-	Rise time		t _r	Duty 50%	_	9	-	ns
teristics		Fall time	$t_{\rm f}$		_	8	-	ns
	Instantaneous common mode rejection voltage "Output : High level"		CM _H	$V_{IN}=V_{CC1}, V_{O}<0.8\times V_{CC2}$ $V_{CM}=1kV$	20	_	_	kV/μs
	Instantaneous common mode rejection voltage "Output : Low level"		CM_L	V_{IN} =0, V_{O} <0.8V V_{CM} =1kV	-20	П	_	kV/μs

^{*3} When measuring output and transfer characteristics, connect a by-pass capacitor $(0.01\mu F \text{ or more})$ between V_{CC1} (pin ①) and GND_1 (pin ④), between V_{CC2} (pin ⑧) and GND_2 (pin ⑤) near the device.

All typical values:at T_a =25°C, V_{CC1} = V_{CC2} =5V

^{*2} For 10s



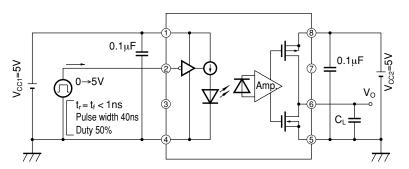
■ Model Line-up

Lead Form	Through-Hole		SMT Gullwing		Wide SMT Gullwing	
Doolsooo	Sleeve		Sleeve		Sleeve	
Package	50pcs/sleeve		50pcs/sleeve		50pcs/sleeve	
VDE0884		Approved		Approved		Approved
Model No.	PC912L0NSZ	PC912L0YSZ	PC912L0NIZ	PC912L0YIZ	PC912L0NUZ	PC912L0YUZ

Please contact a local SHARP sales representative to inquire about production status and Lead-Free options.



Fig.1 Test Circuit for Propagation Delay Time and Rise Time, Fall Time



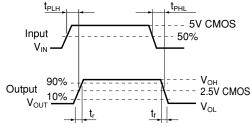
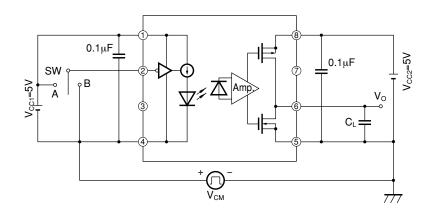


Fig.2 Test Circuit for Instantaneous Common Mode Rejection Voltage



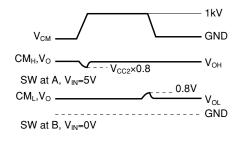


Fig.3 Propagation Delay Time vs. Ambient Temperature

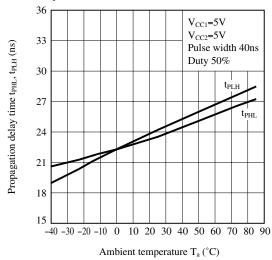
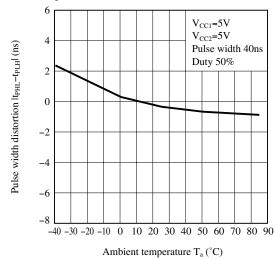


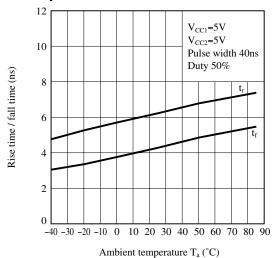
Fig.4 Pulse Width Distortion vs. Ambient Temperature



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Fig.5 Rise Time / Fall Time vs. Ambient Temperature



Remarks: Please be aware that all data in the graph are just for reference and not for guarantee.



■ Design Considerations

Recommended operating conditions

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Supply voltage	V_{CC1}	4.5	-	5.5	V
Supply voltage	V_{CC2}	4.5	_	5.5	V
Low level input voltage	V_{IL}	0	-	0.8	V
High level input voltage	V_{IH}	2.0	_	V _{CC1}	V
Operating temperature	Topr	-40	-	+70	°C

Notes about static electricity

Transistor of detector side in CMOS configuration may be damaged by static electricity due to its minute design.

When handling these devices, general countermeasure against static electricity should be taken to avoid breakdown of devices or degradation of characteristics.

Design guide

In order to stabilize power supply line, we should certainly recommend to connect a by-pass capacitor of $0.01\mu F$ or more between V_{CC1} -GND and V_{CC2} -GND near the device.

The detector which is used in this device, has parasitic diode between each pins and GND.

There are cases that miss operation or destruction possibly may be occurred if electric potential of any pin becomes below GND level even for instant.

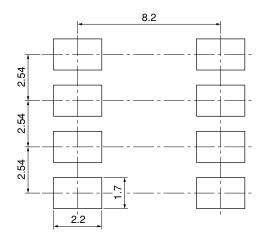
Therefore it shall be recommended to design the circuit that electric potential of any pin does not become below GND level.

This product is not designed against irradiation and incorporates non-coherent LED.



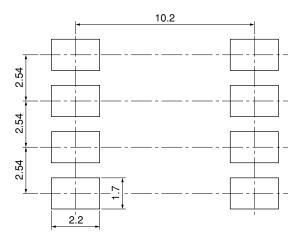
Recommended Foot Print (reference)

SMT Gullwing Lead-form



(Unit: mm)

Wide SMT Gullwing Lead-form



(Unit:mm)

[☆] For additional design assistance, please review our corresponding Optoelectronic Application Notes.



■ Manufacturing Guidelines

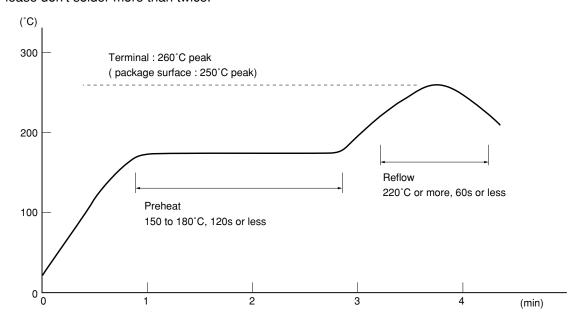
Soldering Method

Reflow Soldering:

Reflow soldering should follow the temperature profile shown below.

Soldering should not exceed the curve of temperature profile and time.

Please don't solder more than twice.



Flow Soldering:

Due to SHARP's double transfer mold construction submersion in flow solder bath is allowed under the below listed guidelines.

Flow soldering should be completed below 270°C and within 10s.

Preheating is within the bounds of 100 to 150°C and 30 to 80s.

Please don't solder more than twice.

Hand soldering

Hand soldering should be completed within 3s when the point of solder iron is below 400°C.

Please don't solder more than twice.

Other notices

Please test the soldering method in actual condition and make sure the soldering works fine, since the impact on the junction between the device and PCB varies depending on the tooling and soldering conditions.



Cleaning instructions

Solvent cleaning:

Solvent temperature should be 45°C or below Immersion time should be 3minutes or less

Ultrasonic cleaning:

The impact on the device varies depending on the size of the cleaning bath, ultrasonic output, cleaning time, size of PCB and mounting method of the device.

Therefore, please make sure the device withstands the ultrasonic cleaning in actual conditions in advance of mass production.

Recommended solvent materials:

Ethyl alcohol, Methyl alcohol and Isopropyl alcohol

In case the other type of solvent materials are intended to be used, please make sure they work fine in actual using conditions since some materials may erode the packaging resin.

Presence of ODC

This product shall not contain the following materials.

And they are not used in the production process for this device.

Regulation substances: CFCs, Halon, Carbon tetrachloride, 1.1.1-Trichloroethane (Methylchloroform)

Specific brominated flame retardants such as the PBBOs and PBBs are not used in this product at all.

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■ Package specification

Sleeve package

1. Through-Hole or SMT Gullwing

Package materials

Sleeve: HIPS (with anti-static material)

Stopper: Styrene-Elastomer

Package method

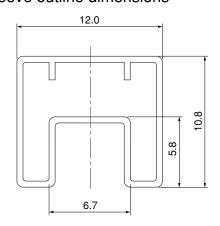
MAX. 50 pcs. of products shall be packaged in a sleeve.

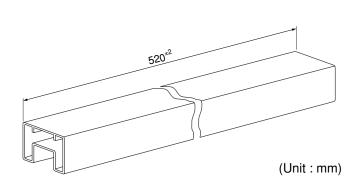
Both ends shall be closed by tabbed and tabless stoppers.

The product shall be arranged in the sleeve with its primary side mark on the tabless stopper side.

MAX. 20 sleeves in one case.

Sleeve outline dimensions





2. Wide SMT Gullwing

Package materials

Sleeve: HIPS (with anti-static material)

Stopper: Styrene-Elastomer

Package method

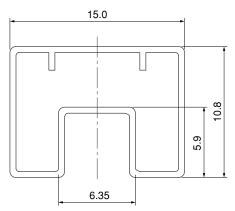
MAX. 50 pcs. of products shall be packaged in a sleeve.

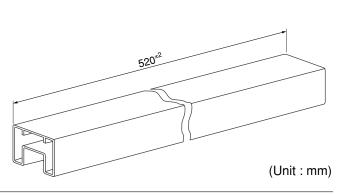
Both ends shall be closed by tabbed and tabless stoppers.

The product shall be arranged in the sleeve with its primary side mark on the tabless stopper side.

MAX. 20 sleeves in one case.

Sleeve outline dimensions







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- --- Various safety devices, etc.
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