



60V P-Channel Enhancement Mode MOSFET

Voltage

-60 V

Current

-200mA

Features

- RDS(ON), VGS@-10V, ID@-500mA<4Ω
- RDS(ON) , VGS@-4.5V, ID@-200mA<6Ω
- RDS(ON) , VGS@-2.5V, ID@-50mA<13Ω
- Advanced Trench Process Technology
- Specially Designed for Relay driver, Speed line drive, etc.
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

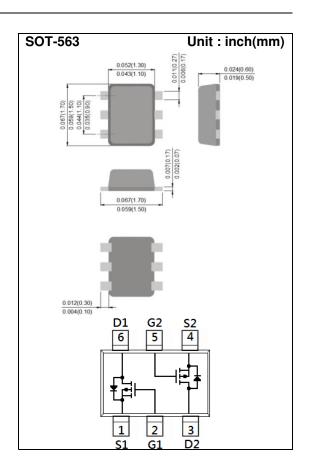
Mechanical Data

Case: SOT-563 Package

Terminals: Solderable per MIL-STD-750, Method 2026

Approx. Weight: 0.0026 grams

Marking: X39



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	-60	V
Gate-Source Voltage		V_{GS}	<u>+</u> 20	V
Continuous Drain Current		ID	-200	mA
Pulsed Drain Current		I _{DM}	-800	mA
Power Dissipation	T _A =25°C	P _D	300	mW
	Derate above 25°C		2.4	mW/°C
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C
Typical Thermal Resistance - Junction to Ambient ^(Note 3)		R _{θJA}	417	°C/W





Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V,I _D =-250uA	-60	-	-	V
Gate Threshold Voltage	$V_{\text{GS(th)}}$	V _{DS} =V _{GS} , I _D =-250uA	-1.0	-1.5	-2.5	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-10V,I _D =-500mA	-	2.4	4	Ω
		V _{GS} =-4.5V,I _D =-200mA	-	2.65	6	
		V _{GS} =-2.5V,I _D =-50mA	-	4.5	13	
Zero Gate Voltage Drain Current	IDSS	V _{DS} =-48V,V _{GS} =0V	-	-	-1	uA
Gate-Source Leakage Current	Igss	V _{GS=+} 20V,V _{DS} =0V	-	-	<u>+</u> 100	nA
Dynamic ^(Note 4)						
Total Gate Charge	Q_g	V _{DS} =-25V, I _D =-100mA, V _{GS} =-4.5V	-	1.1	-	nC
Gate-Source Charge	Q_{gs}		-	0.3	-	
Gate-Drain Charge	Q_{gd}		-	0.2	-	
Input Capacitance	Ciss	V _{DS} =-25V, V _{GS} =0V, f=1.0MHZ	-	51	-	pF
Output Capacitance	Coss		-	15	-	
Reverse Transfer Capacitance	Crss		-	2.2	-	
Turn-On Delay Time	td _(on)	\\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	-	4.8	-	
Turn-On Rise Time	tr	V_{DD} =-25V, I_{D} =-100mA, V_{GS} =-10V, R_{G} =6 $\Omega^{(Note 1,2)}$	-	19	-	ns
Turn-Off Delay Time	td _(off)		-	52	-	
Turn-Off Fall Time	tf	MG=012(Note 1,2)	-	32	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	ls		-	-	-200	mA
Diode Forward Voltage	V _{SD}	I _S =-500mA, V _{GS} =0V	-	-0.95	-1.3	V

NOTES:

- 1. Pulse width<a>300us, Duty cycle<a>2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. mounted on a 1 inch square pad of copper
- 4. Guaranteed by design, not subject to production testing





TYPICAL CHARACTERISTIC CURVES

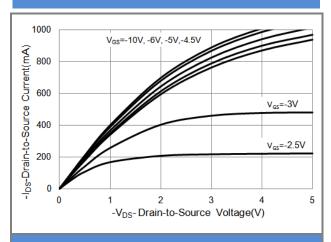


Fig.1 On-Region Characteristics

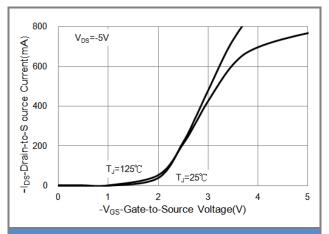


Fig.2 Transfer Characteristics

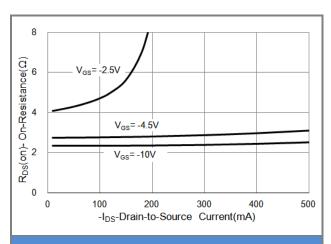


Fig.3 On-Resistance vs. Drain Current

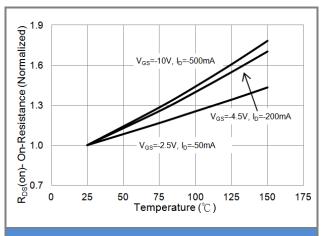


Fig.4 On-Resistance vs. Junction temperature

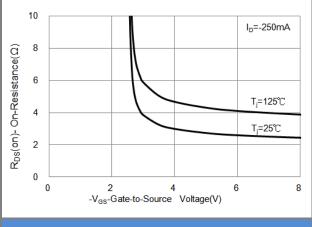


Fig.5 On-Resistance Variation with VGS.

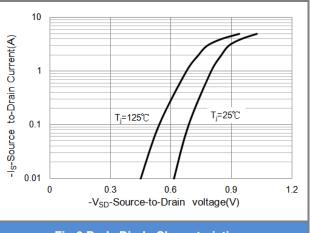


Fig.6 Body Diode Characteristics





TYPICAL CHARACTERISTIC CURVES

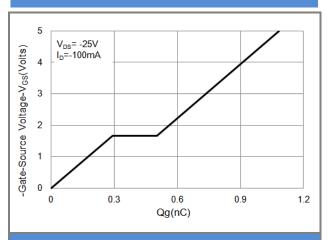


Fig.7 Gate-Charge Characteristics

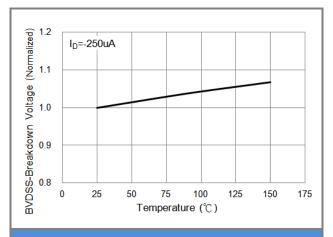


Fig.8 Breakdown Voltage Variation vs. Temperature

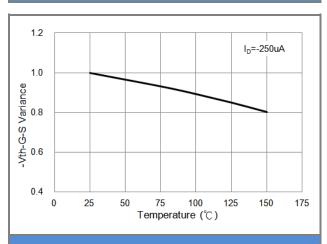


Fig.9 Threshold Voltage Variation with Temperature.

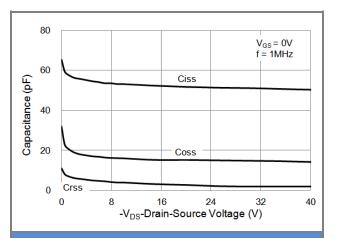


Fig.10 Capacitance vs. Drain-Source Voltage.

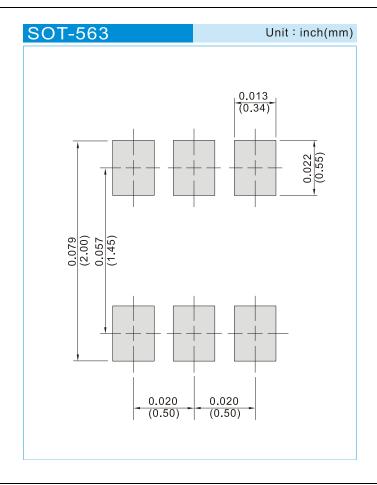




PART NO. PACKING CODE VERSION

PART NO. PACKING CODE	Package Type	Packing Type	Marking	Version
PJX8839_R1_00001	SOT-563	4K pcs / 7" reel	X39	Halogen free RoHS compliant
PJX8839_R2_00001	SOT-563	10K pcs / 13" reel	X39	Halogen free RoHS compliant

MOUNTING PAD LAYOUT







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