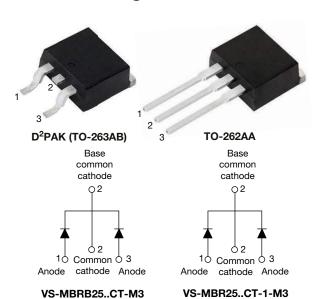
### VS-MBRB25..CT-M3, VS-MBR25..CT-M3

Vishay Semiconductors

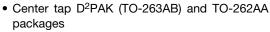
## High Performance Schottky Rectifier, 2 x 15 A



PRIMARY CHARACTERISTICS						
I <sub>F(AV)</sub> 2 x 15 A						
$V_{R}$	35 V, 45 V					
V <sub>F</sub> at I <sub>F</sub>	See datasheet					
I <sub>RM</sub> max.	40 mA at 125 °C					
T <sub>J</sub> max.	150 °C					
E <sub>AS</sub>	16 mJ					
Package	D <sup>2</sup> PAK (TO-263AB), TO-262AA					
Circuit configuration	Common cathode					

#### **FEATURES**

• 150 °C T<sub>J</sub> operation





Low forward voltage drop

- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **DESCRIPTION**

This center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL	CHARACTERISTICS	VALUES	UNITS					
I <sub>F(AV)</sub>	Rectangular waveform (per device)	30	^					
I <sub>FRM</sub>	T <sub>C</sub> = 130 °C (per leg)	30	A					
V <sub>RRM</sub>		35/45	V					
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	1060	Α					
V <sub>F</sub>	30 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.73	V					
T <sub>J</sub>	Range	-65 to +150	°C					

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-MBRB2535CT-M3 VS-MBR2535CT-1-M3	VS-MBRB2545CT-M3 VS-MBR2545CT-1-M3	UNITS				
Maximum DC reverse voltage	$V_R$	35	45	V				
Maximum working peak reverse voltage	$V_{RWM}$		45	V				



# VS-MBRB25..CT-M3, VS-MBR25..CT-M3

# Vishay Semiconductors

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	DL TEST CONDITIONS		VALUES	UNITS		
Maximum average per le		T 100 %0t-d.V		15			
forward current per device	F(AV)	$T_C = 130  ^{\circ}\text{C}$ , rated $V_R$		30			
Peak repetitive forward current per leg	I <sub>FRM</sub>	Rated V <sub>R</sub> , square wave, 20 kHz, T <sub>C</sub> = 130 °C		30			
Non-repetitive peak surge current	I <sub>FSM</sub>	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V <sub>RRM</sub> applied	1060	А		
		Surge applied at rated load conditions halfwave, single phase, 60 Hz		150			
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 2 A, L = 8 mH		16	mJ		
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s  Frequency limited by T <sub>I</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		2	А		

ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS				
Maximum famuard valtage drap	V (1)	30 A	T <sub>J</sub> = 25 °C	0.82	V		
Maximum forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	30 A	T <sub>J</sub> = 125 °C	0.73	V		
Maximum instantaneous	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C Rated DC voltage		0.2	mA		
reverse current	'RM '''	T <sub>J</sub> = 125 °C	- hated DC voltage	40	ı IIIA		
Threshold voltage	V <sub>F(TO)</sub>	T - T movimum	T. T				
Forward slope resistance	r <sub>t</sub>	ij = ijiliaxiiliulii	$T_J = T_J$ maximum		mΩ		
Maximum junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal range	700	pF			
Typical series inductance	L <sub>S</sub>	Measured from top of term	8.0	nH			
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>	10 000	V/µs			

#### Note

 $^{(1)}$  Pulse width < 300  $\mu$ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction tempe	rature range	TJ		-65 to 150	°C			
Maximum storage temper	rature range	T <sub>Stg</sub>		-65 to 175	C			
Maximum thermal resistance, junction to case per leg		R <sub>thJC</sub> DC operation 1.5		1.5				
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.50	°C/W			
Approximate weight				2	g			
Approximate weight				0.07	OZ.			
Mounting torque	minimum		New July wighted through	6 (5)	kgf · cm			
Mounting torque maximum			Non-lubricated threads	12 (10)	(lbf · in)			
Marking device			Case style D <sup>2</sup> PAK (TO-263AB)	MBRB2 MBRB2	2535CT 2545CT			
			Case style TO-262AA	MBR25 MBR25				

## Vishay Semiconductors

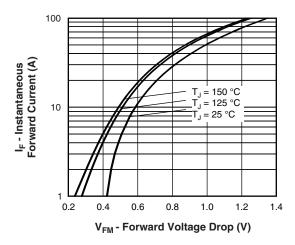


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

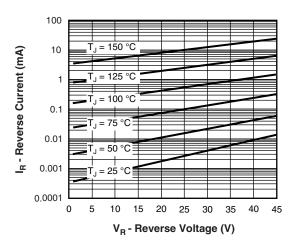


Fig. 2 - Typical Values of Reverse Current vs.Reverse Voltage (Per Leg)

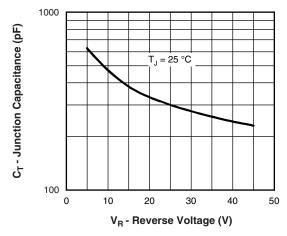


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

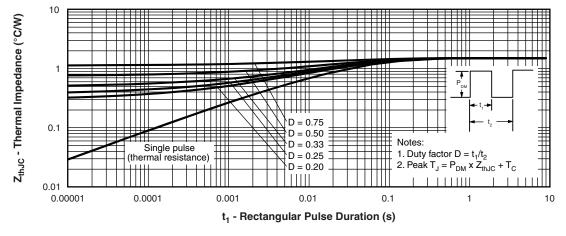


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

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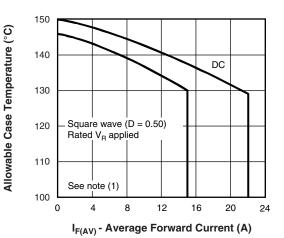


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

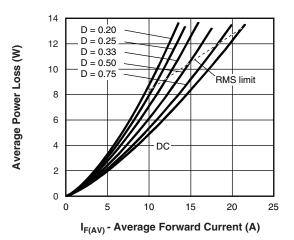


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

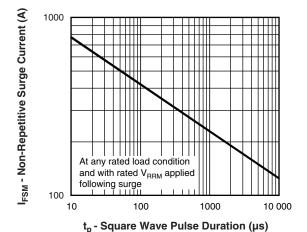


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

#### Note

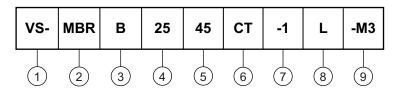
 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (\text{Pd} + \text{Pd}_{\text{REV}}) \times \text{R}_{\text{thJC}}; \\ \text{Pd} = & \text{forward power loss} = \text{I}_{\text{F(AV)}} \times \text{V}_{\text{FM}} \text{ at } (\text{I}_{\text{F(AV)}}/\text{D}) \text{ (see fig. 6)}; \\ \text{Pd}_{\text{REV}} = & \text{inverse power loss} = \text{V}_{\text{R1}} \times \text{I}_{\text{R}} \text{ (1 - D)}; \text{I}_{\text{R}} \text{ at } \text{V}_{\text{R1}} = \text{rated V}_{\text{R}} \\ \end{array}$ 

# VS-MBRB25..CT-M3, VS-MBR25..CT-M3

### Vishay Semiconductors

### **ORDERING INFORMATION TABLE**

Device code



1 - Vishay Semiconductors product

2 - Essential part number

3 - • B =  $D^2PAK$  (TO-263AB) 7 None

4 - Current rating (25 = 25 A)

Voltage ratings 35 = 35 V 45 = 45 V

6 - CT = essential part number

7 - • None =  $D^2PAK (TO-263AB)$  3 = B

• -1 = TO-262AA **3** None

8 - • None = tube

5

• L = tape and reel (left oriented - for D<sup>2</sup>PAK (TO-263AB) only)

• R = tape and reel (right oriented - for D<sup>2</sup>PAK (TO-263AB) only)

9 - -M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION					
VS-MBRB2535CTL-M3	800	13" diameter plastic tape and reel					
VS-MBRB2535CT-M3	50	Antistatic plastic tubes					
VS-MBRB2535CTR-M3	800	13" diameter plastic tape and reel					
VS-MBRB2545CTL-M3	800	13" diameter plastic tape and reel					
VS-MBRB2545CT-M3	50	Antistatic plastic tubes					
VS-MBRB2545CTR-M3	800	13" diameter plastic tape and reel					
VS-MBR2535CT-1-M3	50	Antistatic plastic tubes					
VS-MBR2545CT-1-M3	50	Antistatic plastic tubes					

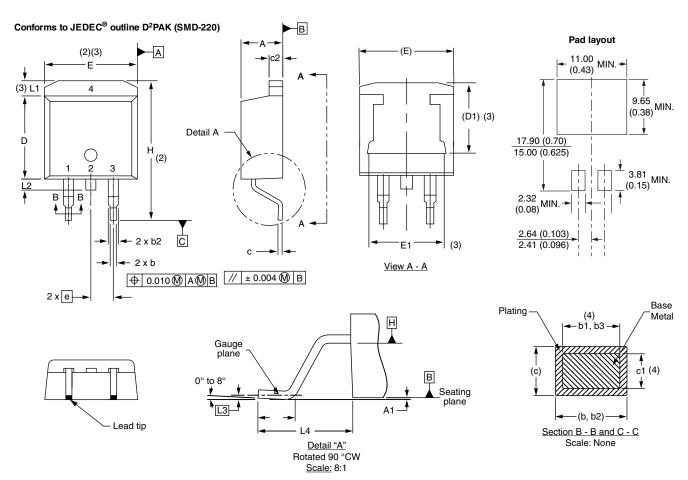
LINKS TO RELATED DOCUMENTS						
Dimensions D <sup>2</sup> PAK (TO-263AB) <u>www.vishay.com/doc?96164</u>						
Difficultions	TO-262AA	www.vishay.com/doc?96165				
Part marking information —	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?95444				
Fart marking information —	TO-262AA	www.vishay.com/doc?95443				
Packaging information		www.vishay.com/doc?96424				



## Vishay Semiconductors

## D<sup>2</sup>PAK

### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIM	ETERS	INCHES		NOTES	NOTES SYMBOL		MILLIM	ETERS	INC	HES	NOTES
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

#### Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inches
- (7) Outline conforms to JEDEC® outline TO-263AB

Revision: 13-Jul-17 Document Number: 96164

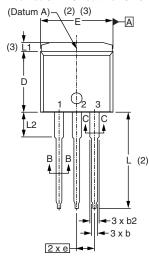


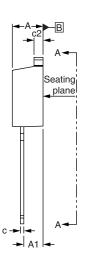
### Vishay Semiconductors

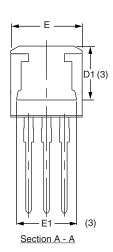
### **TO-262AA**

### **DIMENSIONS** in millimeters and inches

#### Modified JEDEC® outline TO-262







**⊕** 0.010 **M** A**M** B

#### Lead assignments

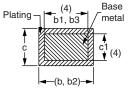


**Diodes** 

1. - Anode (two die)/open (one die)

2., 4. - Cathode

3. - Anode



Section B - B and C - C Scale: None

SYMBOL	MILLIN	IETERS	INC	INCHES			
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES		
А	4.06	4.83	0.160	0.190			
A1	2.03	3.02	0.080	0.119			
b	0.51	0.99	0.020	0.039			
b1	0.51	0.89	0.020	0.035	4		
b2	1.14	1.78	0.045	0.070			
b3	1.14	1.73	0.045	0.068	4		
С	0.38	0.74	0.015	0.029			
c1	0.38	0.58	0.015	0.023	4		
c2	1.14	1.65	0.045	0.065			
D	8.51	9.65	0.335	0.380	2		
D1	6.86	8.00	0.270	0.315	3		
E	9.65	10.67	0.380	0.420	2, 3		
E1	7.90	8.80	0.311	0.346	3		
е	2.54 BSC		0.100	BSC			
L	13.46	14.10	0.530	0.555			
L1	-	1.65	-	0.065	3		
L2	3.56	3.71	0.140	0.146			

#### **Notes**

(4) Dimension b1 and c1 apply to base metal only

Controlling dimension: inches

<sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994
(2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

Thermal pad contour optional within dimension E, L1, D1 and E1

Outline conform to JEDEC® TO-262 except A1 (max.), b (min., max.), b1 (min.), b2 (max.), c (min.), c1(min.), c2 (max.), D (min.), E (max.), L1 (max.), L2 (min., max.)



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