

#### **Features**

- Optimized for CW, Pulsed, WiMAX, and other applications from 3.3 - 3.8 GHz
- 18 W P3dB CW Power
- 25 W P3dB Peak Envelope Power
- 1.7 W Linear Power @ 2% EVM for single carrier OFDM, 10.3 dB peak/average, 10.3 dB @ 0.01% Probability on CCDF, 10.5 dB Gain, 18% Drain Efficiency
- 100% RF tested
- Thermally-Enhanced Surface Mount SOIC Package
- High Reliability Gold Metallization Process
- Subject to EAR99 Export Control
- RoHS\* Compliant

#### **Applications**

- Defense Communications
- Land Mobile Radio
- Avionics
- Wireless Infrastructure
- ISM
- VHF/UHF/L/S-Band Radar

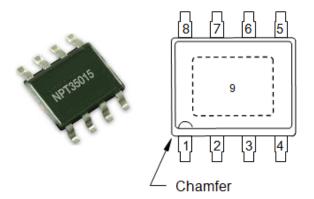
#### **Description**

The NPT35015 GaN HEMT is a power transistor optimized for 3.3 - 3.5 GHz operation. This device supports CW, pulsed, and linear operation with output power levels to 18 W. This transistor is assembled in an industry standard surface mount plastic package.

#### **Ordering Information**

Part Number	Package
NPT35015DT	Tube (97 pieces)
NPT35015DR	1500 piece reel

#### **Functional Schematic**



### **Pin Configuration**

Pin#	Function
1 - 4	Gate
5 - 8	Drain
9	Paddle <sup>1</sup>

 The exposed pad centered on the package bottom must be connected to RF and DC ground. This path must also provide a low thermal resistance heat path.

<sup>\*</sup> Restrictions on Hazardous Substances, compliant to current RoHS EU directive.



# Typical 2-Tone RF Performance: (measured in test fixture) Freq. = 3.5 GHz, $V_{DS}$ = 28 V, $I_{DQ}$ = 200 mA, Tone Spacing = 1 MHz, $T_{C}$ = 25°C

Parameter	Test Conditions	Symbol	Min.	Тур.	Max.	Units
Peak Envelope Power	3 dB Compression 1 dB Compression	P <sub>3dB, PEP</sub> P <sub>1dB, PEP</sub>	14 —	18 10		W
Small Signal Gain	_	G <sub>SS</sub>	10	11		dB
Drain Efficiency	3 dB Compression	η	43	48	_	%

# Typical RF Specifications (CW): (measured in Load Pull System) Freq. = 3.5 GHz, $V_{DS} = 28 \text{ V}$ , $I_{DQ} = 200 \text{ mA}$ , $T_{C} = 25^{\circ}\text{C}$

Parameter	Test Conditions	Symbol	Min.	Тур.	Max.	Units
Average Output Power	3 dB Compression	P <sub>3dB</sub>		18	_	W
Small Signal Gain	3 dB Compression 1 dB Compression	P <sub>3dB, Pulsed</sub> P <sub>1dB, Pulsed</sub>		20 15	_	W

Typical OFDM Performance: (measured in load pull system, refer to Table 1 and Figure 1))  $V_{DS}$  = 28 V,  $I_{DQ}$  = 200 mA, Single Carrier OFDM waveform 64-QAM 3/4, 8 burst, 20 ms frame, 15 ms frame data, 3.5 GHz channel bandwidth, Peak/Avg = 10.3 dB @ 0.01% probability on CCDF,  $P_{OUT}$  = 1.7 W avg.,  $T_{C}$  = 25°C

Parameter	Test Conditions	Symbol	Min.	Тур.	Max.	Units
Power Gain	3.3 - 3.8 GHz	G₽	_	10.5	_	dB
Drain Efficiency	3.3 - 3.8 GHz	η	_	18	_	%
Error Vector Magnitude	3.3 - 3.8 GHz	EVM	_	2	_	%
Input Return Loss	3.3 - 3.8 GHz	I <sub>RL</sub>	_	10	_	dB

#### **Handling Procedures**

Please observe the following precautions to avoid damage:

#### Static Sensitivity

Gallium Nitride Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these HBM Class 1A devices.



# DC Electrical Characteristics: T<sub>c</sub> = 25°C

Parameter	Test Conditions	Symbol	Min.	Тур.	Max.	Units
Off Characteristics						
Drain-Source Breakdown Voltage	V <sub>GS</sub> = -8 V, I <sub>D</sub> = 8 mA	V <sub>BDS</sub>	100	_	_	V
Gate-Source Leakage Current	V <sub>GS</sub> = -8 V, V <sub>DS</sub> = 60 V	I <sub>DLK</sub>	_	_	4	mA
On Characteristics						
Gate Threshold Voltage	V <sub>DS</sub> = 28 V, I <sub>D</sub> = 8 mA	V <sub>T</sub>	-2.3	-1.8	-1.3	V
Gate Quiescent Voltage	V <sub>DS</sub> = 28 V, I <sub>D</sub> = 200 mA	$V_{GSQ}$	-2.0	-1.5	-1.0	V
On Resistance $V_{GS} = 2 \text{ V}, I_D = 60 \text{ mA}$		R <sub>ON</sub>	_	0.45	0.50	Ω
Drain Current	V <sub>DS</sub> = 7 V pulsed, pulse width 300 μs 0.2% Duty Cycle, V <sub>GS</sub> = 2 V	I <sub>D</sub>	_	5	_	Α

# Absolute Maximum Ratings<sup>2,3,4</sup>

Parameter	Absolute Maximum
Drain Source Voltage, V <sub>DS</sub>	100 V
Gate Source Voltage, V <sub>GS</sub>	-10 to 3 V
Total Device Power Dissipation (derated above 25°C)	28 W
Junction Temperature, T <sub>J</sub>	+200°C
Operating Temperature	-40°C to +85°C
Storage Temperature	-65°C to +150°C

- 2. Exceeding any one or combination of these limits may cause permanent damage to this device.
- 3. MACOM does not recommend sustained operation near these survivability limits.
- 4. Operating at nominal conditions with  $T_J \le 200^{\circ}$ C will ensure MTTF > 1 x  $10^6$  hours.

### Thermal Characteristics<sup>5</sup>

Parameter	Parameter Test Conditions		Parameter Test Conditions Symbol Typical		Typical	Units
Thermal Resistance	V <sub>DS</sub> = 28 V, T <sub>J</sub> = 200°C	$R_{\theta JC}$	6.25	°C/W		

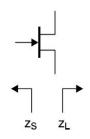
<sup>5.</sup> Junction temperature (T<sub>J</sub>) measured using IR Microscopy. Case temperature measured using thermocouple embedded in heat-sink.



Table 1: Optimum Impedance Characteristics for Single Carrier OFDM waveform 64-QAM 3/4, 8 burst, 20 ms frame, 15 ms frame data, 3.5 GHz Channel Bandwidth, Peak/Avg = 10.3 dB @ 0.01% probability on CCDF, 2% EVM.

Frequency (MHz)	Z <sub>S</sub> (Ω)	Z <sub>L</sub> (Ω)	P <sub>out</sub> (W)	Gain (dB)	Drain Efficiency (%)
3300	5.4 - j10.3	2.9 - j2.5	1.7	10.9	19
3400	5.0 - j10.7	2.9 - j2.6	1.8	11.0	22
3500	4.4 - j11.2	2.8 - j2.7	1.7	10.9	21
3600	4.0 - j12.5	2.8 - j3.3	1.7	10.9	20
3700	3.5 - j13.4	3.0 - j3.8	1.8	10.8	20
3800	3.5 - j14.6	3.2 - j4.2	1.8	10.7	20

#### Impedance Reference



ZS is the source impedance presented to the device.

Z<sub>L</sub> is the load impedance presented to the device.

#### Z<sub>S</sub> and Z<sub>L</sub> vs. Frequency

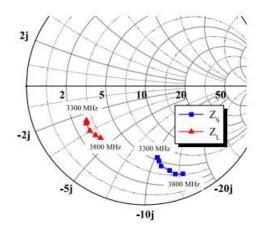


Figure 1 - Optimum Impedance Characteristics for OFDM Tuning, V<sub>DS</sub> = 28 V, I<sub>DO</sub> = 200 mA



# Load-Pull Data, Reference Plane at Device Leads: $V_{DS} = 28 \text{ V}$ , $I_{DQ} = 200 \text{ mA}$ (unless noted)

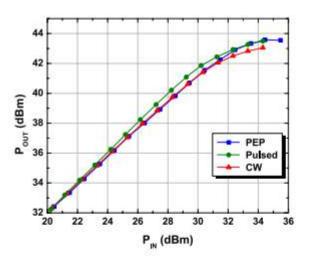


Figure 2 - CW, pulsed CW, and PEP, 3500MHz, Constant Impedance States

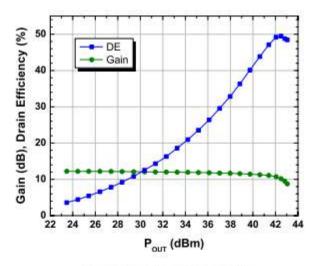


Figure 3 - CW Power Sweep, 3500MHz

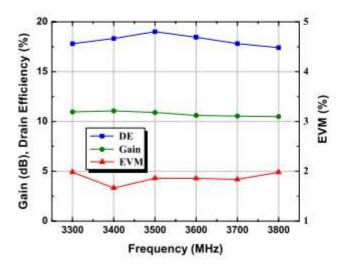


Figure 4 - Typical OFDM Performance Pout = 1.5W

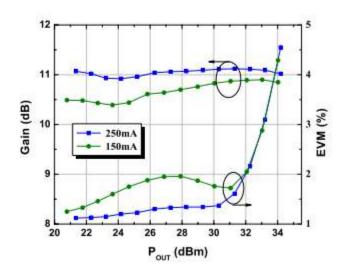


Figure 5 - Typical OFDM Performance at 3500MHz versus I<sub>DQ</sub>



#### Load-Pull Data, Reference Plane at Device Leads:

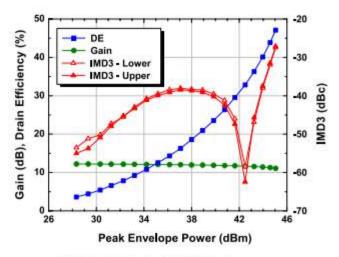


Figure 6 - Typical IMD3 Performance, 3500MHz

# **Typical Device Characteristics**

V<sub>DS</sub>=28V, I<sub>DO</sub>=200mA, T<sub>A</sub>=25°C unless otherwise noted.

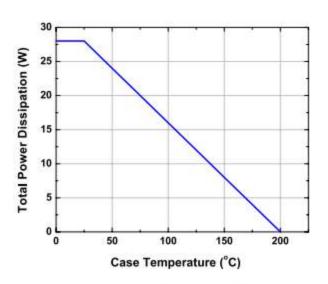


Figure 7 - Power Derating Curve

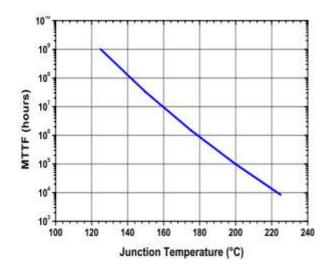
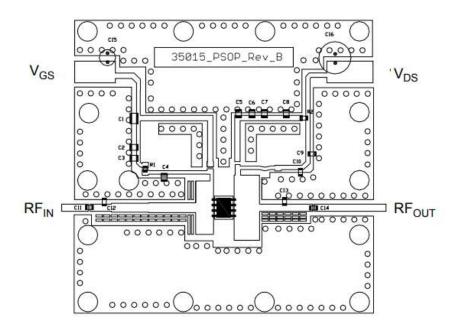


Figure 8 - MTTF of NRF1 Devices



# AD-006 3.4 - 3.6 GHz, 1.7 W Linear WiMAX Application Board & Schematic 802.16e Single Carrier OFDM, 64-QAM 3/4, 8-burst, 20 ms frame 100% filled, 3.5 MHz channel bandwidth, PAR = 10.3 dB @ 0.01% CCDF



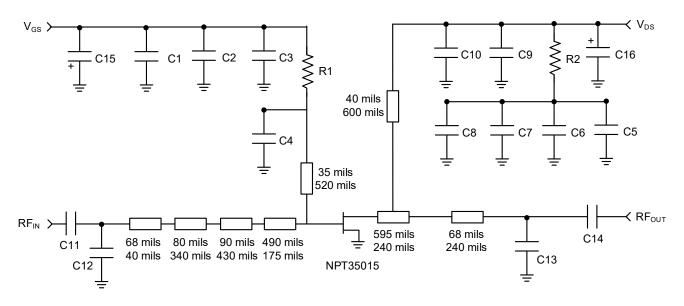


Figure 9 - AD-006 Demonstration Board and Schematic

# GaN Power Transistor, 28 V, 18 W 3.3 - 3.8 GHz



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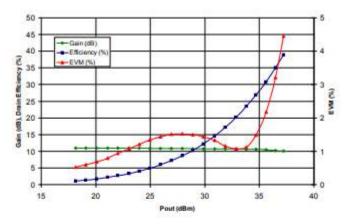
#### **Parts list**

Reference	Value	Tolerance	Manufacturer	Part Number		
C1	0.1 μF	10%	Kemet	C1206C104K1RACTU		
C2, C7	0.01 μF	10%	AVX	12061C103KAT2A		
C3, C6	1000 pF	10%	Kemet	C0805C102K1RACTU		
C5	100 pF	10%	Kemet	C0805C101K1RACTU		
C8	1 μF	10%	Panasonic	ECJ-5YB2A105M		
C4, C9 - C11, C14	5.6 pF	±0.1 pF	ATC	ATC600F5R6B		
C12	0.3 pF	±0.1 pF	ATC	ATC600F0R3B		
C13	0.6 pF	±0.1 pF	ATC	ATC600F0R6B		
C15	150 µF	20%	Nichicon	UPW1C151MED		
C16	270 µF	20%	United Chemi-Con	ELXY630ELL271MK25S		
R1	10 Ω	1%	Panasonic	ERJ-2RKF10R0X		
R2	0.33 Ω	1%	Panasonic	ERJ-6RQFR33V		
PCB	Rogers RO4350, $\varepsilon_r$ =3.5, t = 30 mils					



#### AD-006 3.4 - 3.6 GHz, 1.7 W Linear WiMAX Application Design

802.16e Single Carrier OFDM, 64-QAM 3/4, 8-burst, 20 ms frame 100% filled, 3.5 MHz channel bandwidth, PAR = 10.3 dB @ 0.01% CCDF



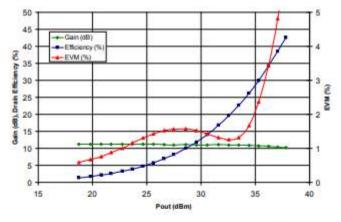


Figure 10 - Gain, Efficiency, EVM at 3400MHz

Figure 11 - Gain, Efficiency, EVM at 3500MHz

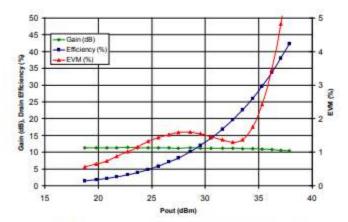


Figure 12 - Gain, Efficiency, EVM at 3600MHz



#### AD-006 3.4 - 3.6 GHz, 1.7 W Linear WiMAX Application Design

802.16e Single Carrier OFDM, 64-QAM 3/4, 8-burst, 20 ms frame 100% filled, 3.5 MHz channel bandwidth, PAR = 10.3 dB @ 0.01% CCDF

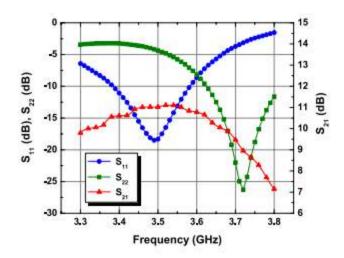


Figure 14 - Typical S<sub>11</sub> and S<sub>21</sub>

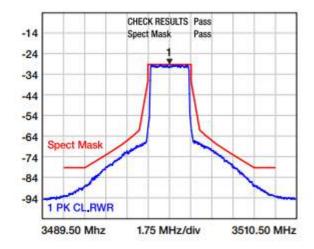
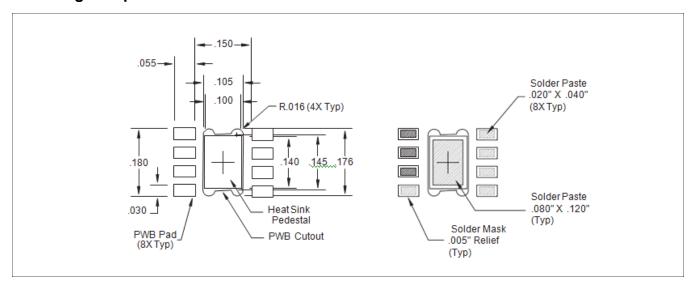


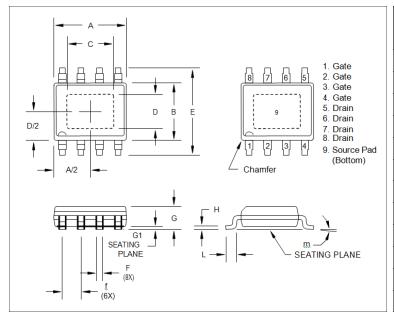
Figure 13 - ETSI Mask Compliance in Nitronex Demonstration Board at 3500MHz and P<sub>OUT</sub> = 1.5W



# **Mounting Footprint**



# Package Dimensions and Pin out<sup>†</sup>



Dim.	Incl	hes	Millimeters		
Dilli.	Min. Max.		Min.	Max.	
Α	0.189	0.196	4.80	4.98	
В	0.150	0.157	3.81	3.99	
С	0.107	0.123	2.72	3.12	
D	0.071 0.870		1.870	2.21	
Е	0.230	0.244	5.85	6.19	
f	0.050	BSC	1.270	70 BSC	
F	0.0138	0.0192	0.35	0.49	
G	0.055	0.061	1.40	1.55	
G1	0.000	0.004	0.00	0.10	
Н	0.075	0.098	1.91	2.50	
L	0.016	0.035	0.41	0.89	
m	0°	8°	0°	8°	

<sup>†</sup> Meets JEDEC moisture sensitivity level 3 requirements. Plating is Matte Sn.

# GaN Power Transistor, 28 V, 18 W 3.3 - 3.8 GHz



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