



80V N-CHANNEL ENHANCEMENT MODE MOSFET

Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2) Halogen and Antimony Free. "Green" Device (Note 3) For automotive applications requiring specific change

please contact us or your local Diodes representative.

https://www.diodes.com/quality/product-definitions/

0.6mm Profile - Ideal for Low Profile Applications

Product Summary

BVDSS	Rds(on) max	I _{D MAX} T _A = +25°C				
001/	25mΩ @ V _{GS} = 10V	7.5A				
80V	$38m\Omega$ @ V _{GS} = 4.5V	6.1A				

Description

This new generation MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

- **Power Management Functions**
- Battery Operated Systems and Solid-State Relays
- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.

U-DFN2020-6 (Type F)

control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities),

Mechanical Data

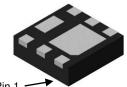
Features and Benefits

PCB Footprint of 4mm² Low On-Resistance

Case: U-DFN2020-6 Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0

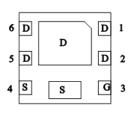
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @4
- Weight: 0.0065 grams (Approximate)



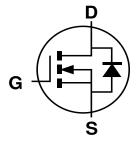


Top View

Bottom View



Pin-Out **Bottom View**



Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Quantity per Reel		
DMT8030LFDF-7	U-DFN2020-6 (Type F)	3,000		
DMT8030LFDF-13	U-DFN2020-6 (Type F)	10,000		

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain < 900ppm bromine, < 900ppm chlorine (< 1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

U-DFN2020-6 (Type F)



83 = Product Type Marking Code YWX = Date Code Marking Y = Year (ex: 0 = 2020)

W = Week (ex: a = week 27; z represents week 52 and 53)

X = Internal Code (ex: U = Monday)

Date Code Key												
Year	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	9	0	1	2	3	4	5	6	7	8	9	0
Week	1-26				27-52 53							
Code		P	۱-Z			а	-Z			Z	7	
Internal Code	Sur	า	Mon		Tue	W	ed	Thu		Fri		Sat
Codo	Т		- 11		W	١	A.I			V		7



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V _{DSS}	80	V	
Gate-Source Voltage	V_{GSS}	±20	V	
Continuous Dusin Comment V 10V (Note C)	T _A = +25°C	I-	7.5	Α
Continuous Drain Current, VGS = 10V (Note 6)	T _A = +70°C	ID	6.1	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	40	Α	
Maximum Body Diode Continuous Current	ls	7.5	Α	
Pulsed Body Diode Current (10µs Pulse, T _C = +25°C, Package Limi	Ism	40	Α	
Avalanche Current, L = 0.3mH	las	12.5	Α	
Avalanche Energy, L = 0.3mH	Eas	23.4	mJ	

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Dawar Dissination (Note 5)	$T_A = +25^{\circ}C$	D-	1.2	W
Total Power Dissipation (Note 5)	T _A = +70°C	PD	0.7] vv
Thermal Resistance, Junction to Ambient (Note 5)	Rеja	103	°C/W	
Total Bayer Dissination (Note 6)	T _A = +25°C		2.2	w
Total Power Dissipation (Note 6)	$T_A = +70^{\circ}C$	PD	1.4] vv
Thermal Resistance, Junction to Ambient (Note 6)	Reja	58	°C/W	
Thermal Resistance, Junction to Case (Note 6)	Rejc	6.7	- C/VV	
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

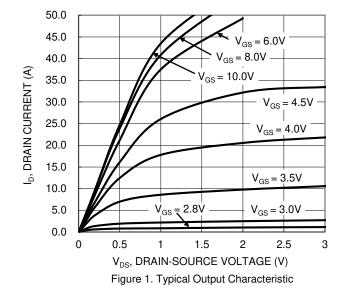
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)	-						
Drain-Source Breakdown Voltage	BVDSS	80	-	_	V	$V_{GS} = 0V$, $I_D = 1mA$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 64V$, $V_{GS} = 0V$	
Gate-Source Leakage	Igss	_	_	±100	nA	$V_{GS} = \pm 20V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	1.2		2.5	٧	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	Descent	_	23.8	25	mΩ	$V_{GS} = 10V$, $I_D = 5A$	
Static Dialii-Source Off-nesistatice	RDS(ON)		33.6	38	11122	$V_{GS} = 4.5V$, $I_D = 4A$	
Diode Forward Voltage	V _{SD}		0.7	1.2	٧	V _{GS} = 0V, I _S = 10A	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	641	_		V 05V V 0V	
Output Capacitance	Coss	_	272	_	pF	$V_{DS} = 25V$, $V_{GS} = 0V$, $f = 1.0MHz$	
Reverse Transfer Capacitance	Crss	_	32	_			
Gate Resistance	Rg	_	1.4	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (VGS = 4.5V)	Qg	_	5.4	_			
Total Gate Charge (V _{GS} = 10V)	Qg	_	10.4	_	nC	V _{DS} = 40V, I _D = 7.5A	
Gate-Source Charge	Qgs	_	1.8	_	110	VDS = 40 V, ID = 7.5A	
Gate-Drain Charge	Qgd	_	2.4	_			
Turn-On Delay Time	t _{D(ON)}	_	11.3	_		V 40V	
Turn-On Rise Time	t _R	_	14.3	_	ns	$V_{DD} = 40V,$	
Turn-Off Delay Time	tD(OFF)	_	10.8	_	115	$V_{GS} = 4.5V$, $R_g = 2.7\Omega$, $I_D = 10A$	
Turn-Off Fall Time	tr	_	8.3	_		10A	
Body Diode Reverse Recovery Time	t _{RR}	_	25.5	_	ns	$I_F = 7.5A$, $di/dt = 100A/\mu s$	
Body Diode Reverse Recovery Charge	Qrr	_	20.6	_	nC	I _F = 7.5A, di/dt = 100A/μs	

Notes:

- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 Short duration pulse test used to minimize self-heating effect.

- 8. Guaranteed by design. Not subject to product testing.





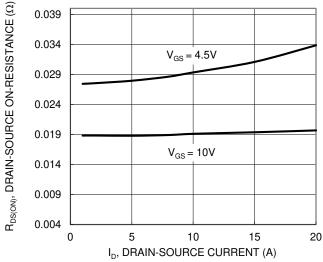


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

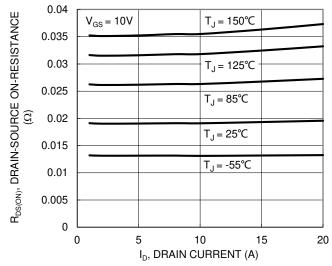


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

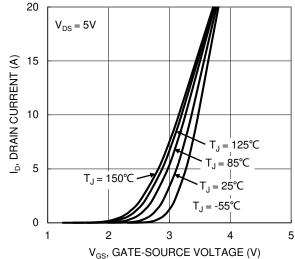


Figure 2. Typical Transfer Characteristic

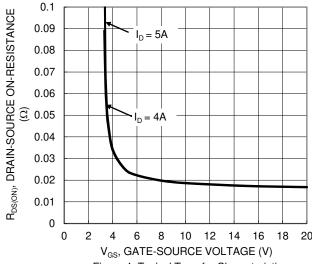


Figure 4. Typical Transfer Characteristic

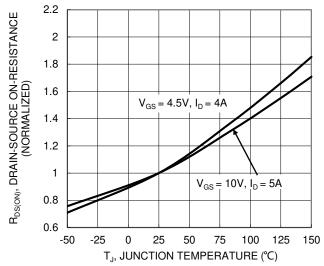


Figure 6. On-Resistance Variation with Junction Temperature





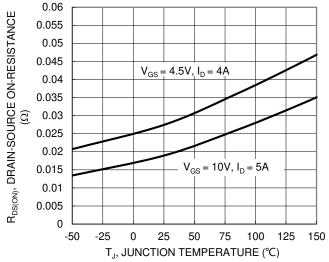


Figure 7. On-Resistance Variation with Junction Temperature

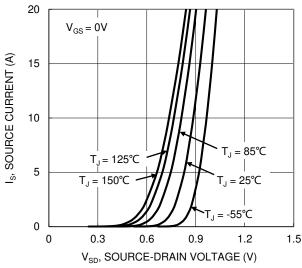


Figure 9. Diode Forward Voltage vs. Current

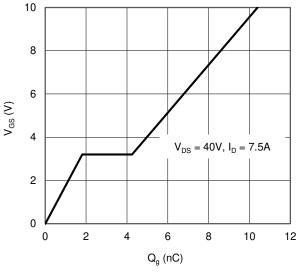


Figure 11. Gate Charge

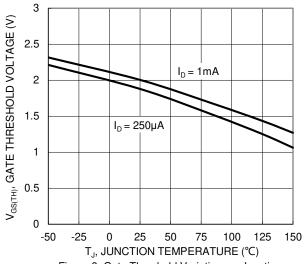


Figure 8. Gate Threshold Variation vs. Junction Temperature

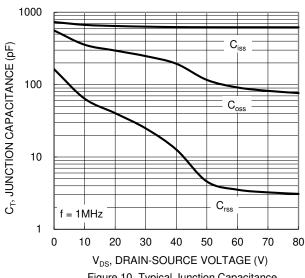


Figure 10. Typical Junction Capacitance

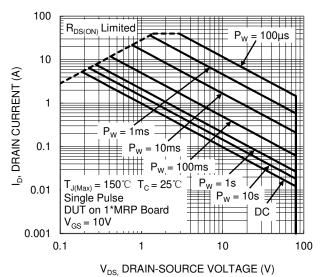


Figure 12. SOA, Safe Operation Area



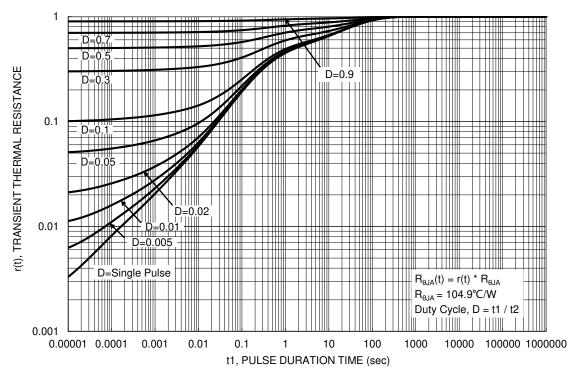


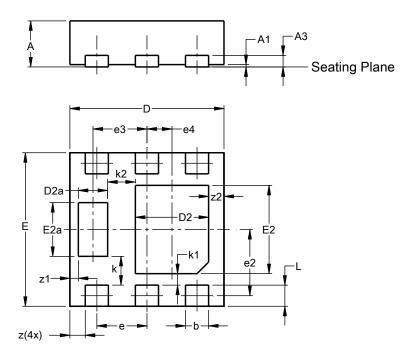
Figure 13. Transient Thermal Resistance



Package Outline Dimension

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)

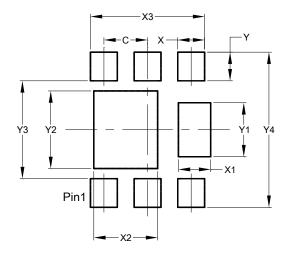


U-DFN2020-6								
(Type F)								
Dim	Min	- 7F						
Α	0.57 0.63 0.60							
A 1	0.00	0.00 0.05 0.03						
A3	-	-	0.15					
b	0.25	0.35	0.30					
D	1.95	2.05	2.00					
D2	0.85	1.05	0.95					
D2a	0.33	0.43	0.38					
Е	1.95	2.05	2.00					
E2	1.05	1.25	1.15					
E2a	0.65 0.75 0.70							
е	0.65 BSC							
e2).863 BS						
e3		0.70 BS	_					
e4	().325 BS	SC					
k	0.37 BSC							
k1	0.15 BSC							
k2	0.36 BSC							
L	0.225 0.325 0.275							
Z	0.20 BSC							
z 1	0.110 BSC							
z2	0.20 BSC							
All C	All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)



Dimensions	Value (in mm)			
С	0.650			
Х	0.400			
X1	0.480			
X2	0.950			
Х3	1.700			
Υ	0.425			
Y1	0.800			
Y2	1.150			
Y3	1.450			
Y4	2.300			



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