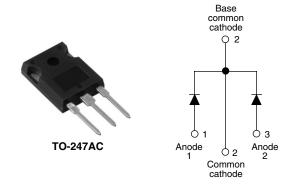


Vishay High Power Products

Schottky Rectifier, 2 x 20 A



PRODUCT SUMMARY				
I _{F(AV)}	2 x 20 A			
V _R	15 V			
I _{RM}	600 mA at 100 °C			

FEATURES

- 125 °C T_J operation (V_R < 5 V)
- · Center tap module
- · Optimized for OR-ing applications
- Ultra low forward voltage drop
- · High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- · Designed and qualified for industrial level

DESCRIPTION

The MBR40L15CW center tap Schottky rectifier module has been optimized for ultra low forward voltage drop specifically for the OR-ing of parallel power supplies. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES			
I _{F(AV)}	Rectangular waveform	40	Α		
V _{RRM}		15	V		
I _{FSM}	$t_p = 5 \mu s sine$	700	А		
V _F	20 Apk, T _J = 125 °C (per leg, typical)	0.26	V		
T _J	Range	- 55 to 125	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	MBR40L15CW	UNITS
Maximum DC reverse voltage	V_{R}	T _{.1} = 100 °C	15	V
Maximum working peak reverse voltage	V_{RWM}	1j = 100 C	15	V

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average per leg		I	F0 % duty ovelo et T = 96 °C rectongular wayofarm		20	
	r device	I _{F(AV)}	50 % duty cycle, at T _C = 86 °C, rectangular waveform		40	۸
Maximum peak one cycle non-repetitive surge current per leg See fig. 7		I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	700	A
			10 ms sine or 6 ms rect. pulse		330	
Non-repetitive avalanche energy per leg		E _{AS}	T _J = 25 °C, I _{AS} = 2 A, L = 6 mH		5	mJ
Repetitive avalanche current per leg I _{AR}		Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		2	Α	

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MBR40L15CW

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
	V _{FM} ⁽¹⁾	20 A	T _J = 25 °C	-	0.42	V
Maximum forward voltage drop per leg		40 A		-	0.52	
See fig. 1		20 A	T _J = 125 °C	0.26	0.34	
		40 A		0.37	0.50	
Reverse leakage current per leg	. (1)	T _J = 25 °C	V _R = Rated V _R	-	10	m 1
See fig. 2	I _{RM} ⁽¹⁾	T _J = 100 °C		-	600	mA
Threshold voltage	V _{F(TO)}	T _J = T _J maximum		0.1	182	V
Forward slope resistance	r _t			7.6		mΩ
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} , (test signal range 100 kHz to 1 MHz) 25 °C		-	2000	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body 8 -		nH		
Maximum voltage rate of change	dV/dt	Rated V _R 10 000		V/µs		

Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction temperature	range	T_J		- 55 to 125	°C
Maximum storage temperature	range	T _{Stg}		- 55 to 150	C
Maximum thermal resistance, junction to case per leg		ם	DC operation See fig. 4	1.4	
Maximum thermal resistance, junction to case per package		R_{thJC}	DC operation	0.7	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.24	
Approximate weight				6	g
Approximate weight				0.21	OZ.
Manuatina taunus	minimum		Non-lubricated threads	6 (5)	kgf ⋅ cm
Mounting torque r	naximum		Non-iubricateu tirreaus	12 (10)	(lbf \cdot in)
Marking device			Case style TO-247AC (JEDEC)	MBR40	L15CW



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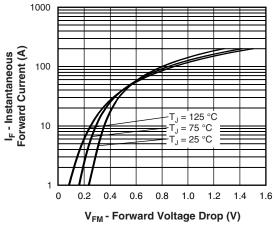


Fig. 1 - Maximum Forward Voltage Drop Characteristics

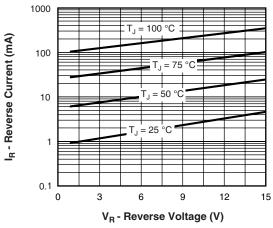


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

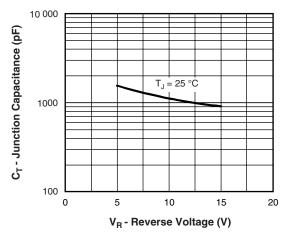


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

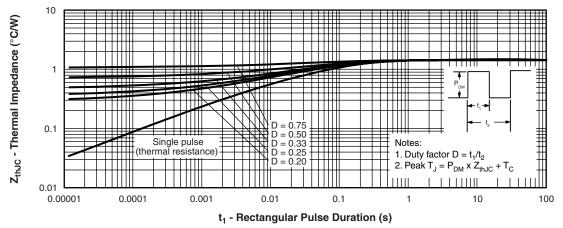


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

Vishay High Power Products Schottky Rectifier, 2 x 20 A



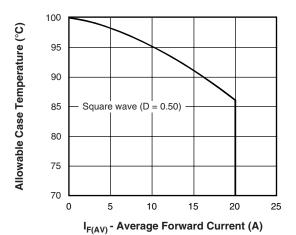


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

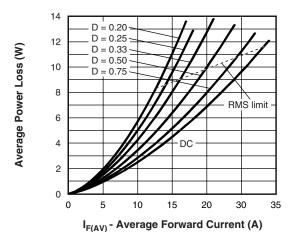


Fig. 6 - Forward Power Loss Characteristics

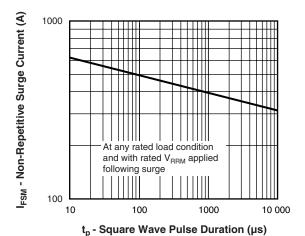


Fig. 7 - Maximum Non-Repetitive Surge Current

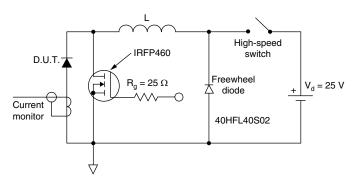
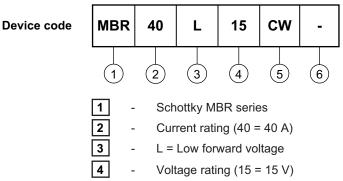


Fig. 8 - Unclamped Inductive Test Circuit



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ORDERING INFORMATION TABLE



5 - Circuit configuration: Center tap TO-247

None = Standard production

• PbF = Lead (Pb)-free

Tube standard pack quantity: 25 pieces

LINKS TO RELATED DOCUMENTS					
Dimensions http://www.vishay.com/doc?95223					
Part marking information	http://www.vishay.com/doc?95226				

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Vishay

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