TOSHIBA Field Effect Transistor Silicon N Channel MOS Type

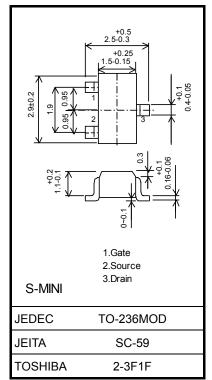
SSM3K15F

High Speed Switching Applications Analog Switch Applications

- Small package
- Low on resistance
 - : $R_{on} = 4.0 \Omega \text{ (max)} (@V_{GS} = 4 \text{ V})$
 - : Ron = 7.0 Ω (max) (@VGS = 2.5 V)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V _{DS}	30	V	
Gate-source voltage		V _{GSS}	±20	V	
Drain current	DC	۱ _D	100	mA	
	Pulse	I _{DP}	200	ШA	
Drain power dissipation (Ta = 25° C)		PD	200	mW	
Channel temperature		T _{ch}	150	°C	
Storage temperature		T _{stg}	–55 to 150	°C	



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the

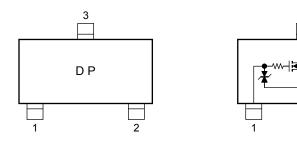
Weight: 0.012 g (typ.)

reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Marking

Equivalent Circuit



Handling Precaution

When handling individual devices (which are not yet mounting on a circuit board), be sure that the environment is protected against electrostatic electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

Start of commercial production 2001-02

Unit: mm

90%

toff

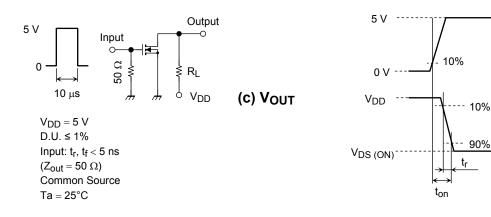
Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS}=\pm 16~V,~V_{DS}=0$	_	_	±1	μA
Drain-source breakdown voltage		V _(BR) DSS	$I_D = 0.1 \text{ mA}, V_{GS} = 0$	30	_	_	V
Drain cut-off curre	ent	I _{DSS}	$V_{DS} = 30 V, V_{GS} = 0$	_	_	1	μA
Gate threshold vo	ltage	V _{th}	$V_{DS} = 3 \text{ V}, \text{ I}_{D} = 0.1 \text{ mA}$	0.8	_	1.5	V
Forward transfer	admittance	Y _{fs}	$V_{DS} = 3 \text{ V}, \text{ I}_{D} = 10 \text{ mA}$	25	_		mS
Drain-source ON resistance		R _{DS (ON)}	$I_D = 10 \text{ mA}, V_{GS} = 4 \text{ V}$		2.2	4.0	Ω
			$I_D = 10 \text{ mA}, V_{GS} = 2.5 \text{ V}$	_	4.0	7.0	
Input capacitance		C _{iss}	$V_{DS} = 3 V, V_{GS} = 0, f = 1 MHz$	_	7.8	_	pF
Reverse transfer capacitance		C _{rss}	$V_{DS} = 3 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	3.6		pF
Output capacitance		C _{oss}	$V_{DS} = 3 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	8.8		pF
Switching time	Turn-on time	t _{on}	$V_{DD} = 5 \text{ V}, \text{ I}_D = 10 \text{ mA},$ $V_{GS} = 0 \text{ to } 5 \text{ V}$	_	50		ns
	Turn-off time	t _{off}		_	180		

Switching Time Test Circuit

(a) Test circuit

(b) V_{IN}



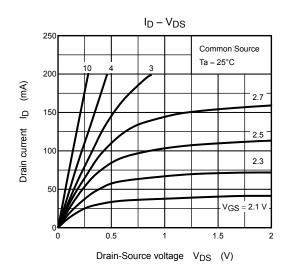
Precaution

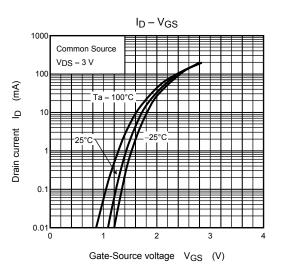
 V_{th} can be expressed as voltage between gate and source when low operating current value is I_D = 100 μA for this product. For normal switching operation, V_{GS} (on) requires higher voltage than V_{th} and V_{GS} (off) requires lower voltage than V_{th} .

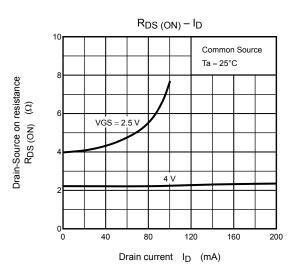
(relationship can be established as follows: V_{GS} (off) $< V_{th} < V_{GS}$ (on))

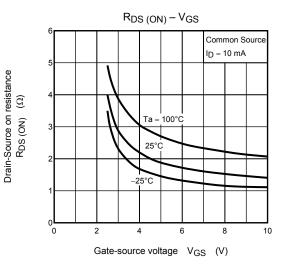
Please take this into consideration for using the device.

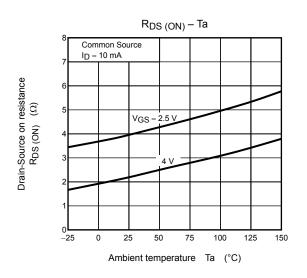
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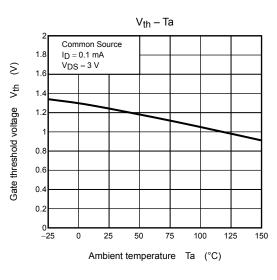




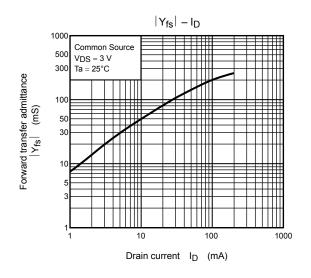


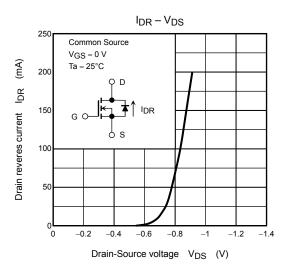


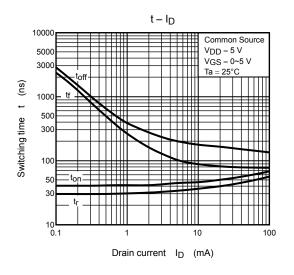


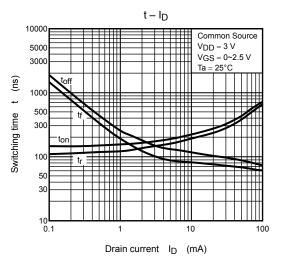


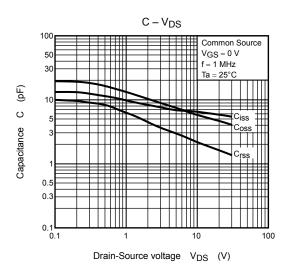
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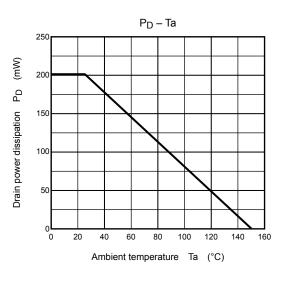












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