# RQ7G080AT

Pch -40V -8A Small Signal Power MOSFET

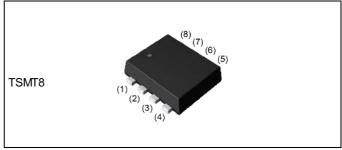
Datasheet

V <sub>DSS</sub>	-40V
R <sub>DS(on)</sub> (Max.)	18.2mΩ
I <sub>D</sub>	±8A
P <sub>D</sub>	1.5W

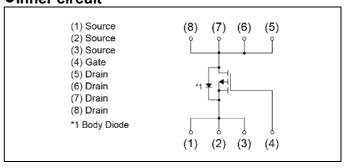
## Features

- 1) Low on resistance
- 2) Small Surface Mount Package (TSMT8)
- 3) Pb-free plating; RoHS compliant

# Outline



# ●Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	180
Туре	Tape width (mm)	8
-	Quantity (pcs)	3000
	Taping code	TCR
	Marking	ВХ

# Application

Switching

# ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified)

	·		
Parameter	Symbol	Value	Unit
Drain - Source voltage	V <sub>DSS</sub>	-40	V
Continuous drain current	I <sub>D</sub>	±8	Α
Pulsed drain current	I <sub>DP</sub> *1	±32	Α
Gate - Source voltage	$V_{GSS}$	±20	V
Avalanche current, single pulse	I <sub>AS</sub> *2	-8	Α
Avalanche energy, single pulse	E <sub>AS</sub> *2	4.6	mJ
Douge dissinction	P <sub>D</sub> *3	1.5	W
Power dissipation	P <sub>D</sub> *4	1.1	W
Junction temperature	T <sub>j</sub>	150	°C
Operating junction and storage temperature range	T <sub>stg</sub>	-55 to +150	°C

## ●Thermal resistance

Dorameter	Symbol	Values			l leit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal registeres innetion, embient	R <sub>thJA</sub> *3	-	-	83.3	°C/W
Thermal resistance, junction - ambient	R <sub>thJA</sub> *4	-	-	113	°C/W

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Davamatav	Symbol Conditions -		Values			Lleit
Parameter			Min.	Тур.	Max.	Unit
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V$ , $I_D = -1mA$	-40	-	-	V
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I <sub>D</sub> = -1mA referenced to 25°C	-	-22	-	mV/°C
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = -40V, V <sub>GS</sub> = 0V	-	-	-1	μA
Gate - Source leakage current	I <sub>GSS</sub>	$V_{GS} = \pm 20V$ , $V_{DS} = 0V$	-	-	±100	nA
Gate threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = -1mA$	-1.0	-	-2.5	V
Gate threshold voltage temperature coefficient	$\frac{\DeltaV_{GS(th)}}{\DeltaT_j}$	I <sub>D</sub> = -1mA referenced to 25°C	-	3.7	-	mV/°C
Static drain - source	D *5	$V_{GS} = -10V, I_D = -8A$	-	14.7	18.2	mO.
on - state resistance	R <sub>DS(on)</sub> *5	$V_{GS} = -4.5V, I_D = -8A$	-	18.1	22.6	mΩ
Gate resistance	R <sub>G</sub>	f = 1MHz, open drain	-	10	-	Ω
Forward Transfer Admittance	Y <sub>fs</sub>  *5	V <sub>DS</sub> = -5V, I <sub>D</sub> = -8A	11	-	-	S

<sup>\*1</sup> Pw ≤ 10µs, Duty cycle ≤ 1%

<sup>\*2</sup> L  $\simeq$  0.1mH, V<sub>DD</sub> = -20V, R<sub>G</sub> = 25 $\Omega$ , Starting T<sub>j</sub> = 25 $^{\circ}$ C Fig.3-1,3-2

<sup>\*3</sup> Mounted on a ceramic board (30×30×0.8mm)

<sup>\*4</sup> Mounted on a FR4 (25×25×0.8mm)

<sup>\*5</sup> Pulsed

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Darameter	Cumbal	Conditions	Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	2060	-		
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = -20V	-	280	-	pF	
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	185	-		
Turn - on delay time	t <sub>d(on)</sub> *5	V <sub>DD</sub> ≃ -20V,V <sub>GS</sub> = -10V	-	12	-		
Rise time	<b>t</b> <sub>r</sub> *5	I <sub>D</sub> = -4.0A	-	42	-		
Turn - off delay time	t <sub>d(off)</sub> *5	$R_L \simeq 5.0\Omega$	-	145	-	ns	
Fall time	<b>t</b> <sub>f</sub> *5	$R_G = 10\Omega$	-	105	-		

# • Gate charge characteristics $(T_a = 25^{\circ}C)$

Daramatar	Cymah al	Conditions		Values			l limit	
Parameter	Symbol			Min.	Тур.	Max.	Unit	
Total gate aborge	O *5		V <sub>GS</sub> = -10V	-	37.0	-		
Total gate charge	$Q_g^{*5}$	V <sub>DD</sub> ≃ -20V		-	18.0	-		
Gate - Source charge	Q <sub>gs</sub> *5	I <sub>D</sub> = -8A	V <sub>GS</sub> = -4.5V	-	5.9	-	nC	
Gate - Drain charge	Q <sub>gd</sub> *5				-	6.6	-	

# ● Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Darameter	Cumbal	Conditions		Values		
Parameter	Symbol	Symbol Conditions		Тур.	Max.	Unit
Continuous forward current	I <sub>S</sub>	T = 25°C	-	-	-1.25	Α
Pulse forward current	I <sub>SP</sub> *1	T <sub>a</sub> = 25°C	-	-	-32	Α
Forward voltage	V <sub>SD</sub> *5	$V_{GS} = 0V, I_S = -1.25A$	-	-	-1.2	V
Reverse recovery time	t <sub>rr</sub> *5	I <sub>S</sub> = -8A, V <sub>GS</sub> =0V	-	27	-	ns
Reverse recovery charge	Q <sub>rr</sub> *5	di/dt = 100A/μs	-	27	-	nC

Fig.1 Power Dissipation Derating Curve

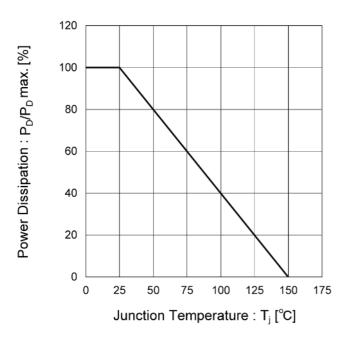
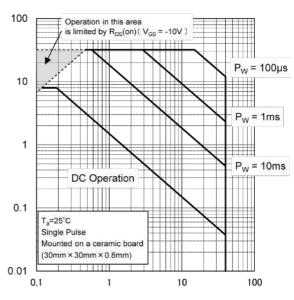


Fig.2 Maximum Safe Operating Area



Drain Current : -l<sub>D</sub> [A]

Drain - Source Voltage : -VDS [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

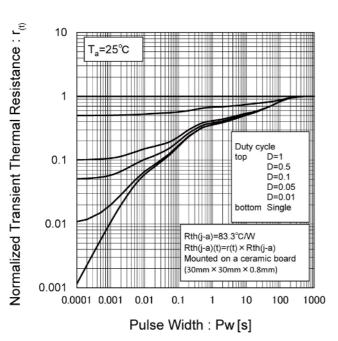
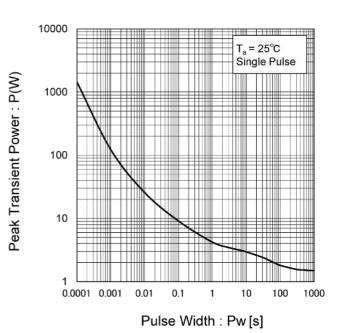


Fig.4 Single Pulse Maximum Power Dissipation



Drain Current: -I<sub>D</sub> [A]

## • Electrical characteristic curves

Fig.5 Typical Output Characteristics(I)

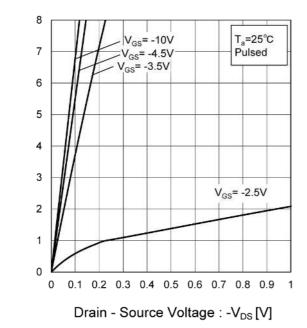
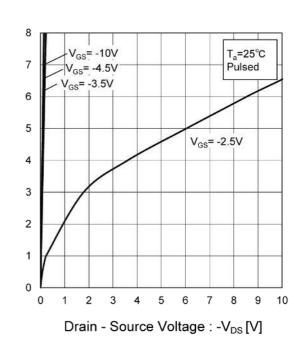


Fig.6 Typical Output Characteristics(II)



Drain Current : -I<sub>D</sub> [A]

Fig.7 Breakdown Voltage vs.
Junction Temperature

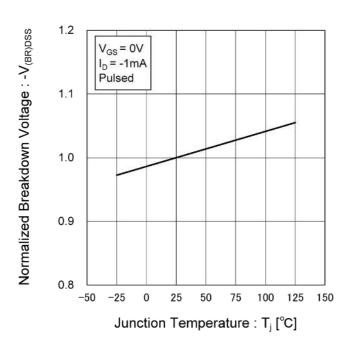


Fig.8 Typical Transfer Characteristics

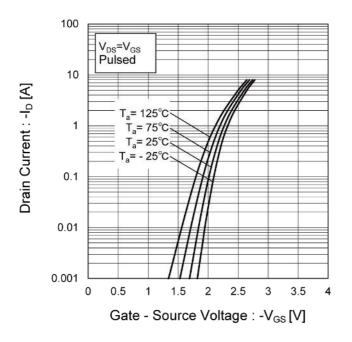
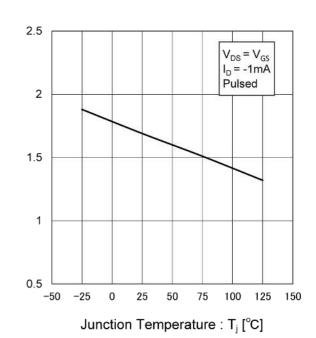


Fig.9 Gate Threshold Voltage vs.
Junction Temperature



Gate Threshold Voltage: -VGS(th) [V]

Fig.10 Forward Transfer Admittance vs.
Drain Current

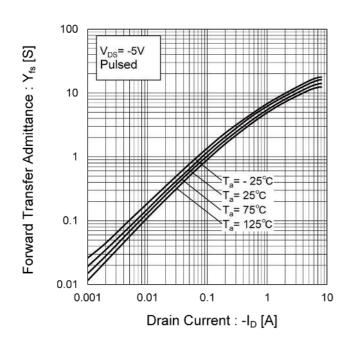


Fig.11 Drain Current Derating Curve

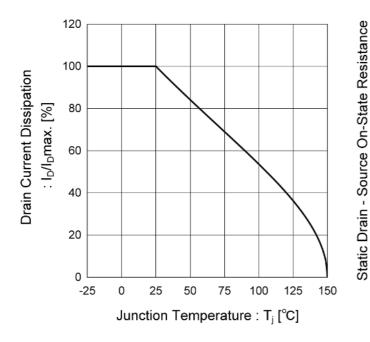


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

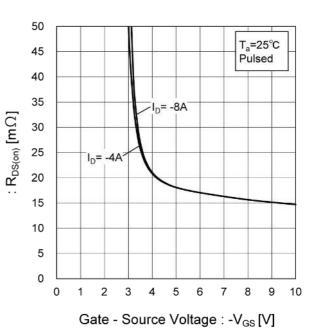


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

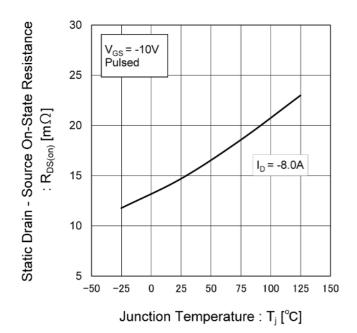


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

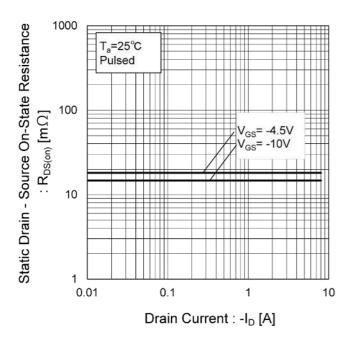
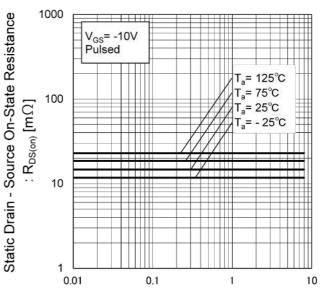


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)



Drain Current: -ID [A]

Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

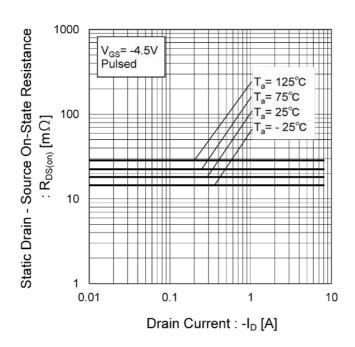


Fig.17 Typical Capacitances vs.

Drain - Source Voltage

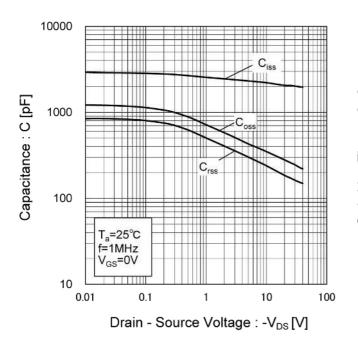


Fig.18 Switching Characteristics

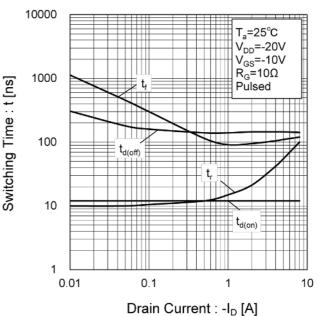


Fig.19 Typical Gate Charge

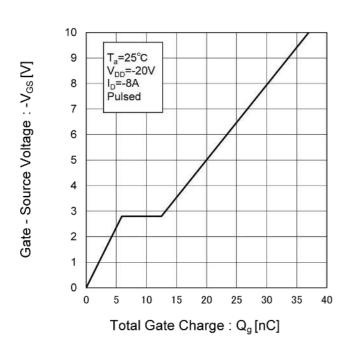
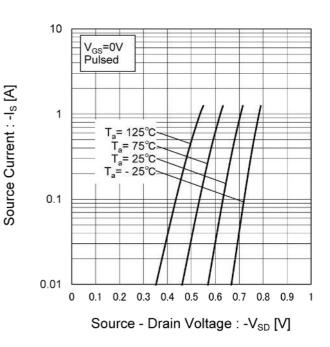


Fig.20 Source Current vs.

Source Drain Voltage



## Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

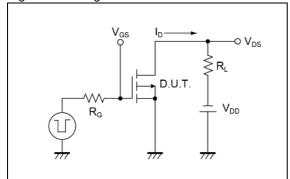


Fig.1-2 Switching Waveforms

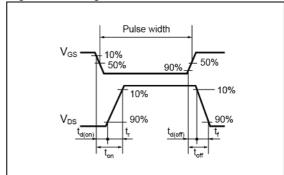


Fig.2-1 Gate Charge Measurement Circuit

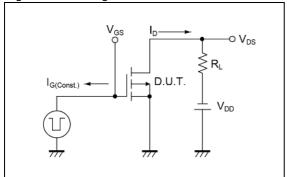


Fig.2-2 Gate Charge Waveform

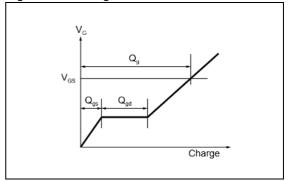


Fig.3-1 Avalanche Measurement Circuit

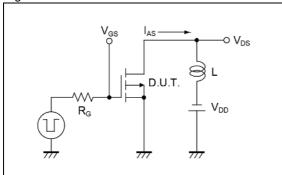
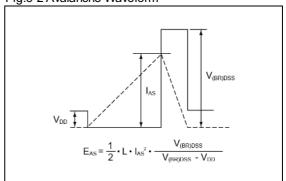


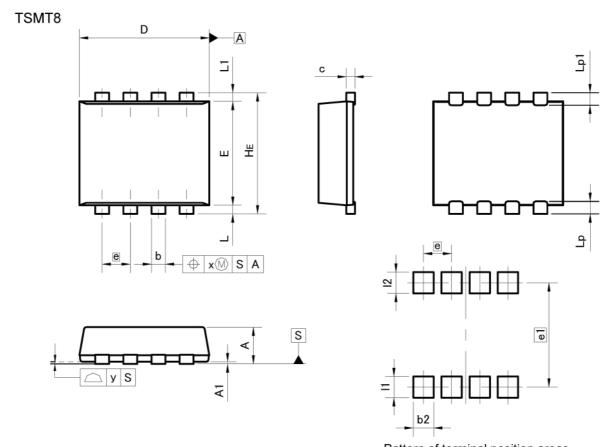
Fig.3-2 Avalanche Waveform



#### Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

## Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES
DIM [	MIN	MAX	MIN	MAX
Α	0.75	0.85	0.030	0.033
A1	0.00	0.05	0.000	0.002
b	0.27	0.37	0.011	0.015
С	0.12	0.22	0.005	0.009
D	2.90	3.10	0.114	0.122
E	2.30	2.50	0.091	0.098
е	0.	65	0.0	26
HE	2.70	2.90	0.106	0.114
L	0.10	0.30	0.004	0.012
L1	0.10	0.30	0.004	0.012
Lp	0.19	0.39	0.007	0.015
Lp1	0.19	0.39	0.007	0.015
х	855	0.10		0.004
у	8 <del></del>	0.10	-	0.004

DIM	MILIM	MILIMETERS		HES
DIM	MIN	MAX	MIN	MAX
b2	100	0.47	-	0.019
e1	2.	2.41		95
11	33 <del>00</del>	0.49	-	0.019
12	-	0.49	9 <del></del> 3	0.019

Dimension in mm/inches



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CLASSIV	CLASSII	CLASSⅢ	CLASSⅢ

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  - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
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  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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